ECM-PNV (for Rev. C1 – Onboard 18bit LVDS)

Intel® Atom™ D525 Dual-Core 3.5" Micro Module with Intel® ICH8-M Chipset

Quick Installation Guide

2nd Ed – 22 April 2014

Part No. E2017383002R

FCC Statement

THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

(1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.

(2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRED OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

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Content

1. Getting Started

1.1 Safety Precautions

Warning!

Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

Caution!

Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

Always note that improper disassembling action could cause damage to the motherboard. We suggest not removing the heatsink without correct instructions in any circumstance. If you really have to do this, please contact us for further support.

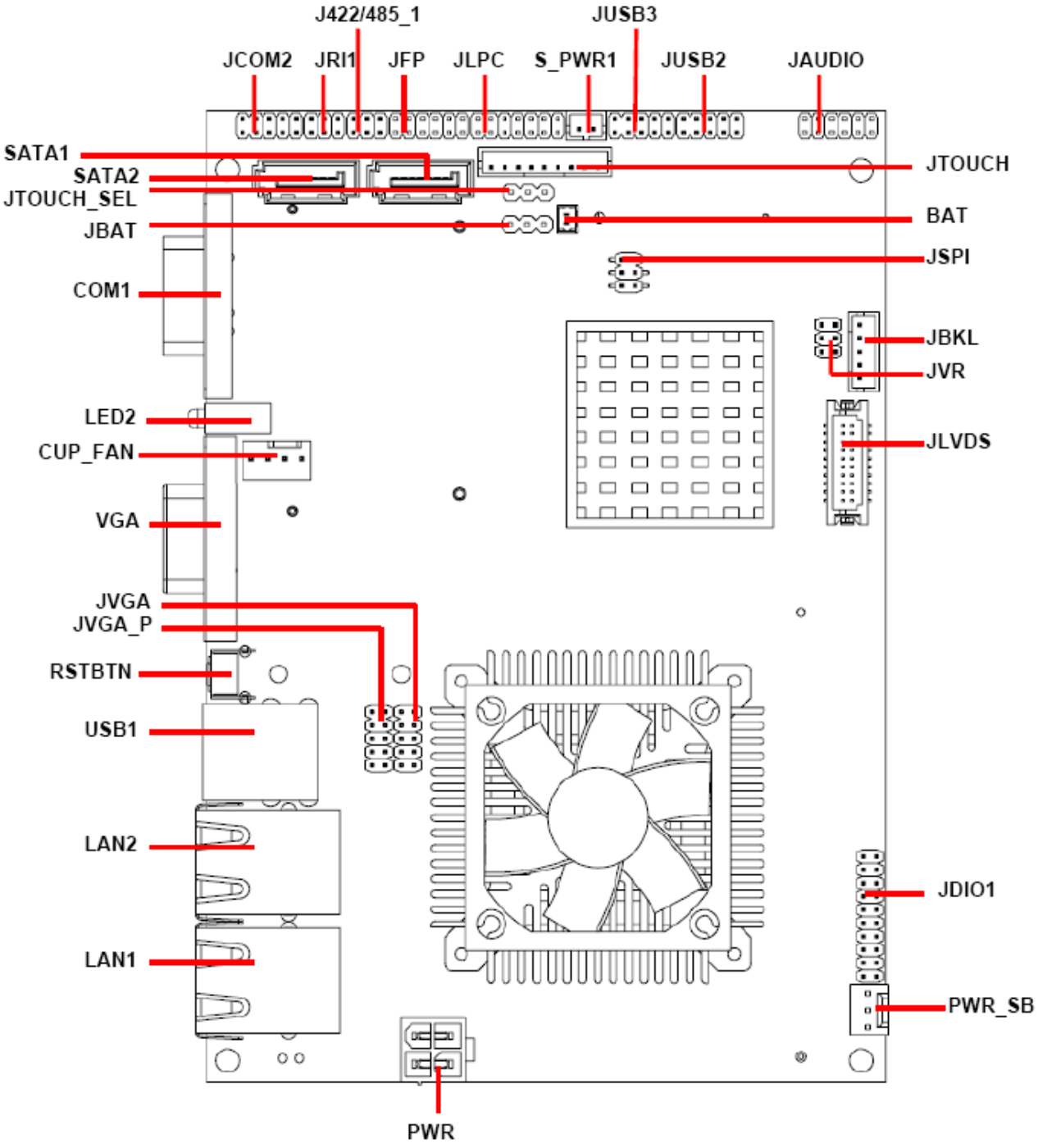
1.1 Packing List

Before you begin installing your single board, please make sure that the following materials have been shipped:

- 1 x 3.5” ECM-PNV Micro Module
- 1 x Quick Installation Guide for ECM-PNV
- 1 x AUX-032 daughter board
- 1 x DVD-ROM contains the followings:
  - User’s Manual (this manual in PDF file)
  - Ethernet driver and utilities
  - VGA drivers and utilities
  - Audio drivers and utilities
- 1 x Cable set contains the followings:
  - 1 x Audio cable (12pin, 2.0mm pitch)
  - 2 x USB cable (10P/2.54mm-10P/2.0mm)
  - 1 x Serial ATA cable (7-pin, standard)
  - 1 x Serial ATA cable (15-pin, 2P/2.0mm)
- 1 x CPU & North Bridge Cooler
- Onboard 18bit LVDS converter Board

2. Hardware Configuration

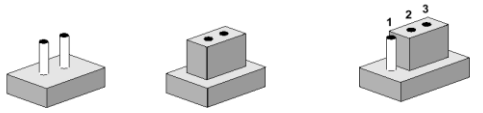
2.1 Product Overview



2.2 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



Open Closed Closed 2-3

The jumper settings are schematically depicted in this manual as follows:

Open Closed Closed 2-3

A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board’s jumpers and connectors.

Jumpers

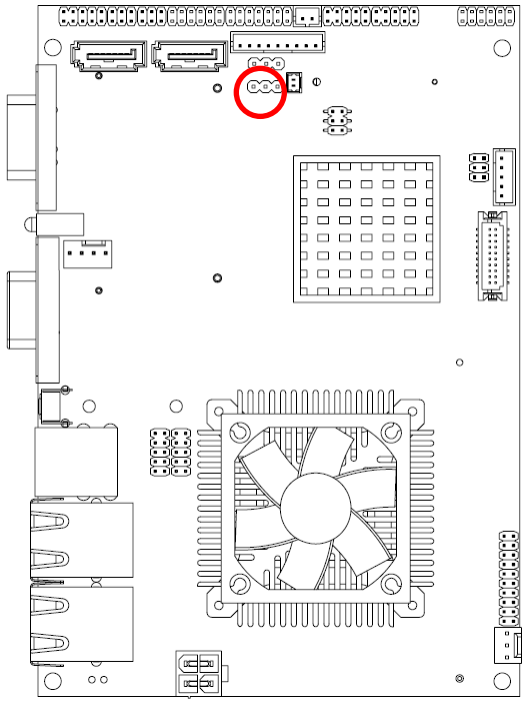
Label	Function	Note
JBAT	Clear CMOS	3 x 1 header, pitch 2.54 mm
JFP	Miscellaneous setting connector	6 x 2 header, pitch 2.0 mm
JRI1	Serial port 1 pin 9 signal select	3 x 2 header, pitch 2.0 mm
JTOUCH_SEL	Touch panel mode select	3 x 1 header, pitch 2.54 mm
JVR	LCD backlight brightness adjustment	3 x 2 header, pitch 2.0mm

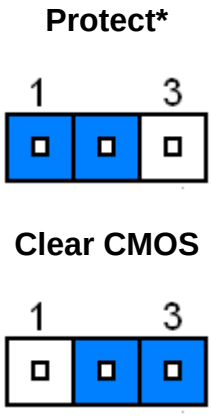
Connectors

Label	Function	Note
BAT	Battery connector	2 x 1 wafer, pitch 1.25 mm
COM1	Serial port 1 connector	D-sub 9-pin, male
CPU_FAN	CPU fan connector	3 x 1 wafer, pitch 2.54 mm
J422/485_1	Serial port 2 in RS-422/485 mode	3 x 2 header, pitch 2.0 mm
JTOUCH	Touch panel connector	9 x 1 header, pitch 2.0 mm
JAUDIO	Audio connector	6 x 2 header, pitch 2.0 mm
JCOM2	Serial port 2 connector	5 x 2 header, pitch 2.0 mm
JDIO	General purpose I/O connector	10 x 2 header, pitch 2.0 mm
JLPC	(Reserved for debug)	7 x 2 header, pitch 2.0 mm
JSPI	SPI connector	3 x 2 header, pitch 2.0 mm
JUSB2	USB connector	5 x 2 header, pitch 2.0 mm
JUSB3	USB connector	5 x 2 header, pitch 2.0 mm
JLVDS1	LVDS connector	2 x 10 header, pitch 1.25mm
JBKL	LCD inverter connector	5 x 1 wafer, pitch 2.0mm
LAN1	RJ-45 Ethernet connector	
LAN2	RJ-45 Ethernet connector	
LED2	LED connector	
PWR	Power connector	2 x 2 wafer, pitch 4.2 mm
PWR_SB	5VSB connector in ATX	3 x 1 wafer, pitch 2.54 mm
S_PWR1	SATA power connector	2 x 1 wafer, pitch 2.0 mm
SATA1	Serial ATA connector 1	
SATA2	Serial ATA connector 2	
RSTBTN	Reset button	
USB1	USB connector	Double Deck
VGA	VGA connector	D-sub 15-pin, female

2.3 Setting Jumpers & Connectors

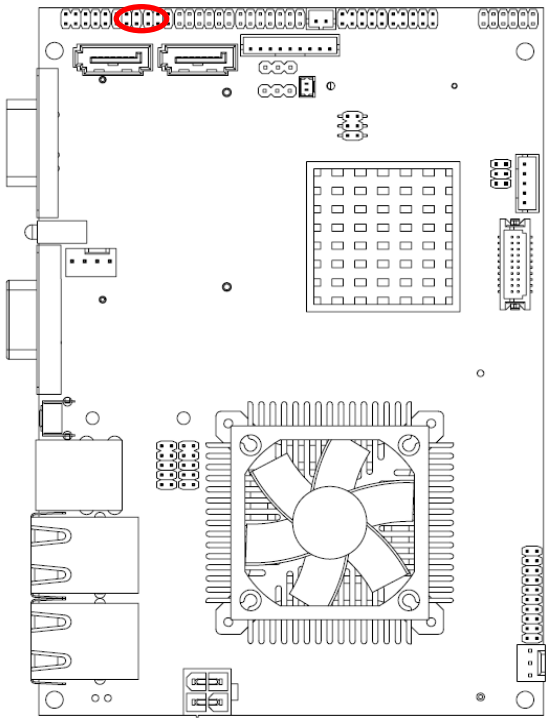
2.3.1 Clear CMOS (JBAT)

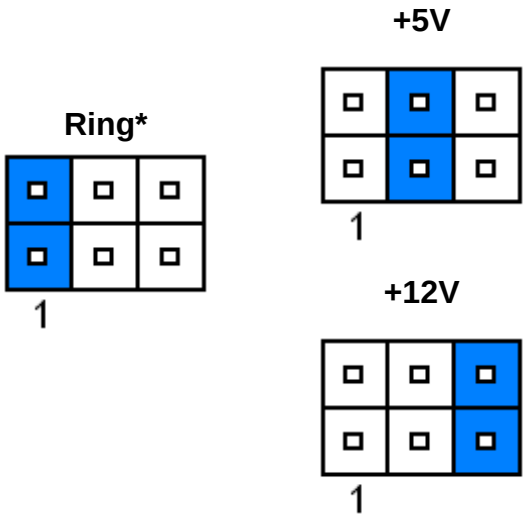




*** Default**

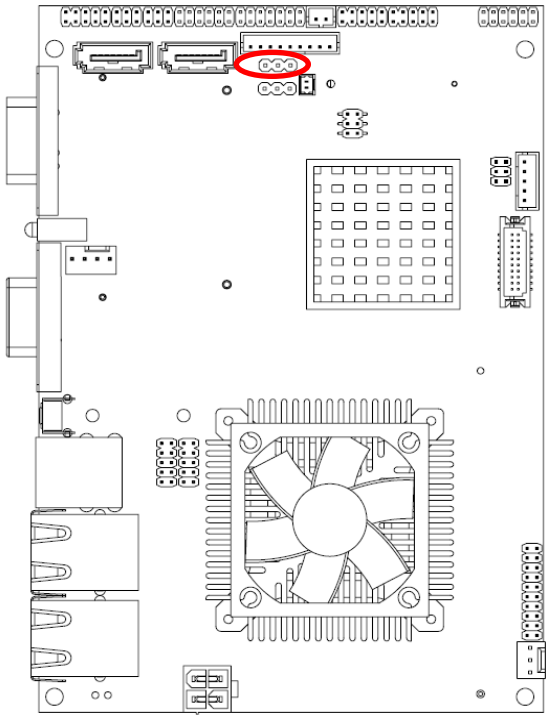
2.3.2 Serial port 1 pin 9 signal select (JRI1)

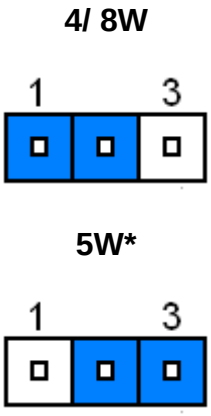




*** Default**

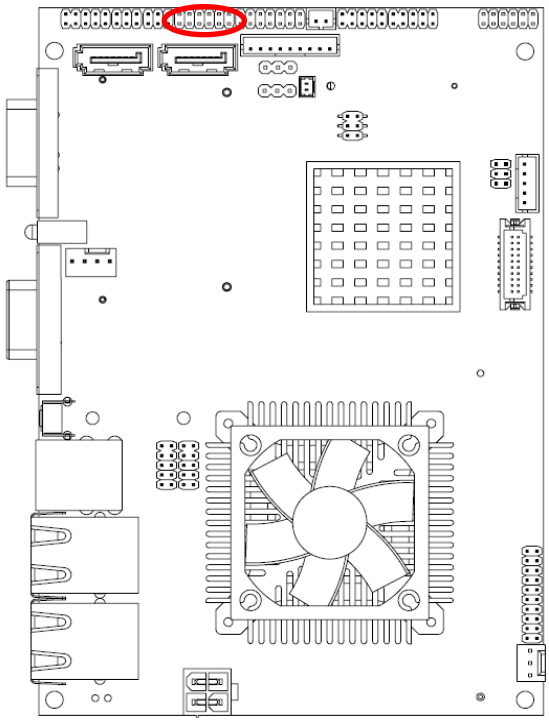
2.3.3 Touch panel mode select (JTOUCH_SEL)

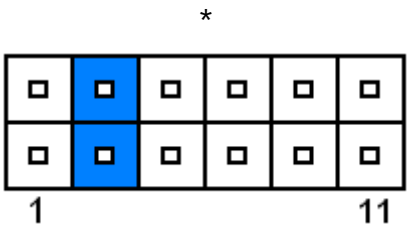




*** Default**

2.3.4 Miscellaneous setting connector (JFP)

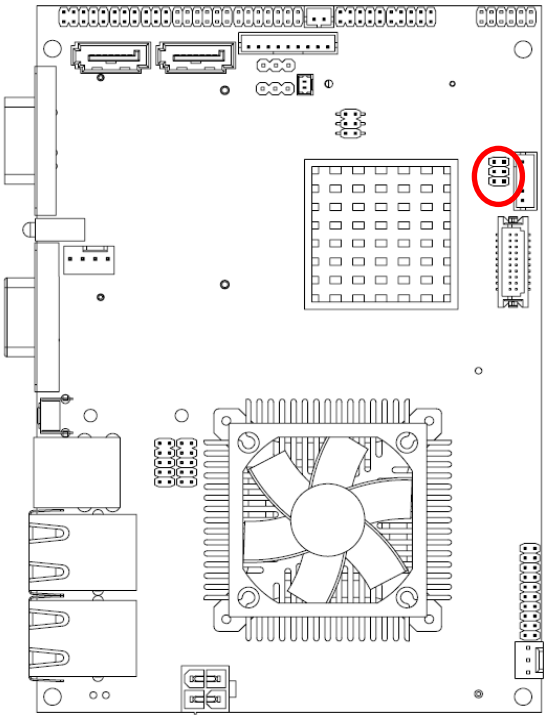


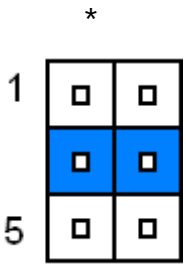


Signal	PIN	PIN	Signal
PWRBTN#	1	2	GND
PWRBTN#	3	4	AUTO_PWR_ON
VCC	5	6	GND
HD_ACT#	7	8	VCC3
VCC	9	10	GND
COPEN#	11	12	GND

*** Default**

2.3.5 LCD backlight brightness adjustment (JVR)

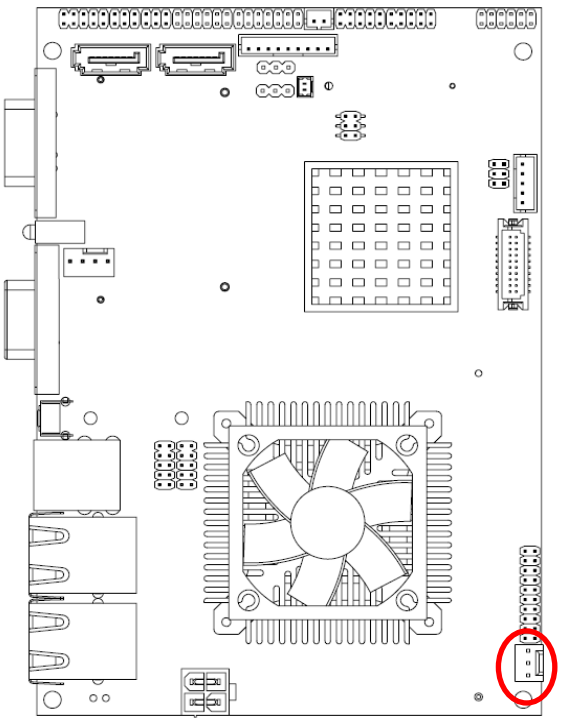


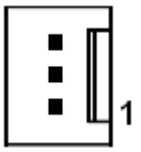


Signal	PIN	PIN	Signal
+5V	1	2	DA1
BRIGHT	3	4	BRIGHT
GND	5	6	PWM1

***Default**

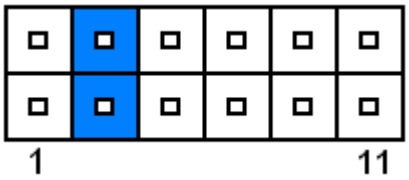
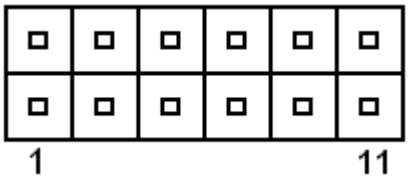
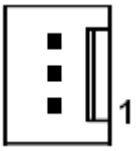
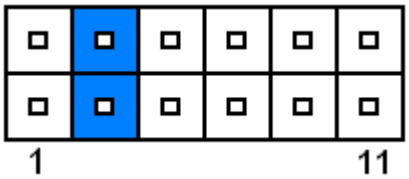
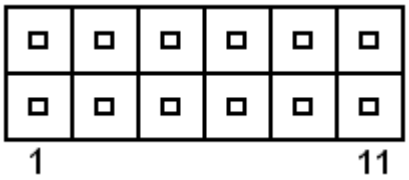
2.3.6 5VSB connector in ATX (PWR_SB)



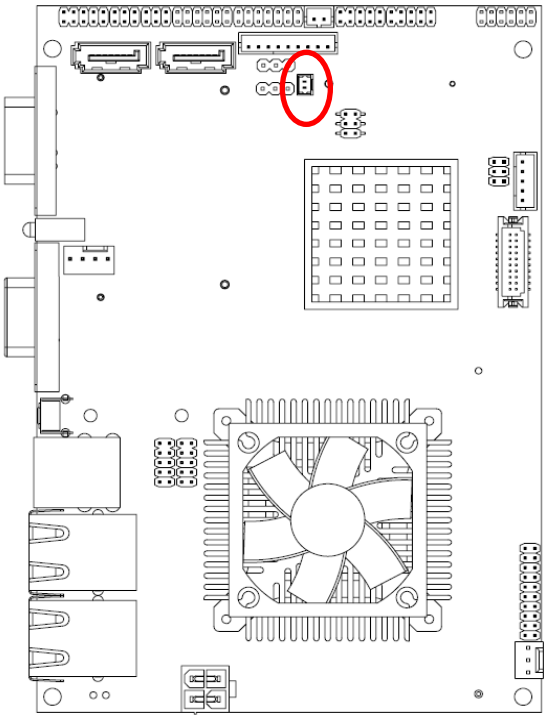


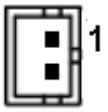
Signal	PIN
ATX5VSB	3
GND	2
PSON	1

2.3.6.1 Signal Description –AT/ ATX mode & Input power type

Input power type	Power-ON Mode	Description
AT Type	**AT Mode** (PWR MSEL)  	Use AT type power input, and set the board in AT mode.
	ATX Mode (PWR MSEL)  	Use AT type power input, and set the board in ATX mode.
ATX Type (PWR_SB)  	**AT Mode** (PWR MSEL)  	Use ATX type power input, and set the board in AT mode.
	ATX Mode (PWR MSEL)  	Use ATX type power input, and set the board in ATX mode.

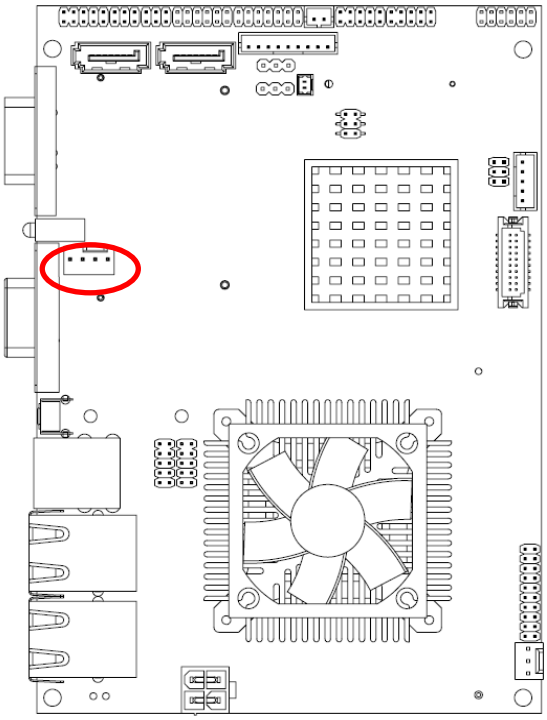
2.3.7 Battery connector (BAT)

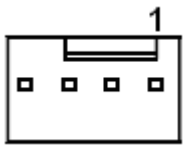




Signal	PIN
BAT	1
GND	2

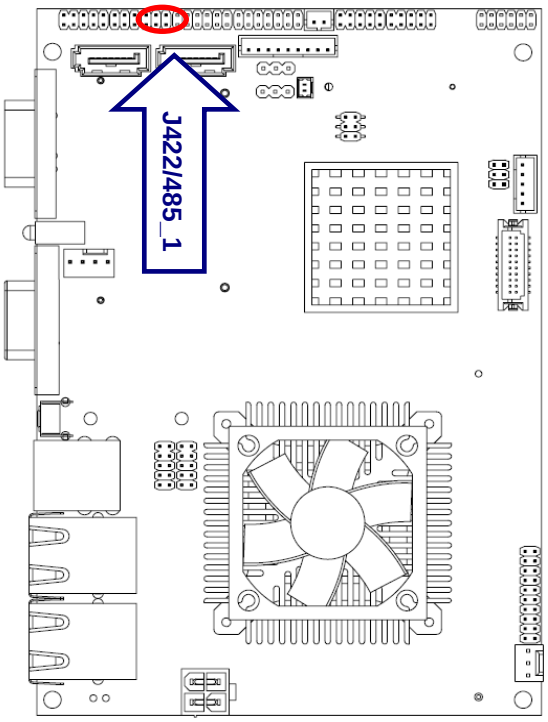
2.3.8 CPU fan connector (CPU_FAN)



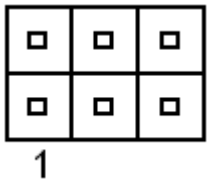


Signal	PIN
GND	1
VCC12	2
FAN_TAC1	3
FAN_CTL1	4

2.3.9 Serial port 2 in RS-422/485 mode (J422/485_1)



J422/485_1

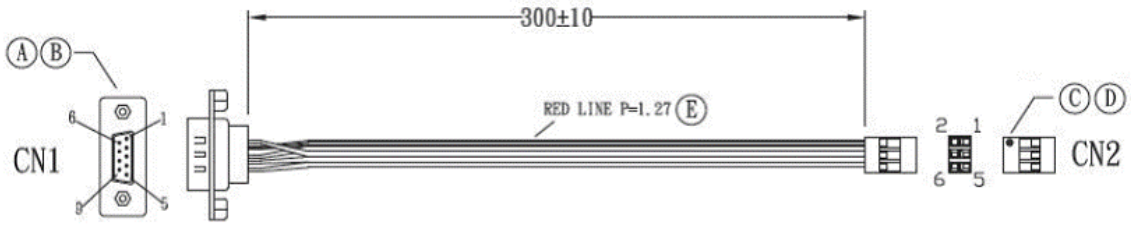


Signal	PIN	PIN	Signal
TX-	1	2	RX-
TX+	3	4	RX+
+5V	5	6	GND

Note:

J422/485 is available after modifying the mode of COM2 in BIOS setting. And the wiring must changed to E1701150300R and Connecting it to this J422/485_1 connector.

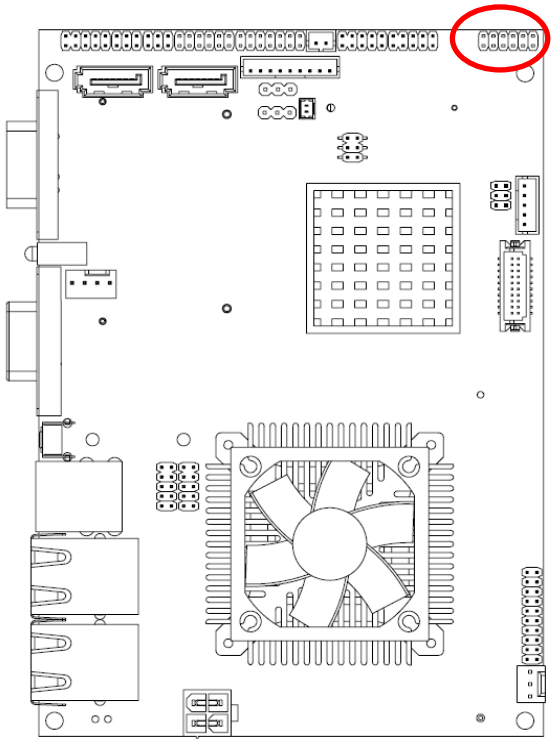
Cable part number: **E1701150300R**

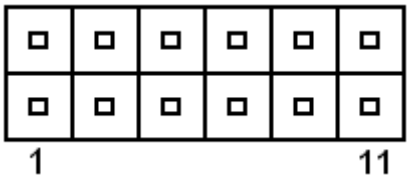


Cable mapping shown below

CN1 (DB9)		CN2		
signal	PIN	PIN	Signal(RS485)	Signal(RS422)
DCD	1			
RxD	2	4		RX+
TxD	3	3	Data+	TX+
DTR	4			
GND	5	6	GND	GND
DSR	6			
RTS	7	2		RX-
CTS	8	1	Data-	TX-
RI	9			

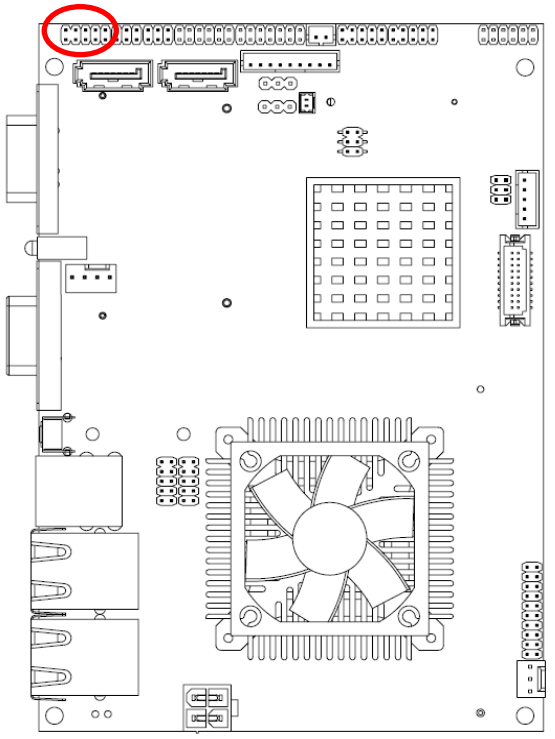
2.3.10 Audio connector (JAUDIO)

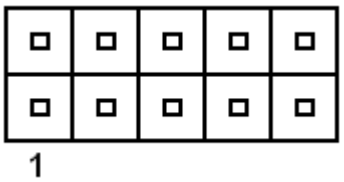




Signal	PIN	PIN	Signal
LINEOUT_R	1	2	LINEOUT_L
GND	3	4	GND
LINEIN_R	5	6	LINEIN_L
MIC-R	7	8	MIC-L
FRONT-JD	9	10	LINE1-JD
MIC1-JD	11	12	GND

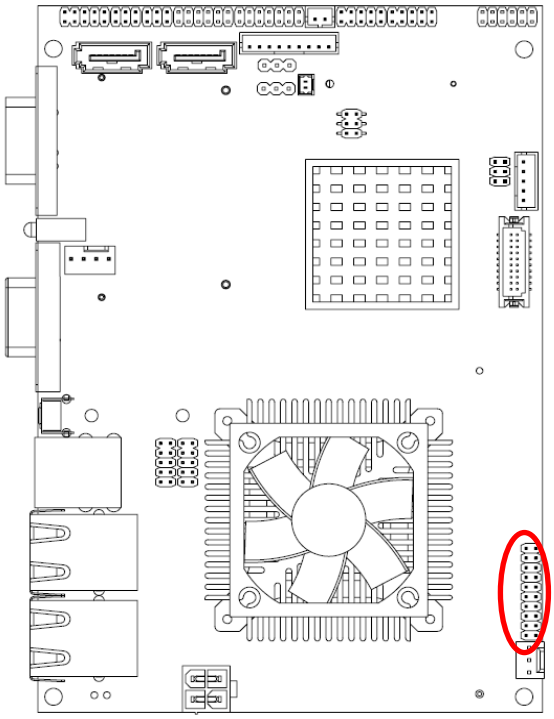
2.3.11 Serial port 2 connector (JCOM2)

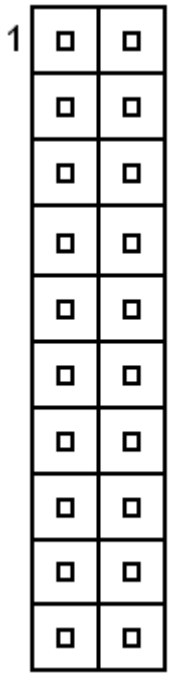




Signal	PIN	PIN	Signal
DCD2	1	2	RxDD2
TxDD2	3	4	DTR2
GND	5	6	DSR2
RTS2	7	8	CTS2
RI2	9	10	NC

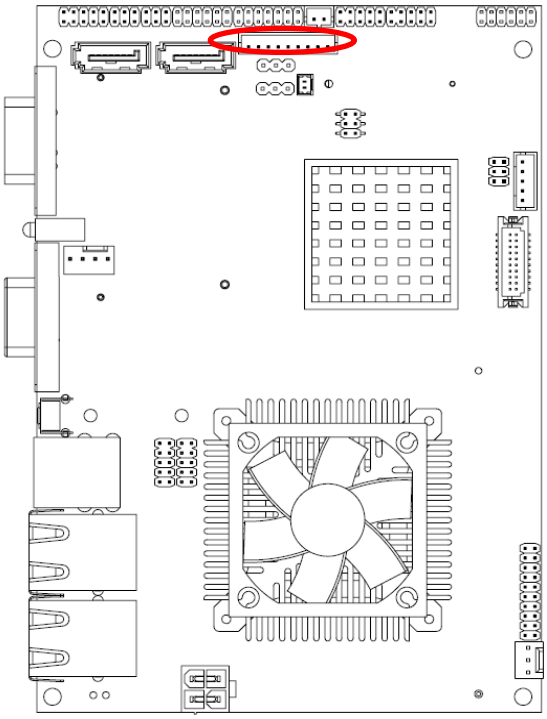
2.3.12 General purpose I/O connector (JDIO)

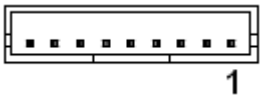




Signal	PIN	PIN	Signal
DIO0	1	2	DIO10
DIO1	3	4	DIO11
DIO2	5	6	DIO12
DIO3	7	8	DIO13
DIO4	9	10	DIO14
DIO5	11	12	DIO15
DIO6	13	14	DIO16
DIO7	15	16	DIO17
SMB_CLK	17	18	SMB_DATA
GND	19	20	+5V

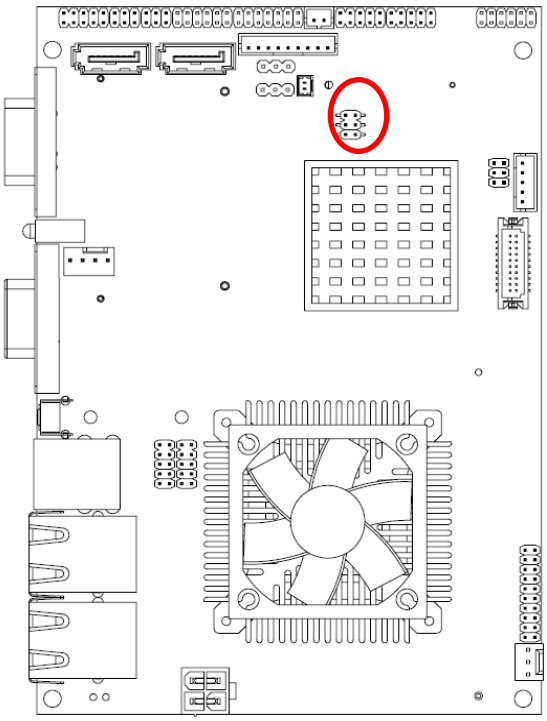
2.3.13 Touch panel connector (JTOUCH)

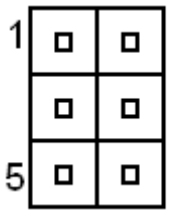




PIN	4-WIRE	5-WIRE	8-WIRE
1	N/A	N/A	Right Sense
2	N/A	N/A	Left Sense
3	N/A	N/A	Bottom Sense
4	N/A	Sense	Top Sense
5	Right	LR	Right Excite
6	Left	LL	Left Excite
7	Bottom	UR	Bottom Excite
8	Top	UL	Top Excite
9	GND	GND	GND

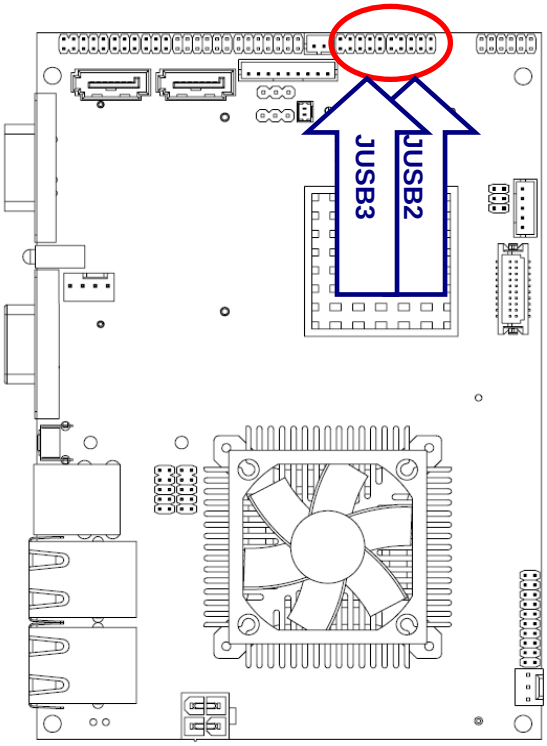
2.3.14 SPI connector (JSPI)

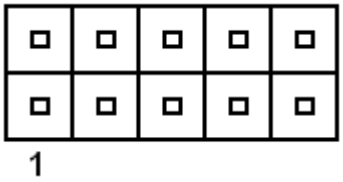




Signal	PIN	PIN	Signal
VSPI	1	2	GND
SPICE#	3	4	SPISCK
SPISO	5	6	SPISI

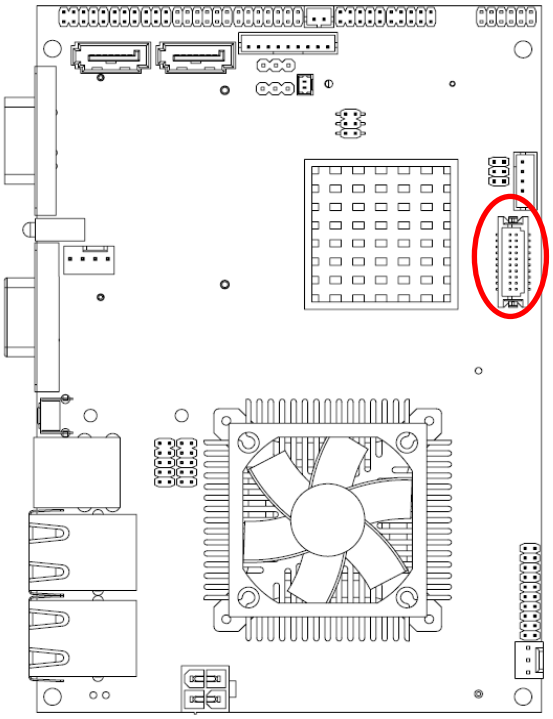
2.3.15 USB connector (JUSB2/ JUSB3)

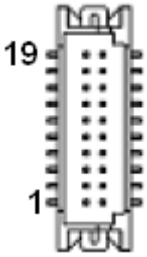




Signal	PIN	PIN	Signal
USBVCC	1	2	GND
N3/ N7	3	4	GND
P3/ P7	5	6	P2/ P6
GND	7	8	N2/ N6
GND	9	10	USBVCC

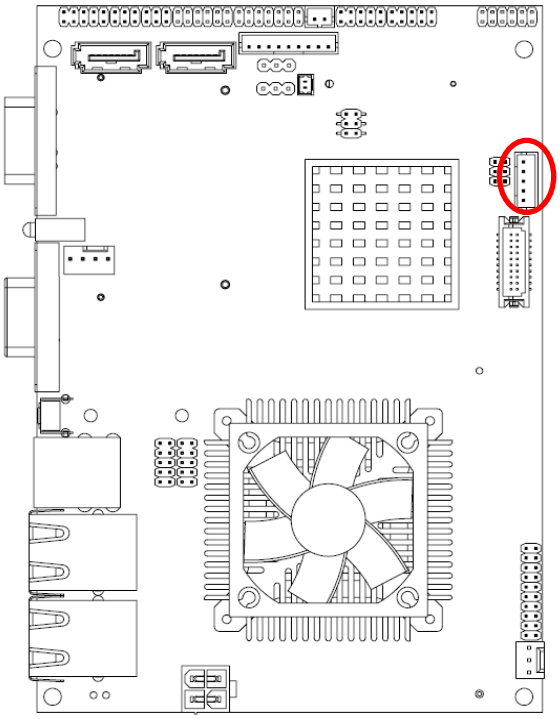
2.3.16 LVDS connector (JLVDS)



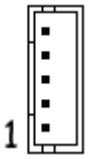


Signal	PIN	PIN	Signal
VCC3_LVDS	19	20	VCC_LVDS
VCC3_LVDS	17	18	VCC_LVDS
I_SDA	15	16	I_SCL
GND	13	14	GND
LVDS_CLK+	11	12	LVDS_CLK-
NC	9	10	NC
LVDS_2+	7	8	LVDS_2-
LVDS_1+	5	6	LVDS_1-
LVDS_0+	3	4	LVDS_0-
GND	1	2	GND

2.3.17 LCD Inverter Connector (JBKL)



JBKL



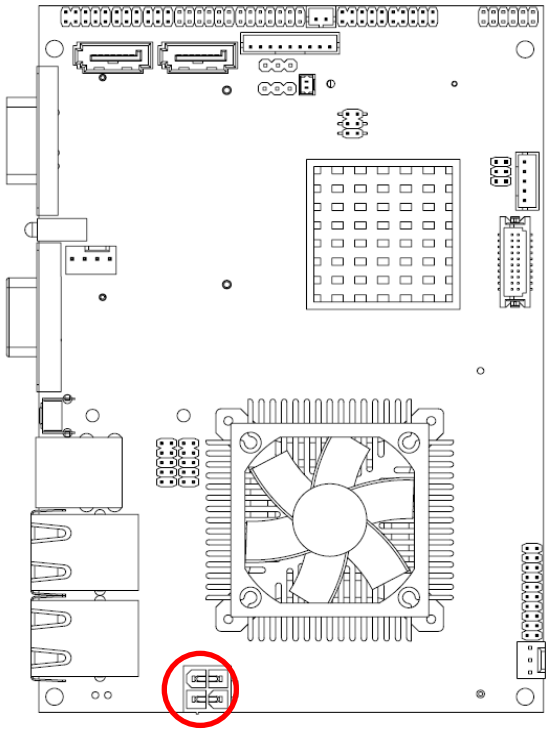
Signal	PIN
+12V	1
GND	2
BLK_ON	3
BRIGHT	4
+5V	5

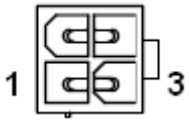
Note:
For inverters with adjustable Backlight function, it is possible to control the LCD brightness through the VR signal controlled by **JVR**. Please see the **JVR** section for detailed circuitry information.

2.3.17.1 Signal Description – LCD Inverter Connector (JBKL)

Signal	Signal Description
BRIGHT	Vadj = 0.75V ~ 4.25V (Recommended: 4.7KΩ, >1/16W)
BKL_ON	LCD backlight ON/OFF control signal

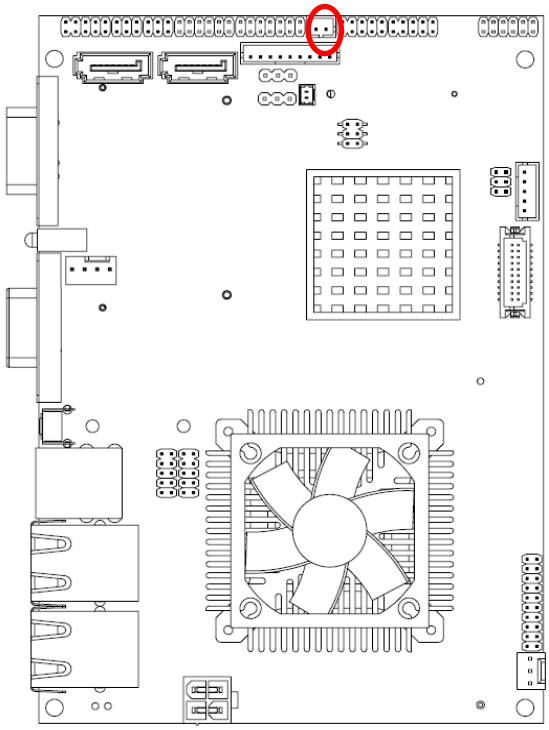
2.3.18 Power connector (PWR)

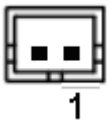




Signal	PIN	PIN	Signal
GND	2	4	VIN
GND	1	3	VIN

2.3.19 SATA power connector (S_PWR1)





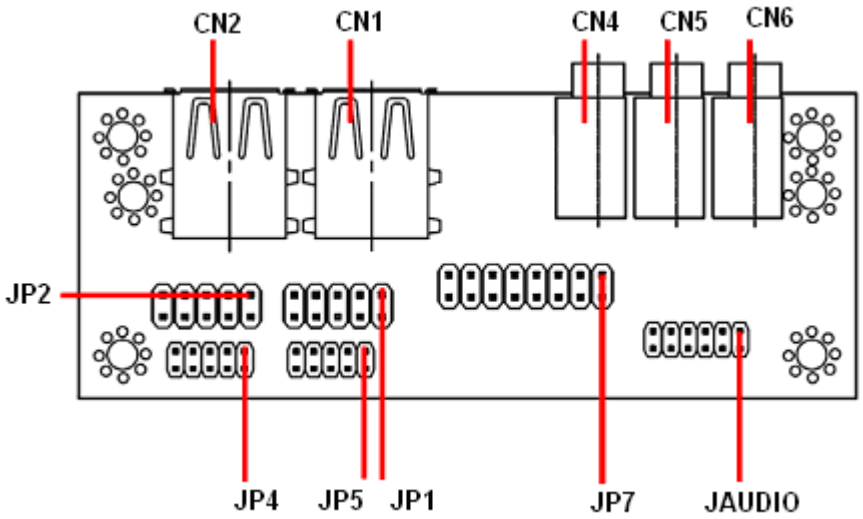
Signal	PIN
SATA_PWR	2
GND	1

Note:

SATA_PWR is _+5V for SATA DOM uses

2.4 Audio / USB Daughter Board User's Guide

2.4.1 Jumper and Connector Layout



2.4.2 Jumper and Connector List

Connectors

Label	Function	Note
CN1, CN2	USB connector	
CN4	Line out connector	Phone Jack
CN5	Line in connector	Phone Jack
CN6	Mic in connector	Phone Jack
JAUDIO	Audio connector	6 x 2 header, pitch 2.0mm
JP1	2.54mm USB connector	5 x 2 header, pitch 2.54mm
JP2	2.54mm USB connector	5 x 2 header, pitch 2.54mm
JP4	2.0mm USB connector	5 x 2 header, pitch 2.0mm
JP5	2.0mm USB connector	5 x 2 header, pitch 2.0mm
JP7	TV / Audio connector	8 x 2 header, pitch 2.54mm

2.4.3 Setting Jumper and Connector

Audio Connector (JAUDIO)

Signal	PIN	PIN	Signal
OUTR	1	2	OUTL
GND	3	4	GND
INR1	5	6	INL1
MICIN1	7	8	AREF
FRONT-JD1	9	10	LINE1-JD1
MIC1-JD1	11	12	GND

2.54mm USB Connector (JP1)

Signal	PIN	PIN	Signal
+5V	1	2	GND
D1-	3	4	GND
D1+	5	6	D2+
GND	7	8	D2-
GND	9	10	+5V

Note: Wrong USB cable configuration with your USB devices might damage your USB devices.

2.54mm USB Connector (JP2)

Signal	PIN	PIN	Signal
+5V	1	2	GND
D3-	3	4	GND
D3+	5	6	D4+
GND	7	8	D4-
GND	9	10	+5V

TV / Audio Connector (JP7)

Signal	PIN	PIN	Signal
Mic In	1	2	Mic Bais
GND	3	4	GND
Line out L	5	6	Line out R
SPK L	7	8	SPK R
Line in L	9	10	Line in R
GND	11	12	NC
TVGND	13	14	NC
TVGND	15	16	COMP

2.0mm USB Connector (JP4)

Signal	PIN	PIN	Signal
+5V	1	2	GND
D3-	3	4	GND
D3+	5	6	D4+
GND	7	8	D4-
GND	9	10	+5V

2.0mm USB Connector (JP5)

Signal	PIN	PIN	Signal
+5V	1	2	GND
D1-	3	4	GND
D1+	5	6	D2+
GND	7	8	D2-
GND	9	10	+5V