

ESM-QM87

Intel BGA Type CPU QM87 COM Express Type 6 Module

Quick Installation Guide

1st Ed – 06 January 2014

Notice

This guide is designed for experienced users to perform quick setup of the system. For detailed information, please always refer to the electronic user's manual.

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Part No. E2017286800R

FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

(1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.

(2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

A Message to the Customer

Avalue Customer Services

Each and every Avalue's product is built to the most exacting specifications to ensure reliable performance in the harsh and demanding conditions typical of industrial environments. Whether your new Avalue device is destined for the laboratory or the factory floor, you can be assured that your product will provide the reliability and ease of operation for which the name Avalue has come to be known.

Your satisfaction is our primary concern. Here is a guide to Avalue's customer services. To ensure you get the full benefit of our services, please follow the instructions below carefully.

Technical Support

We want you to get the maximum performance from your products. So if you run into technical difficulties, we are here to help. For the most frequently asked questions, you can easily find answers in your product documentation. These answers are normally a lot more detailed than the ones we can give over the phone. So please consult the user's manual first.

To receive the latest version of the user's manual; please visit our Web site at:

<http://www.avalue.com.tw/>

1. Getting Started

1.1 Safety Precautions

Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

1.2 Packing List

Before you begin installing your single board, please make sure that the following materials have been shipped:

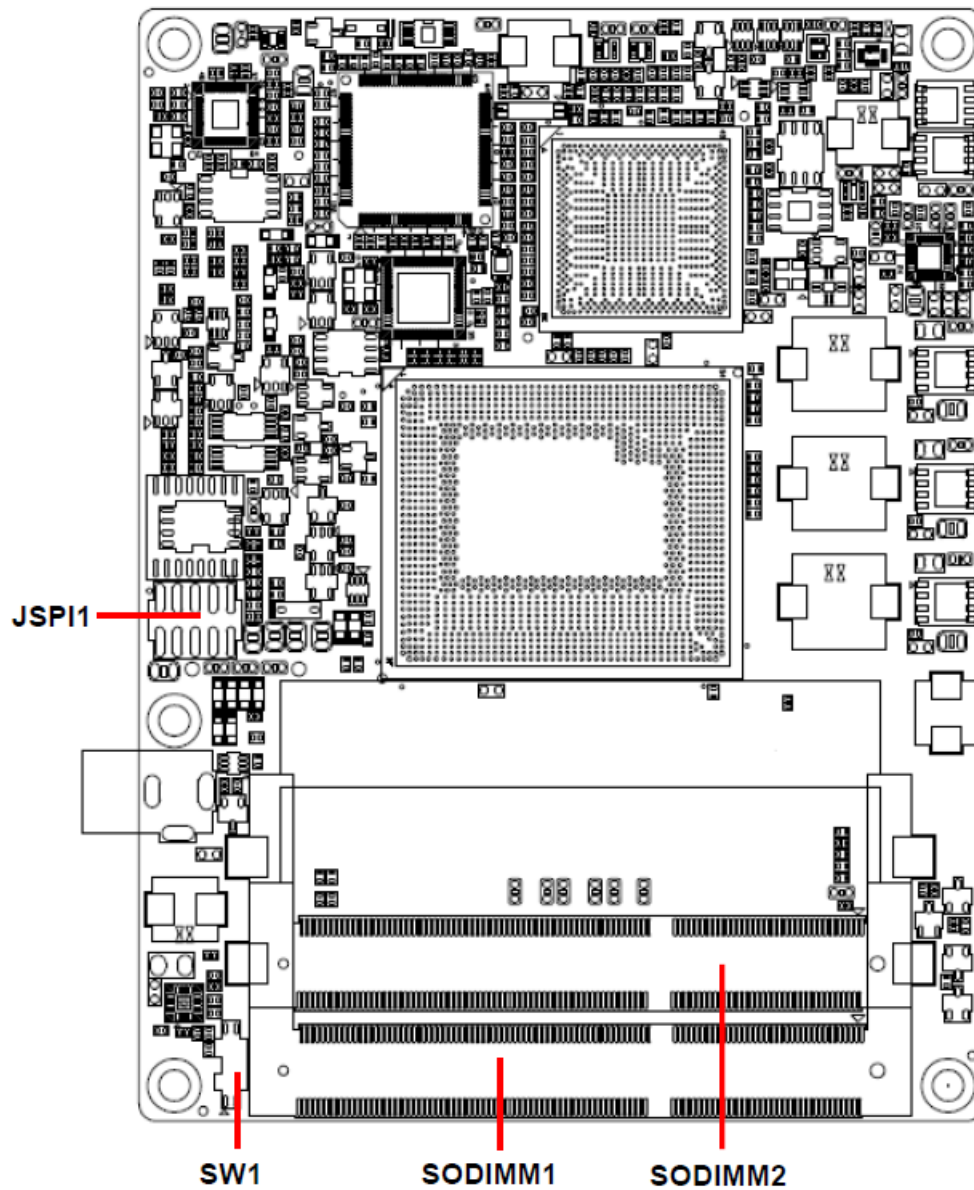
- 1 x Intel BGA Type CPU QM87 COM Express Type 6 Module
- 1 x Quick Installation Guide
- 1 x DVD-ROM contains the followings:
 - User's Manual (this manual in PDF file)
 - Chipset and Ethernet driver

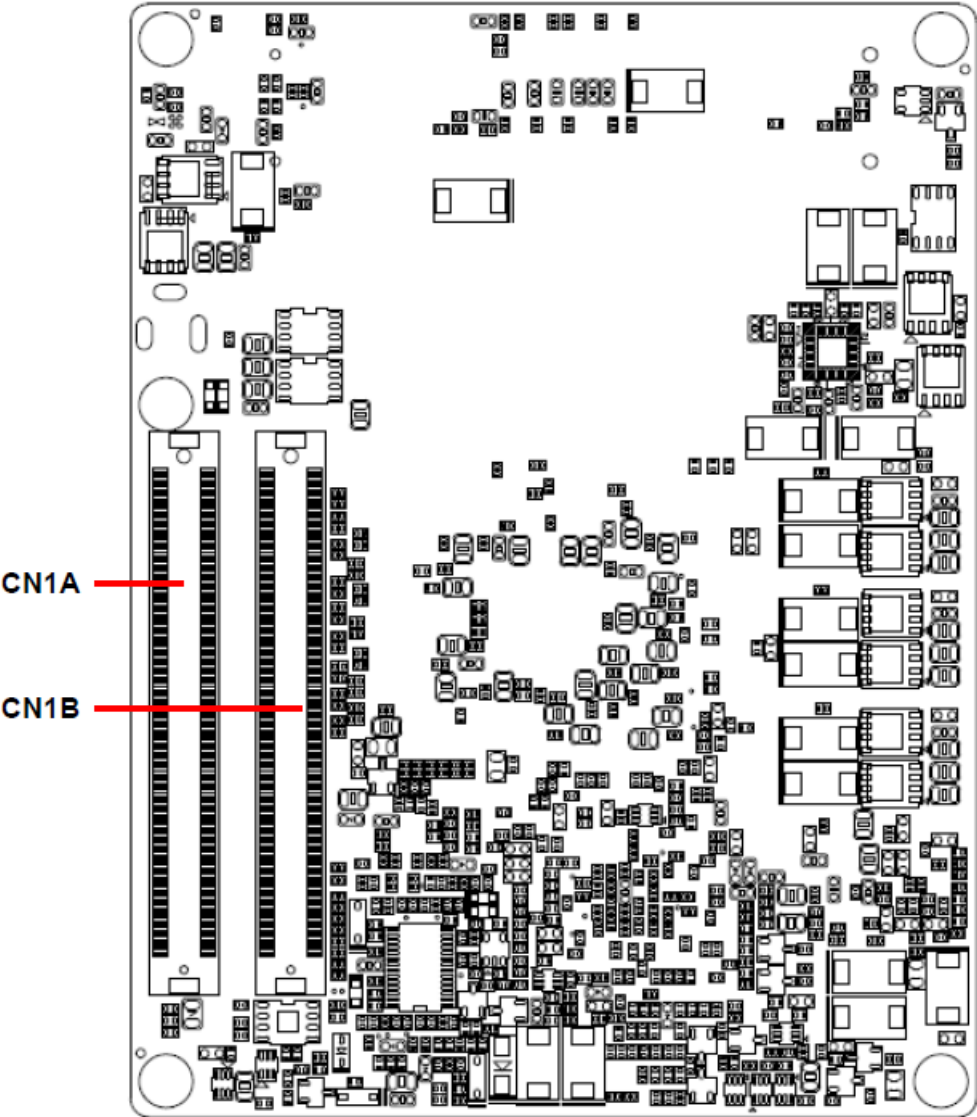


If any of the above items is damaged or missing, contact your retailer.

2. Hardware Configuration

2.1 Product Overview

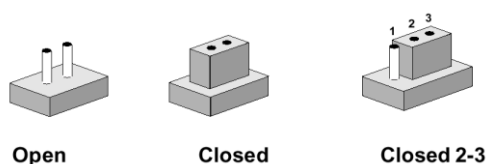




2.2 Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

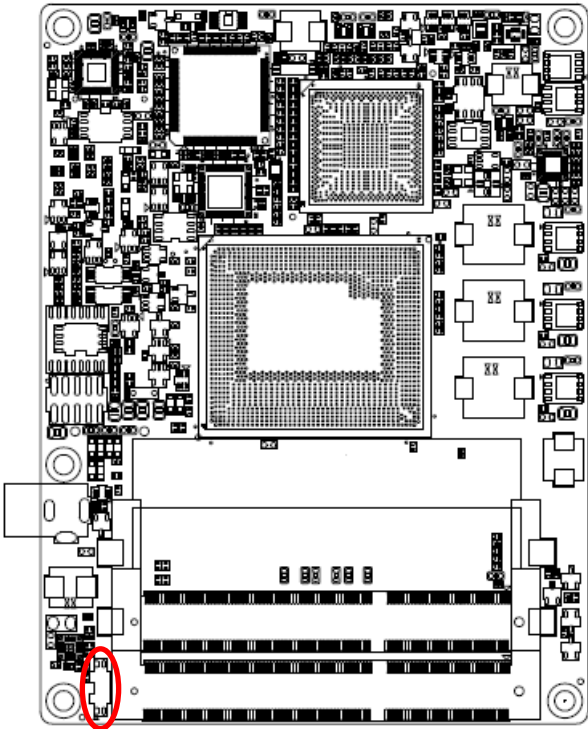
If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board’s jumpers and connectors.

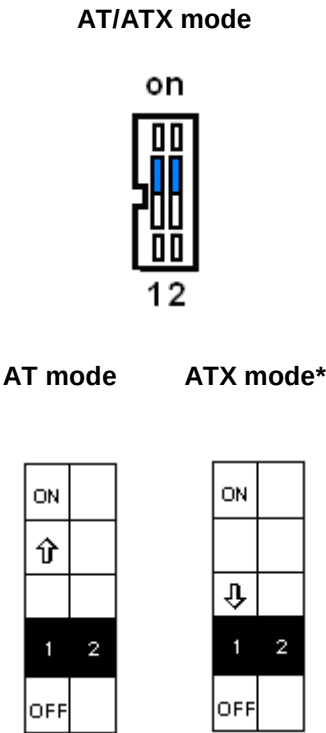
Connectors

Label	Function	Note
JSPI1	(Reserved for BIOS programming)	5 x 2 header, pitch 2.00mm
CN1A	COM Express connector 1	
CN1B	COM Express connector 2	
SODIMM1	204-pin DDR3 SDRAM DIMM socket	
SODIMM2	204-pin DDR3 SDRAM DIMM socket	
SW1	AT/ATX mode selector	

2.3.1 AT/ATX mode selector (SW1)



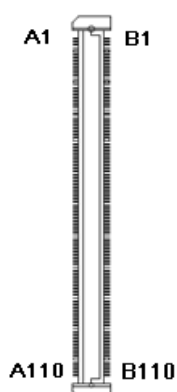
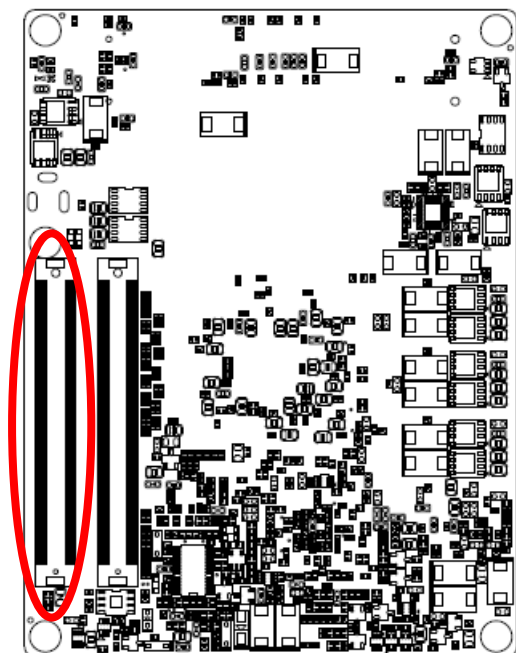
*Default



2.3.1.1 Signal Description –AT/ATX mode selection

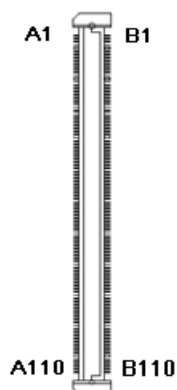
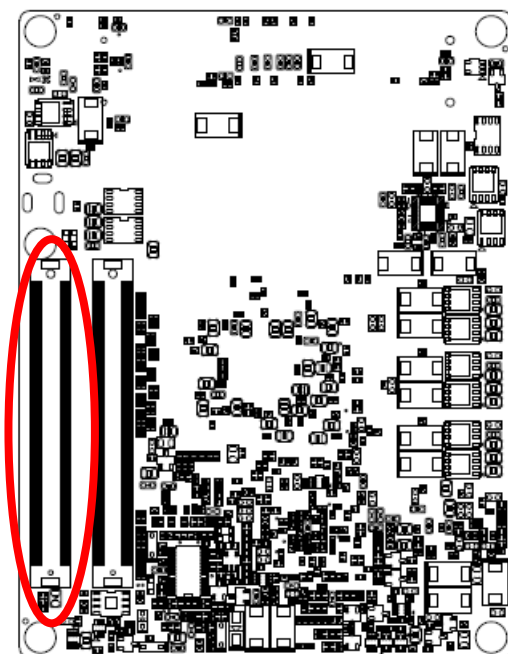
AT/ATX mode	Description
AT mode on 12	Auto-power on, no need to press Power button to enable power on/off
ATX mode on 12	Press the power button to enable power on/off

2.3.2 COM Express Connector 1 (CN1A)



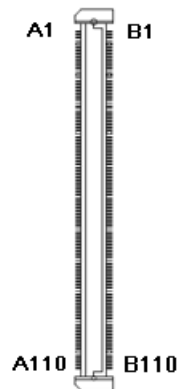
Signal	PIN	PIN	Signal
GND	A1	B1	GND
GBE0_MDI3-	A2	B2	GBE0_ACT#
GBE0_MDI3+	A3	B3	LPC_FRAME#
GBE0_LINK100#	A4	B4	LPC_AD0
GBE0_LINK1000#	A5	B5	LPC_AD1
GBE0_MDI2-	A6	B6	LPC_AD2
GBE0_MDI2+	A7	B7	LPC_AD3
GBE0_LINK#	A8	B8	LPC_DRQ0#
GBE0_MDI1-	A9	B9	LPC_DRQ1#
GBE0_MDI1+	A10	B10	LPC_CLK
GND	A11	B11	GND
GBE0_MDI0-	A12	B12	PWRBTN#
GBE0_MDI0+	A13	B13	SMB_CK
GBE0_CTREF	A14	B14	SMB_DAT
SUS_S3#	A15	B15	SMB_ALERT#
SATA0_TX+	A16	B16	SATA1_TX+
SATA0_TX-	A17	B17	SATA1_TX-
SUS_S4#	A18	B18	SUS_STAT#
SATA0_RX+	A19	B19	SATA1_RX+
SATA0_RX-	A20	B20	SATA1_RX-
GND	A21	B21	GND
SATA2_TX+	A22	B22	SATA3_TX+
SATA2_TX-	A23	B23	SATA3_TX-
SUS_S5#	A24	B24	PWR_OK
SATA2_RX+	A25	B25	SATA3_RX+
SATA2_RX-	A26	B26	SATA3_RX-
BATLOW#	A27	B27	WDT
(S)ATA_ACT#	A28	B28	HDA_SDIN2
HDA_SYNC	A29	B29	HDA_SDIN1
HDA_RST#	A30	B30	HDA_SDIN0

ESM-QM87

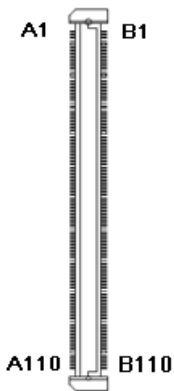
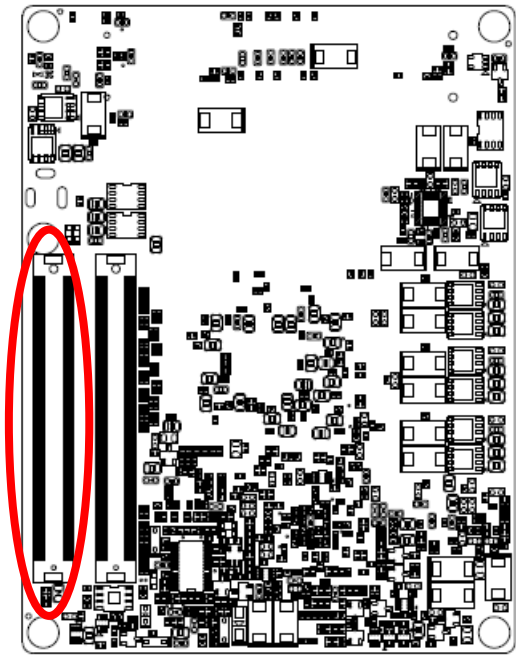


Signal	PIN	PIN	Signal
GND	A31	B31	GND
HDA_BITCLK	A32	B32	SPKR
HDA_SDOOUT	A33	B33	NC
BIOS_DIS0#	A34	B34	NC
NC	A35	B35	NC
USB6-	A36	B36	USB7-
USB6+	A37	B37	USB7+
USB_6_7_OC#	A38	B38	USB_4_5_OC#
USB4-	A39	B39	USB5-
USB4+	A40	B40	USB5+
GND	A41	B41	GND
USB2-	A42	B42	USB3-
USB2+	A43	B43	USB3+
USB_2_3_OC#	A44	B44	USB_0_1_OC#
USB0-	A45	B45	USB1-
USB0+	A46	B46	USB1+
VCC_RTC	A47	B47	EXCD1_PERST#
EXCD0_PERST#	A48	B48	EXCD1_CPPE#
EXCD0_CPPE#	A49	B49	SYS_RESET#
LPC_SERIRQ	A50	B50	CB_RESET#
GND	A51	B51	GND
PCIE_TX5+	A52	B52	PCIE_RX5+
PCIE_TX5-	A53	B53	PCIE_RX5-
GPI0	A54	B54	GPO1
PCIE_TX4+	A55	B55	PCIE_RX4+
PCIE_TX4-	A56	B56	PCIE_RX4-
GND	A57	B57	GPO2
PCIE_TX3+	A58	B58	PCIE_RX3+
PCIE_TX3-	A59	B59	PCIE_RX3-
GND	A60	B60	GND

Quick Installation Guide



Signal	PIN	PIN	Signal
PCIE_TX2+	A61	B61	PCIE_RX2+
PCIE_TX2-	A62	B62	PCIE_RX2-
GPI1	A63	B63	GPO3
PCIE_TX1+	A64	B64	PCIE_RX1+
PCIE_TX1-	A65	B65	PCIE_RX1-
GND	A66	B66	WAKE0#
GPI2	A67	B67	WAKE1#
PCIE_TX0+	A68	B68	PCIE_RX0+
PCIE_TX0-	A69	B69	PCIE_RX0-
GND	A70	B70	GND
LVDS_A0+	A71	B71	LVDS_B0+
LVDS_A0-	A72	B72	LVDS_B0-
LVDS_A1+	A73	B73	LVDS_B1+
LVDS_A1-	A74	B74	LVDS_B1-
LVDS_A2+	A75	B75	LVDS_B2+
LVDS_A2-	A76	B76	LVDS_B2-
LVDS_VDD_EN	A77	B77	LVDS_B3+
LVDS_A3+	A78	B78	LVDS_B3-
LVDS_A3-	A79	B79	LVDS_BKLT_EN
GND	A80	B80	GND
LVDS_A_CK+	A81	B81	LVDS_B_CK+
LVDS_A_CK-	A82	B82	LVDS_B_CK-
NC	A83	B83	LVDS_BKLT_CTRL
NC	A84	B84	VCC_5V_SBY_1
GPI3	A85	B85	VCC_5V_SBY_2
RSVD1	A86	B86	VCC_5V_SBY_3
RSVD2	A87	B87	VCC_5V_SBY_4
PCIE_CLK_REF+	A88	B88	BIOS_DIS1#
PCIE_CLK_REF--	A89	B89	VGA_RED
GND	A90	B90	GND



Signal	PIN	PIN	Signal
SPI_POWER	A91	B91	VGA_GRN
SPI_MISO	A92	B92	VGA_BLU
GPO0	A93	B93	VGA_HSYNC
SPI_CLK	A94	B94	VGA_VSYNC
SPI_MOSI	A95	B95	VGA_I2C_CK
PP_TPM	A96	B96	VGA_I2C_DAT
TYPE10#	A97	B97	SPI_CS#
NC	A98	B98	RSVD3
NC	A99	B99	RSVD4
GND	A100	B100	GND
NC	A101	B101	FAN_PWMOUT
NC	A102	B102	FAN_TACHIN
LID#	A103	B103	SLEEP#
VCC	A104	B104	VCC
VCC	A105	B105	VCC
VCC	A106	B106	VCC
VCC	A107	B107	VCC
VCC	A108	B108	VCC
VCC	A109	B109	VCC
GND	A110	B110	GND

2.3.2.1 Signal Description – COM Express Connector 1 (CN1A)

2.3.2.1.1 Audio Signals

Signal	Signal Description
HDA_SYNC	HD Audio Sync
HDA_RST#	HD Audio Reset
HDA_SDIN[0:2]	Audio CODEC Serial Data
HDA_BITCLK	HD Audio Clock
HDA_SDOUT	HD Audio Data

2.3.2.1.2 Gigabit Ethernet Signals

Signal	Signal Description			
GBE0_MD[0:3] +/-	Gigabit Ethernet Controller 0: Media Dependent Interface Differential Pairs 0,1,2,3. The MDI can operate in 1000, 100 and 10 Mbit / sec modes. Some pairs are unused in some modes, per the following:			
		1000B-T	100B-T	10B-T
	MDI[0]+/-	B1_DA+/-	TX+/-	TX+/-
	MDI[1]+/	B1_DB+/-	RX+/-	RX+/-
	MDI[2]+/	B1_DC+/-	X	X
	MDI[3]+/	B1_DD+/-	X	X
GBE0_ACT#	Gigabit Ethernet Controller 0 activity indicator, active low.			
GBE0_Link#	Gigabit Ethernet Controller 0 link indicator, active low.			
GBE0_Link100#	Gigabit Ethernet Controller 0 100 Mbit / sec link indicator, active low.			
GBE0_Lin1000#	Gigabit Ethernet Controller 0 1000 Mbit / sec link indicator, active low.			

2.3.2.1.3 GPIO Signals

Signal	Signal Description
GPI[0:4]	General purpose input pins.
GPO[0:4]	General purpose output pins.

ESM-QM87

2.3.2.1.4 Flat Panel LVDS Signals

Signal	Signal Description
LVDS_BKLT_CTRL	Controls panel digital power.
ENBKL#	Controls backlight power enable.

2.3.2.1.5 LPC Signals

Signal	Signal Description
LPC_FRAME#	LPC frame indicates the start of an LPC cycle
LPC_AD[0:3]	LPC multiplexed address, command and data bus
LPC_DRQ[0:1]#	LPC serial DMA request
LPC_CLK	LPC clock output - 33MHz nominal
LPC_SERIRQ	LPC serial interrupt

2.3.2.1.6 Miscellaneous Signals

Signal	Signal Description							
SPKR	Output for audio enunciator - the "speaker" in PC-AT systems							
BIOS_DIS0# BIOS_DIS1#	Selection straps to determine the BIOS boot device							
	BIOS_DIS1#	BIOS_DIS0#	Chipset SPI CS1# Destination	Chipset SPI CS0# Destination	Carrier SPI_CS#	SPI Descriptor	Bios Entry	Ref Line
	1	1	Module	Module	High	Module	SPI0/SPI1	0
	1	0	Module	Module	High	Module	Carrier FWH	1
	0	1	Module	Carrier	SPI0	Carrier	SPI0/SPI1	2
	0	0	Carrier	Module	SPI1	Module	SPI0/SPI1	3

2.3.2.1.7 PCI Express Signals

Signal	Signal Description
PCIE_TX[0:6] +/-	PCI Express Differential Transmit Pair 0-6
PCIE_RX[0:6] +/-	PCI Express Differential Receive Pair 0-6
PCIE0_CK_REF+/-	Reference clock output for PCI Express lanes 0-6 and for PCI Express Graphics lanes 0-15

2.3.2.1.8 Power Signals

Signal	Signal Description
VCC_5V_SBY	Standby power input: +5.0V nominal. See Electrical Specifications for allowable input range. If VCC5_SBY is used, all available VCC_5V_SBY pins on the connector(s) must be used. Only used for standby and suspend functions. May be left unconnected if these functions are not used in the system design.
VCC_RTC	Real-time clock circuit-power input. Nominally +3.0V.

2.3.2.1.9 Power & System Management Signals

Signal	Signal Description
SUS_S3#	Indicates system is in Suspend to RAM state. Active low output.
SUS_S4#	Indicates system is in Suspend to Disk state. Active low output.
SUS_S5#	Indicates system is in Soft Off state.
BATLOW#	Indicates that external battery is low
PWRBTN#	Power button to bring system out of S5 (soft off), active on rising edge.
SMB_CK	System Management Bus bidirectional clock line.
SMB_DTA	System Management Bus bidirectional data line.
SMB_ALERT#	System Management Bus Alert - input can be used to generate an SMI# (System Management Interrupt) or to wake the system.
SUS_STAT#	Indicates imminent suspend operation.
PWR_OK	Power OK from main power supply
SYS_RESET#	Reset button input. Active low input.
WAKE0#	PCI Express wake up signal.
WAKE1#	General purpose wake up signal.

2.3.2.1.10 SATA Signals

Signal	Signal Description
SATA[0:3]_TX +/-	Serial ATA Channel 0-3 transmit differential pair.
SATA[0:3]_RX +/-	Serial ATA Channel 0-3 receive differential pair.
ATA_ACT#	ATA (parallel and serial) activity indicator, active low.

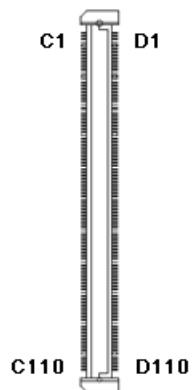
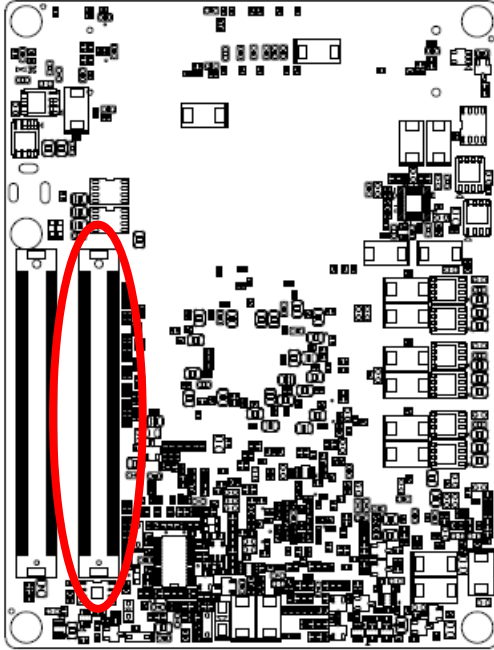
2.3.2.1.11 VGA Signals

Signal	Signal Description
VGA_RED	Red for monitor. Analog DAC output.
VGA_GRN	Green for monitor. Analog DAC output.
VGA_BLU	Blue for monitor. Analog DAC output.
VGA_HSYNC	Horizontal sync output to VGA monitor
VGA_VSYNC	Vertical sync output to VGA monitor
VGA_I ² C_CLK	DDC clock line (I2C port dedicated to identify VGA monitor capabilities)
VGA_I ² C_DAT	DDC data line.

2.3.2.1.12 USB Signals

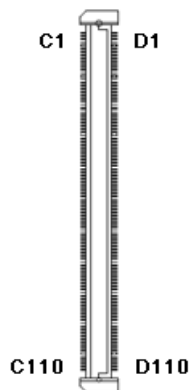
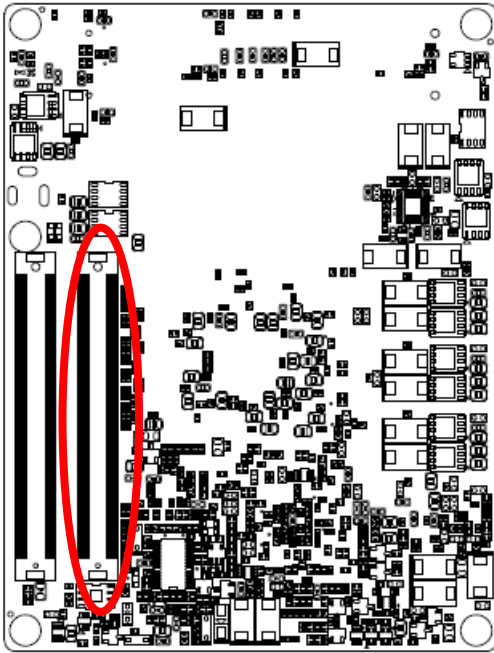
Signal	Signal Description
USB[0:7] +/-	USB differential pairs, channels 0 through 7
USB_0_1_OC#	USB over-current sense, USB channels 0 and 1
USB_2_3_OC#	USB over-current sense, USB channels 2 and 3
USB_4_5_OC#	USB over-current sense, USB channels 4 and 5
USB_6_7_OC#	USB over-current sense, USB channels 6 and 7

2.3.3 COM Express Connector 2 (CN1B)



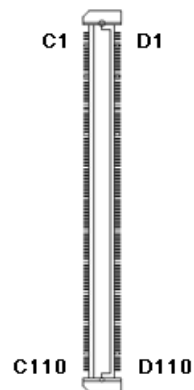
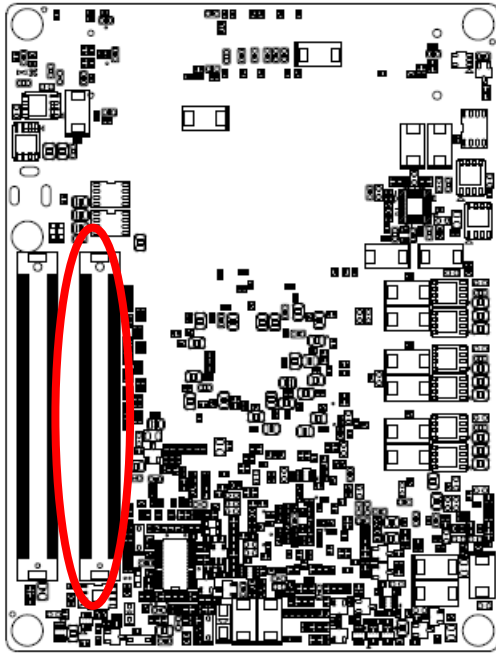
Signal	PIN	PIN	Signal
GND	C1	D1	GND
GND	C2	D2	GND
USB_SSRX0-	C3	D3	USB_SSTX0-
USB_SSRX0+	C4	D4	USB_SSTX0+
GND	C5	D5	GND
USB_SSRX1-	C6	D6	USB_SSTX1-
USB_SSRX1+	C7	D7	USB_SSTX1+
GND	C8	D8	GND
USB_SSRX2-	C9	D9	USB_SSTX2-
USB_SSRX2+	C10	D10	USB_SSTX2+
GND	C11	D11	GND
USB_SSRX3-	C12	D12	USB_SSTX3-
USB_SSRX3+	C13	D13	USB_SSTX3+
GND	C14	D14	GND
NC	C15	D15	DDI1_CTRLCLK_AUX+
NC	C16	D16	DDI1_CTRLDATA_AUX-
RSVD5	C17	D17	NC
RSVD6	C18	D18	NC
PCIE_RX6+	C19	D19	PCIE_TX6+
PCIE_RX6-	C20	D20	PCIE_TX6-
GND	C21	D21	GND
NC	C22	D22	NC
NC	C23	D23	NC
DDI1_HPD	C24	D24	NC
NC	C25	D25	NC
NC	C26	D26	DDI1_PAIR0+
NC	C27	D27	DDI1_PAIR0-
NC	C28	D28	NC
NC	C29	D29	DDI1_PAIR1+
NC	C30	D30	DDI1_PAIR1-

ESM-QM87

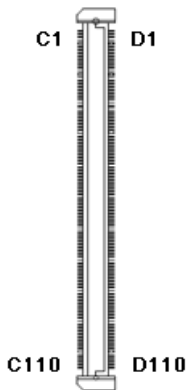
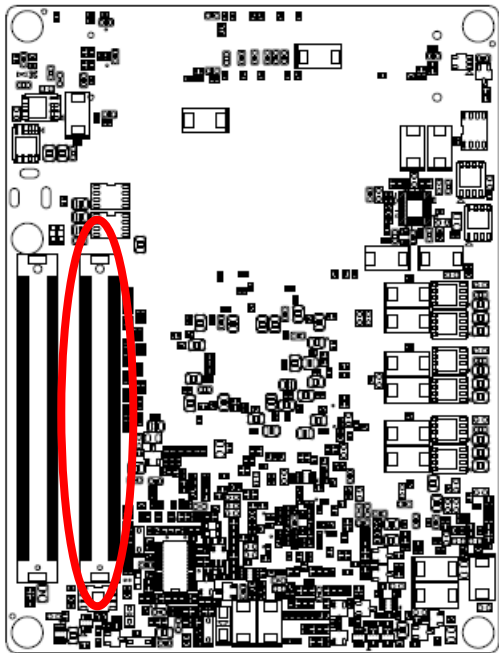


Signal	PIN	PIN	Signal
GND	C31	D31	GND
DDI2_CTRLCLK_AUX+	C32	D32	DDI1_PAIR2+
DDI2_CTRLCLK_AUX-	C33	D33	DDI1_PAIR2-
DDI2_DDC_AUX_SEL	C34	D34	DDI1_DDC_AUX_SEL
NC	C35	D35	NC
DDI3_CTRLCLK_AUX+	C36	D36	DDI1_PAIR3+
DDI3_CTRLCLK_AUX-	C37	D37	DDI1_PAIR3-
DDI3_DDC_AUX_SEL	C38	D38	NC
DDI3_PAIR0+	C39	D39	DDI2_PAIR0+
DDI3_PAIR0-	C40	D40	DDI2_PAIR0-
GND	C41	D41	GND
DDI3_PAIR1+	C42	D42	DDI2_PAIR1+
DDI3_PAIR1-	C43	D43	DDI2_PAIR1-
DDI3_HPD	C44	D44	DDI2_HPD
NC	C45	D45	NC
DDI3_PAIR2+	C46	D46	DDI2_PAIR2+
DDI3_PAIR2-	C47	D47	DDI2_PAIR2-
NC	C48	D48	NC
DDI3_PAIR3+	C49	D49	DDI2_PAIR3+
DDI3_PAIR3-	C50	D50	DDI2_PAIR3-
GND	C51	D51	GND
PEG_RX0+	C52	D52	PEG_TX0+
PEG_RX0-	C53	D53	PEG_TX0-
TYPE0#	C54	D54	PEG_LANE_RV#
PEG_RX1+	C55	D55	PEG_TX1+
PEG_RX1-	C56	D56	PEG_TX1-
TYPE1#	C57	D57	TYPE2#
PEG_RX2+	C58	D58	PEG_TX2+
PEG_RX2-	C59	D59	PEG_TX2-
GND	C60	D60	GND

Quick Installation Guide



Signal	PIN	PIN	Signal
PEG_RX3+	C61	D61	PEG_TX3+
PEG_RX3-	C62	D62	PEG_TX3-
NC	C63	D63	NC
NC	C64	D64	NC-
PEG_RX4+	C65	D65	PEG_TX4+
PEG_RX4-	C66	D66	PEG_TX4-
NC	C67	D67	GND
PEG_RX5+	C68	D68	PEG_TX5+
PEG_RX5-	C69	D69	PEG_TX5-
GND	C70	D70	GND
PEG_RX6+	C71	D71	PEG_TX6+
PEG_RX6-	C72	D72	PEG_TX6-
GND	C73	D73	GND
PEG_RX7+	C74	D74	PEG_TX7+
PEG_RX7-	C75	D75	PEG_TX7-
GND	C76	D76	GND
NC	C77	D77	NC
PEG_RX8+	C78	D78	PEG_TX8+
PEG_RX8-	C79	D79	PEG_TX8-
GND	C80	D80	GND
PEG_RX9+	C81	D81	PEG_TX9+
PEG_RX9-	C82	D82	PEG_TX9-
NC	C83	D83	NC
GND	C84	D84	GND
PEG_RX10+	C85	D85	PEG_TX10+
PEG_RX10-	C86	D86	PEG_TX10-
GND	C87	D87	GND
PEG_RX11+	C88	D88	PEG_TX11+
PEG_RX11-	C89	D89	PEG_TX11-
GND	C90	D90	GND



Signal	PIN	PIN	Signal
PEG_RX12+	C91	D91	PEG_TX12+
PEG_RX12-	C92	D92	PEG_TX12-
GND	C93	D93	GND
PEG_RX13+	C94	D94	PEG_TX13+
PEG_RX13-	C95	D95	PEG_TX13-
GND	C96	D96	GND
NC	C97	D97	NC
PEG_RX14+	C98	D98	PEG_TX14+
PEG_RX14-	C99	D99	PEG_TX14-
GND	C100	D100	GND
PEG_RX15+	C101	D101	PEG_TX15+
PEG_RX15-	C102	D102	PEG_TX15-
GND	C103	D103	GND
VCC	C104	D104	VCC
VCC	C105	D105	VCC
VCC	C106	D106	VCC
VCC	C107	D107	VCC
VCC	C108	D108	VCC
VCC	C109	D109	VCC
GND	C110	D110	GND

2.3.3.1 Signal Description – COM Express Connector 2 (CN1B)

2.3.3.1.1 USB Signals

Signal	Signal Description
USB_SSTX[0:3]+ USB_SSTX[0:3]-	Additional transmit signal differential pairs for the SuperSpeed USB data path.
USB_SSRX[0:3]+ USB_SSRX[0:3]-	Additional receive signal differential pairs for the SuperSpeed USB data path.

2.3.3.1.2 PEG Signals

Signal	Signal Description
PEG_TX[0:15]+ PEG_TX[0:15]-	PCI Express Graphics transmit differential paris.
PEG_RX[0:15]+ PEG_RX[0:15]-	PCI Express Graphics recevie differential paris.
PEG_LANE_RV#	PCI Express Graphics lane reversal input strap. Pull low on the Carrier board to reverse lane order.

2.3.3.1.3 DDI Signals

Signal	Signal Description
DDI[1:3]_PAIR[0:3]+ DDI[1:3]_PAIR [0:3]-	Digital Display Interface 1 to 3 Pair[0:3] differential pairs
DDI[1:3]_DDC_AUX_SEL	Selects the function of DDI[1:3]_CTRLCLK_AUX+ and DDI[1:3]_CTRLDATA_AUX-. If this input is floating the AUX pair is used for the DP AUX+/- signals. If pulled-high the AUX pair contains the CRTCLK and CTRLDATA signals.
DDI[1:3]_CTRLCLK_AUX+	DP AUX+function if DDI[1:3]_DDC_AUX_SEL is no connect HDMI/DVI 12C CTRLCLK if DDI[1:3]_DDC_AUX_SEL is pulled high
DDI[1:3]_CTRLDATA_AUX-	DP AUX-function if DDI[1:3]_DDC_AUX_SEL is no connect HDMI/DVI 12C CTRLDATA if DDI[1:3]_DDC_AUX_SEL is pulled high
DDI[1:3]_HPD	Digital Display Interface Hot-Plug Detect

2.4 ESD/EMI solutions

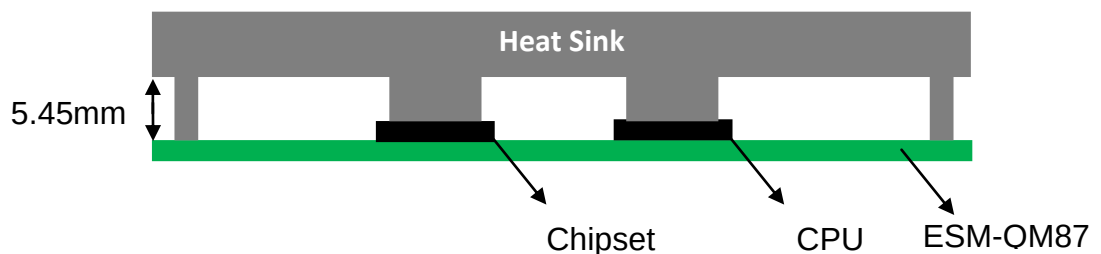
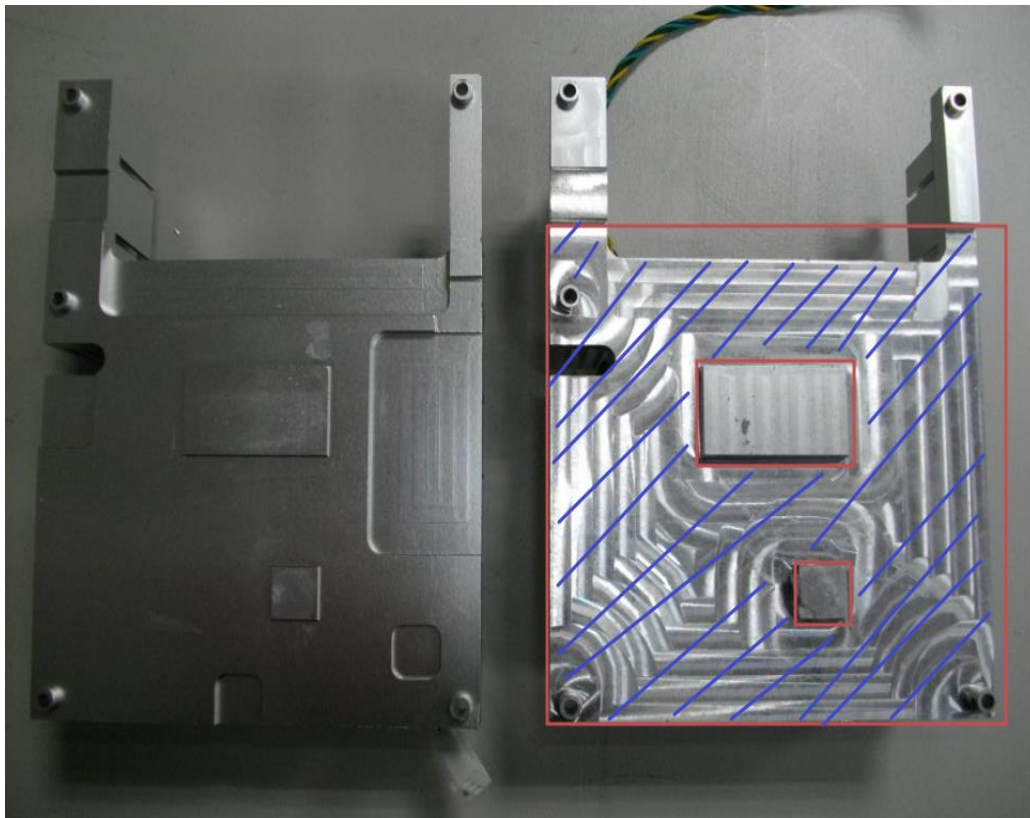
Notification:

There are some ESD/EMI solutions that users have to consider before designing your own Thermal solutions or Carrier board

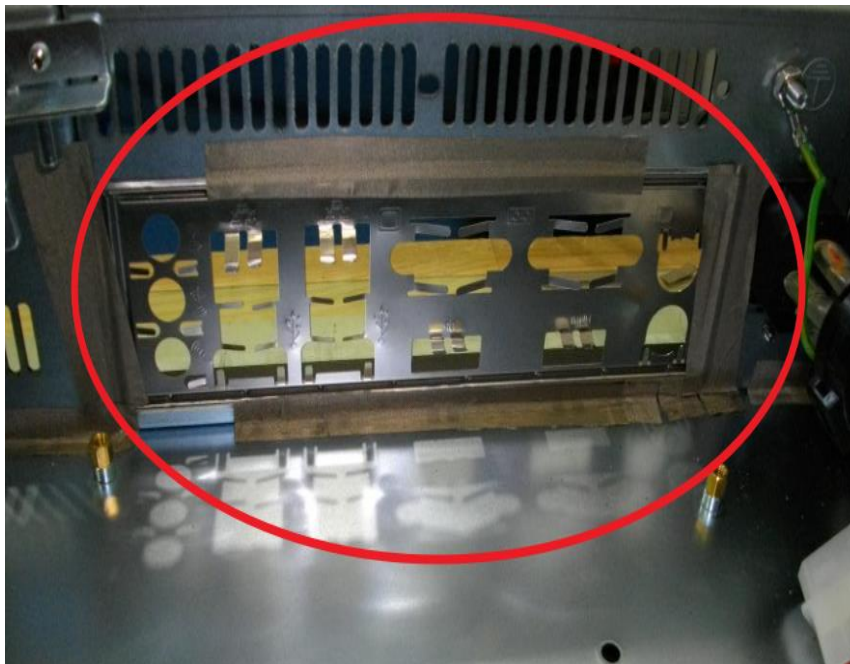
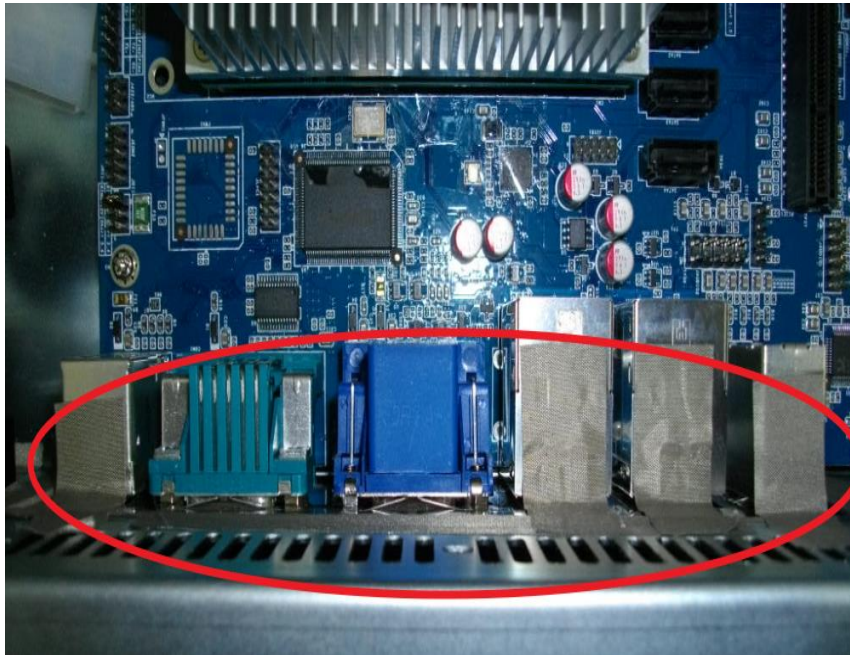
Thermal solution:

- Except CPU and Chipset contact area (Red Square), keep blue shade surface 5.45mm away from surface of ESM-QM87.

Please refer to the pictures shown as below



- Make sure IO of your carrier board is grounding with chassis. You can implement conductive tape or EMI gasket.



ESM-QM87

- Make sure Hard disk is grounding with metal chassis. You can use conductive tape, metal bracket, or EMI gasket.



- Make sure trimming wires well; if it is not necessary please do not let your wire across the carrier board.

