

ARC-1709

17" Intel® Celeron® J1900 Processor Fanless Rugged Touch
Panel PC

Quick Reference Guide

1st Ed – 17 February 2020

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FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

(1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.

(2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRE OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

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We want you to get the maximum performance from your products. So if you run into technical difficulties, we are here to help. For the most frequently asked questions, you can easily find answers in your product documentation. These answers are normally a lot more detailed than the ones we can give over the phone. So please consult the user's manual first.

To receive the latest version of the user's manual; please visit our Web site at:

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1. Getting Started

1.1 Safety Precautions

Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

1.2 Packing List

- 1 x ARC-1709
- 1 x power adapter
- 1 x power cord
- 4 x screws for VESA
- 4 x screws for HDD
- 8 x screws for wall mount
- 8 x brackets for wall mount
- 8 x plastics spacer for wall mount



If any of the above items is damaged or missing, contact your retailer.

1.3 System Specifications

Component	
Mother Board	EMX-BYT2
CPU	Onboard Intel® Celeron® J1900 BGA Processor
CPU Cooler (Type)	Fanless Heatsink
Memory	Two 204-pin DDR3L SODIMM Socket, Supports Up to 8GB DDR3L 1600MTs SDRAM
Power Supply	DC in
Adapter	60W power adaptor
Speaker	3 W*2
Wireless LAN	Optional USB or PCIe module
Bluetooth	Optional
Operating System	Ubuntu, Windows 10
Expansion Card	1 x mini-PCIe Socket
Storage	
Hard Disk Drive	1 x 2.5" Drive Bay
Solid State Drive	1 x 2.5" Drive Bay
Other Storage Device	1 x mSATA supports on the 1st Mini PCIe slot, Auto switch for mSATA or Mini PCIe
Panel	
LCD Panel	17" AUO M170ETN01.1 250nits 1280*1024 (C9689R1701R)
B/L Inverter/Converter	LCD built in
Touch Screen	17"Capacitive Touch Panel (C968XC170T0R)
Touch Controller	ICI2510P.C3.P2.V1 (C968XC170C0R)
External I/O	
Serial Port	6 x COM (default: 1RS485, 5RS232)
USB Port	4 x USB3.0
DIO Port	8-bit GPIO
Audio Port	Realtek ALC662 supports 2-CH
LAN Port	2 x Intel I211AT Gigabit Ethernet
Wireless LAN Antenna	2 x Antenna
Indicator Light	Power LED for
Expansion Slots	1 x mini PCIe full size (support half size) 1 x mSATA supports on the 1st Mini PCIe slot, Auto switch for mSATA or Mini PCIe
Mechanical	
Power Type	AT/ATX

Power Requirement	DC +12V
ACPI	Single power ATX Support S0, S3, S4, S5 and ACPI 3.0 Compliant
Power Connector Type	2.5mm Lockable DC Jack
Dimension	392.7 x 325.3 x 71.65 mm
Weight	5.05 Kgs
Color	Black
Fanless	Yes
OS Support	Linux, Ubuntu, Windows 10
Reliability	
EMI Test	CE/ FCC class B
Dust and Rain Test	Front Panel IP65
Vibration Test	Random Vibration Operation: 1. PSD: 0.00454G²/Hz , 1.5 Grms 2. operation mode 3. Test Frequency : 5-500Hz 4. Test Axis : X,Y and Z axis 5. 30 minutes per each axis 6. IEC 60068-2-64 Test:Fh 7. Storage : CF or SSD Sine Vibration test (Non-operation) 1 Test Acceleration : 2G 2 Test frequency : 5~500 Hz 3 Sweep : 1 Oct/ per one minute. (logarithmic) 4 Test Axis : X,Y and Z axis 5 Test time :30 min. each axis 6 System condition : Non-Operating mode 7. Reference IEC 60068-2-6 Testing procedures Package vibration test 1. PSD: 0.026G²/Hz , 2.16 Grms 2. Non-operation mode 3. Test Frequency : 5-500Hz 4. Test Axis : X,Y and Z axis 5. 30 min. per each axis 6. IEC 60068-2-64 Test:Fh
Mechanical Shock Test	1. Wave form : Half Sine wave 2. Acceleration Rate : 20g for operation mode 3. Duration Time : 11ms

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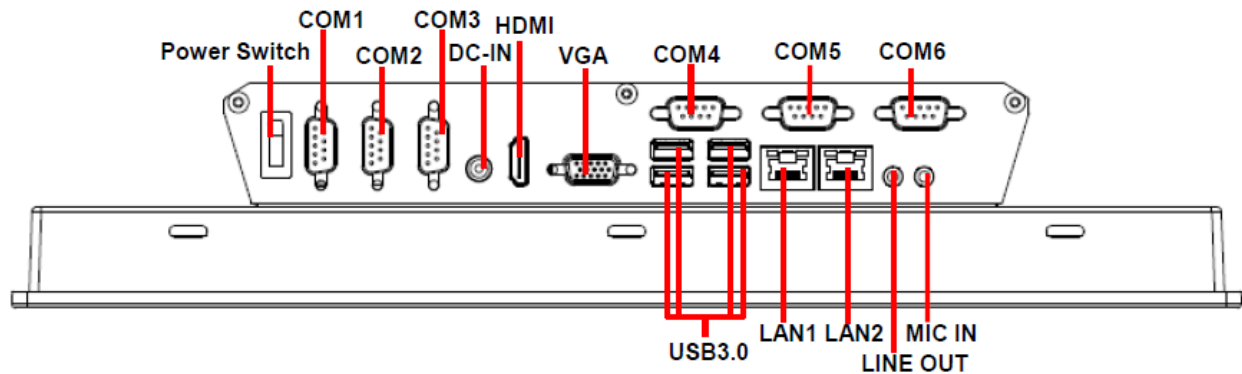
	4. No. of Shock : +/- X,Y,Z axis 3 times 5. Test Axis: +/- X,Y,Z axis 6. Operation mode 7. Reference IEC 60068-2-27 Testing procedures Test Eb : Shock Test
Drop Test	Package drop test 1 One corner , three edges, six faces 2 ISTA 2A, IEC-60068-2-32 Test:Ed
Operating Temperature	0 ~ +50 degree C
Operating Humidity	0 ~ 90% Relative Humidity, Non-condensing
Storage Temperature	-20 ~ 60 degree



Note: Specifications are subject to change without notice.

1.4 System Overview

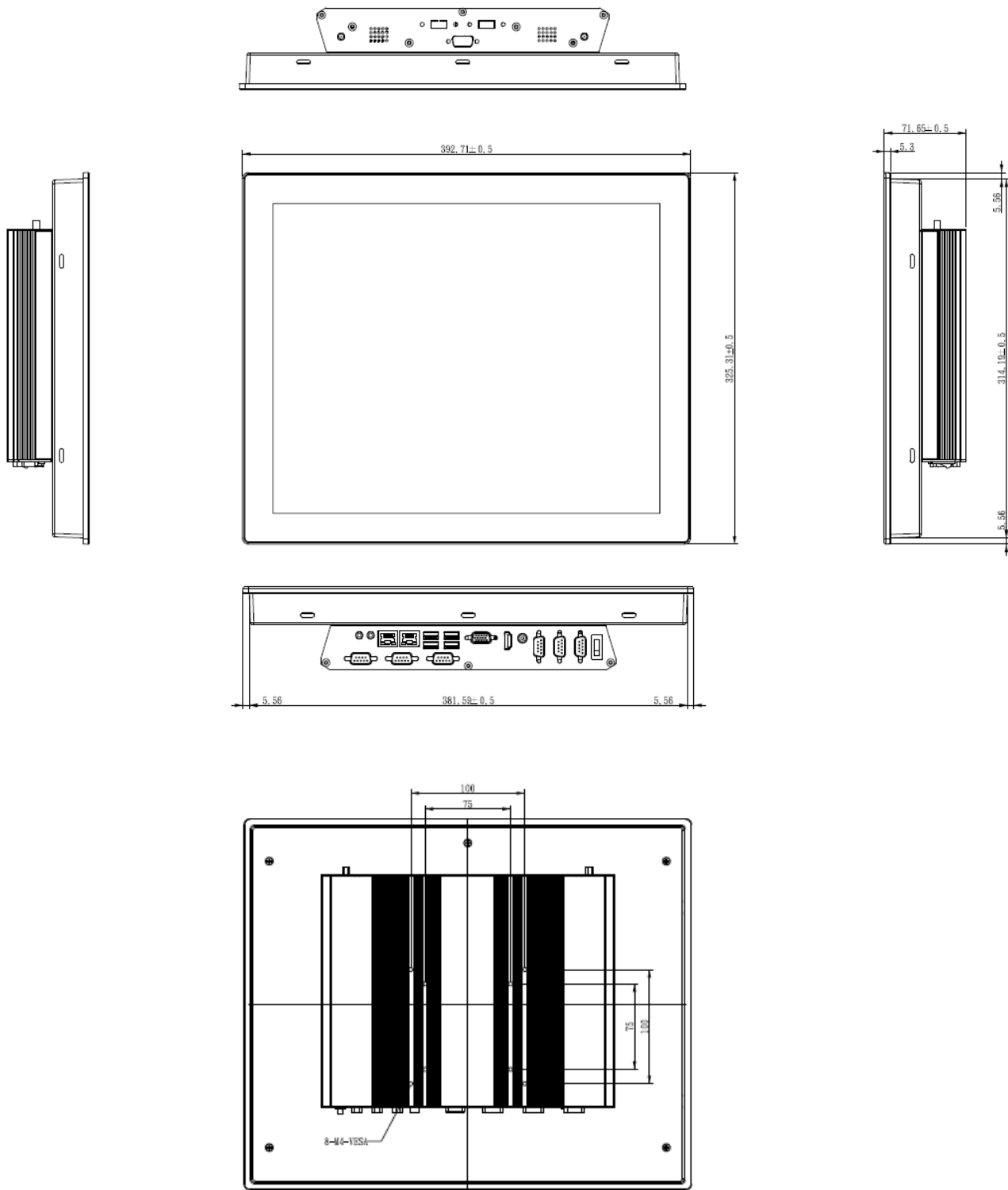
1.4.1 I/O View



Connectors

Label	Function	Note
DC-IN	DC Power-in connector	
COM	6 x Serial port connector (default: 1RS485,5RS232)	DB-9 male connector
VGA	VGA connector	
USB	4 x USB 3.0 connector	
LAN1/2	RJ-45 Ethernet 1/2	
HDMI	HDMI connector	
MIC IN	Mic-in audio jack	
LINE OUT	Line-out audio jack	
Power Switch	Power on button	

1.5 System Dimensions



(Unit: mm)

2. Hardware Configuration

For advanced information, please refer to:

- 1- EMX-BYT2 User's Manual.

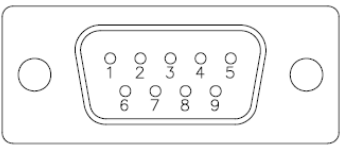
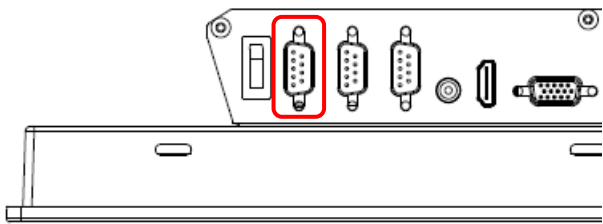


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2.1 ARC-1709 connector mapping

2.1.1 Serial port 1 connector (COM1)



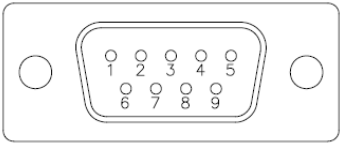
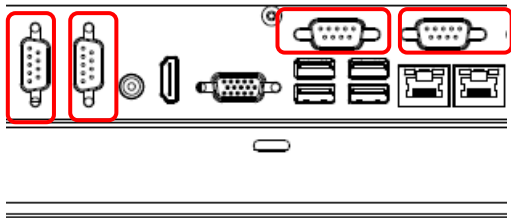
RS-232

Signal	PIN	PIN	Signal
DCD	1	6	DSR
RXD	2	7	RTS
TXD	3	8	CTS
DTR	4	9	RI
GND	5		

RS-422/485

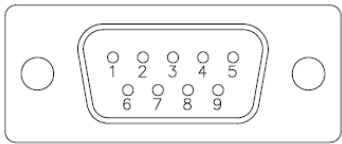
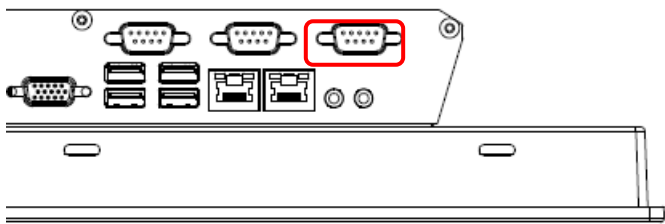
Signal	PIN	PIN	Signal
		5	GND
422RX+	4	3	485TX+
422RX-	2	1	485TX-

2.1.2 Serial port 2/3/4/5 connector (COM2/3/4/5)



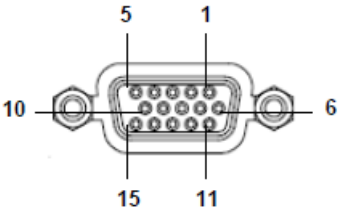
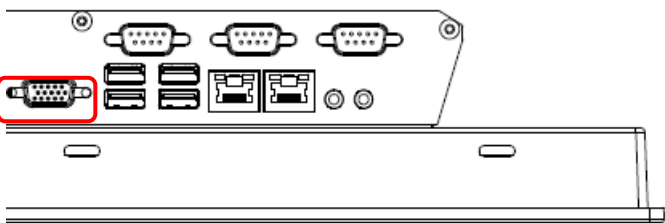
Signal	PIN	PIN	Signal
DCD	1	6	DSR
RXD	2	7	RTS
TXD	3	8	CTS
DTR	4	9	RI
GND	5		

2.1.3 Serial port 6 connector (COM6)



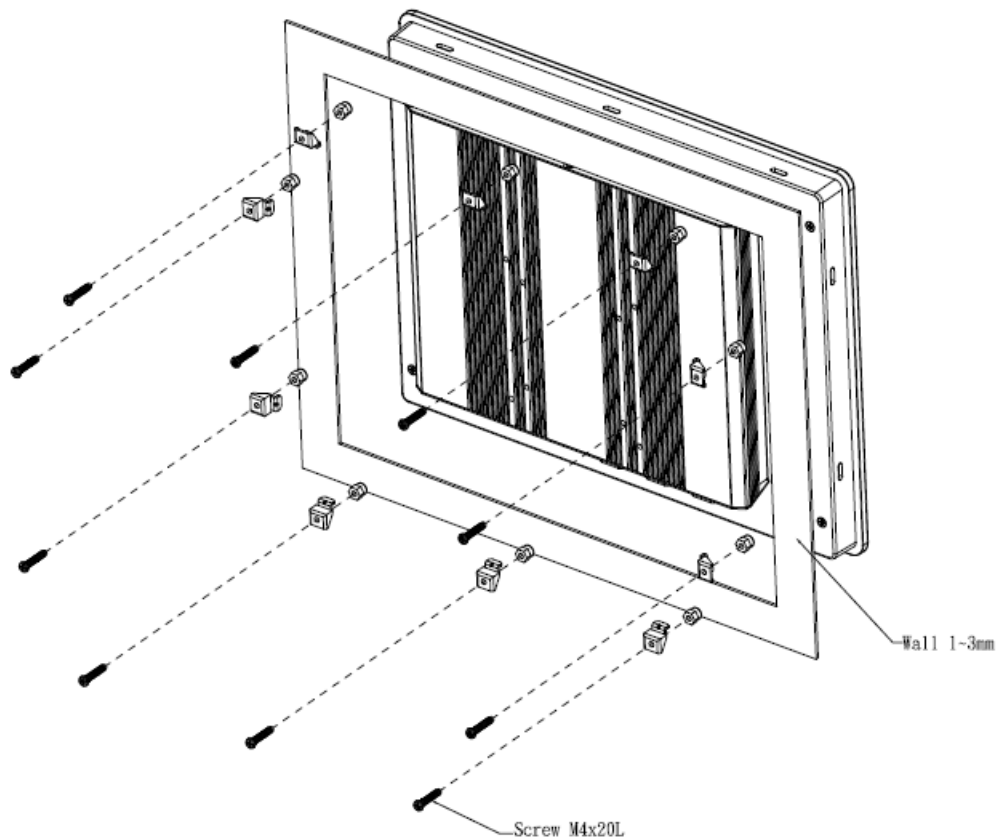
Signal	PIN	PIN	Signal
GPIO1	1	6	GPIO6
GPIO2	2	7	GPIO7
GPIO3	3	8	GPIO8
GPIO4	4	9	+5V
GPIO5	5		

2.1.4 VGA connector (VGA)



PIN	Signal	PIN	Signal	PIN	Signal
1	RED	6	GND	11	NC
2	GREEN	7	GND	12	DDCDAT
3	BLUE	8	GND	13	HSYNC
4	NC	9	+5V	14	VSYSN
5	GND	10	GND	15	DDCCLK

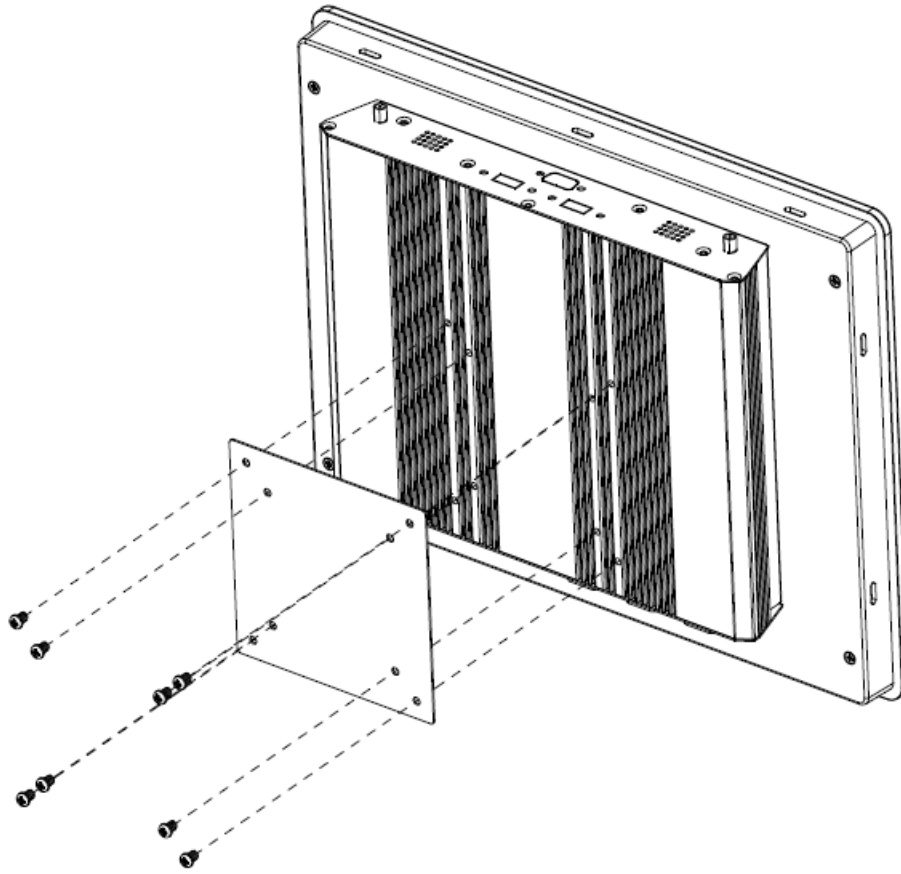
2.2 Installing Mounting Brackets (ARC-1709)



Step 1. Position brackets on both sides, matching the holes on the system.

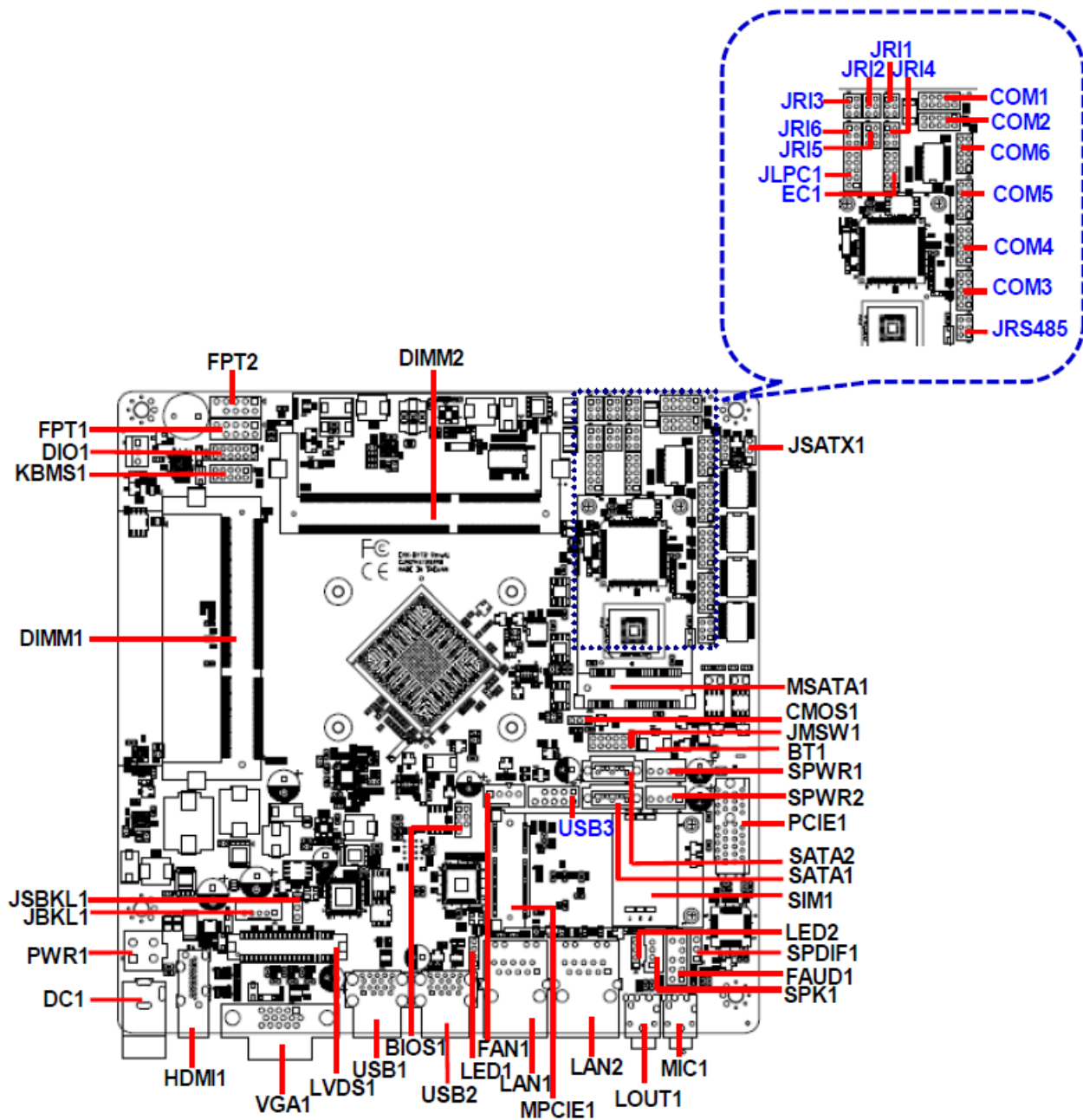
Step 2. Insert and fasten screws on each side of the system to secure Mounting brackets.

2.3 Installing VESA Mounting (ARC-1709)



Step 1. Insert and fasten screws on each side of the system to secure VESA Mounting.

2.4 Product Overview



- Main Memory

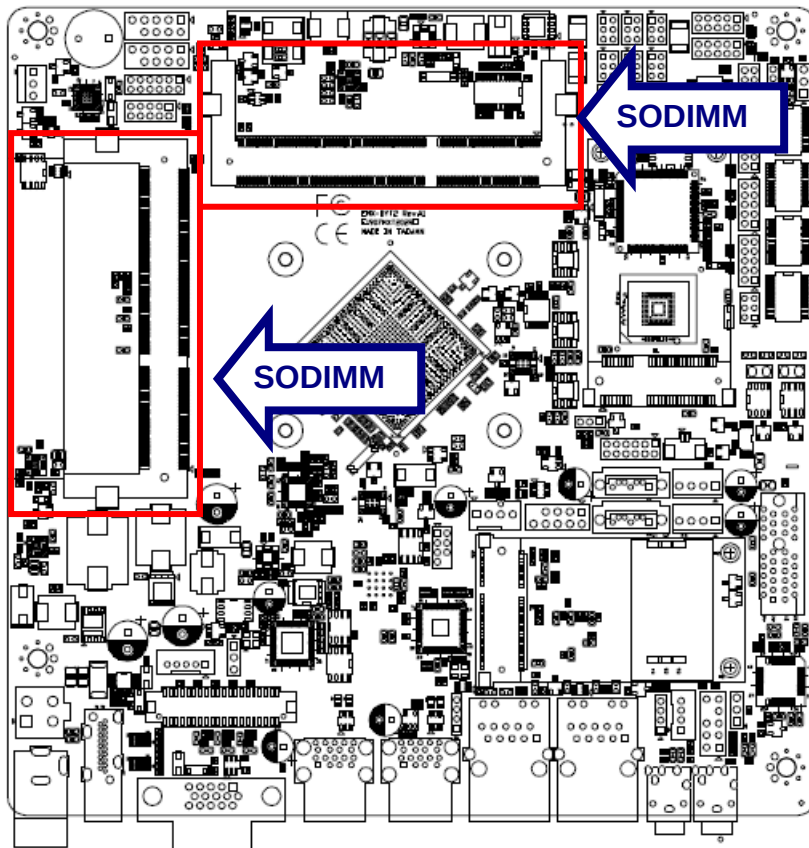
EMX-BYT2 provides 2 x 204-pin DDR3L 1333MHz SODIMMs.

- SODIMM module.



Note:

The Platform requires DDR3L SODIMMs to be populated starting with the SODIMM at DIMM1.



- SD Card

USB Low & Full speed (@3.3V) Transmit traffic AF% less than 10% (max 50TB over lifespan) per port or use only High Speed or Super Speed USB devices USB Receive traffic is not affected. Connecting Low or Full Speed devices through a USB2 High Speed (or greater) hub mitigates the issue.

- SD Card (operating at 3.3V) AF% less than 10%. UHS-1 cards operating at 1.8V are not affected.

Usage of SD cards operating at 3.3V should be minimized and usage should be limited to UHS-I type SD cards operating at 1.8V. Inserting the SD card, and leaving it in the system, as extended storage, reduces the life of the interface. Occasional usage of SD card,

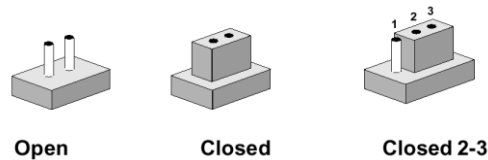
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inserting the card –transferring data to or from the SD card-then removing the SD card, is not a concern. Customers have the option to enable a default D3 device setting to extend the life of the SD card interface, if supported by the OS.

2.5 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board's jumpers and connectors.

Jumpers

Label	Function	Note
JRI1/2/3/4/5/6	Serial port 1/2/3/4/5/6 pin9 signal select	3 x 2 header, pitch 2.00mm
JMSW1	SATA2/MSATA1 mPCIe slot selector	6 x 2 header, pitch 2.00mm
JSBKL1	LVDS Back Light power selection	3 x 1 header, pitch 2.54mm
JSATX1	AT/ATX Power Mode Select	3 x 1 header, pitch 2.54mm
CMOS1	Clear CMOS	3 x 1 header, pitch 2.00mm

Connectors

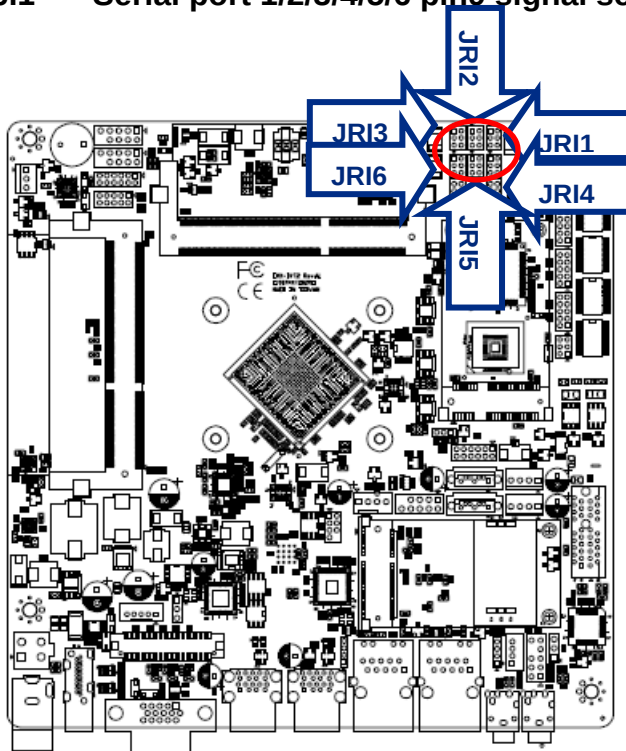
Label	Function	Note
FAN1	CPU fan connector	4 x 1 wafer, pitch 2.54mm
FPT1	Miscellaneous setting connector 1	5 x 2 header, pitch 2.54 mm
FPT2	Miscellaneous setting connector 2	5 x 2 header, pitch 2.54 mm
DIMM1/2	204-pin DDR3L DIMM socket	
FAUD1	Front Audio connector	5 x 2 header, pitch 2.54 mm

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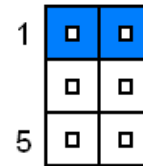
JBKL1	LCD Inverter connector	5 x 1 wafer, pitch 2.00mm
BIOS1	BIOS connector	4 x 2 header, pitch 2.00 mm
COM1	Serial Port 1 connector	5 x 2 header, pitch 2.00mm
COM2	Serial Port 2 connector	5 x 2 header, pitch 2.00mm
COM3	Serial Port 3 connector	5 x 2 header, pitch 2.00mm
COM4	Serial Port 4 connector	5 x 2 header, pitch 2.00mm
COM5	Serial Port 5 connector	5 x 2 header, pitch 2.00mm
COM6	Serial Port 6 connector	5 x 2 header, pitch 2.00mm
DIO1	General purpose I/O connector	6 x 2 header, pitch 2.00mm
SPK1	Speaker connector	1 x 4 wafer, pitch 2.00 mm
LVDS1	LVDS Connector	DIN 40-pin wafer, pitch 1.25mm
USB1/2	USB connector 1/2	
USB3	USB connector 3	5 x 2 header, pitch 2.54mm
SPDIF1	Sony/Philips Digital Interface	3 x 1 header, pitch 2.54 mm
LAN1/2	RJ-45 Ethernet 1/2	
PCIE1	PCIe connector	
LED1	LED indicator connector 1	4 x 1 header, pitch 2.00mm
LED2	LED indicator connector 2	4 x 1 header, pitch 2.00mm
KBMS1	PS/2 keyboard & mouse connector	5 x 2 header, pitch 2.00 mm
BT1	Battery connector	2 x 1 wafer, pitch 1.25mm
MSATA1	Full size mPCIe Slot	
MPCIE1	Mini-PCIe connector 1	
JRS485	Serial Port 1 RS485/422 Mode connector	3 x 2 header, pitch 2.00 mm
JLPC1	LPC connector	5 x 2 header, pitch 2.00mm
PWR1	Power connector	2 x 2 wafer, pitch 4.20mm
SATA1	Serial ATA connector 1	
SATA2	Serial ATA connector 2	
SPWR1/2	SATA Power connector 1/2	4 x 1 wafer, pitch 2.54mm
EC1	EC_Program	5 x 2 header, pitch 2.00 mm
DC1	DC Power-in connector	
SIM1	SIM card slot	
HDMI1	HDMI connector	
LOUT1	Line-out audio jack	
MIC1	Mic-in audio jack	
VGA1	VGA connector	

2.6 Setting Jumpers & Connectors

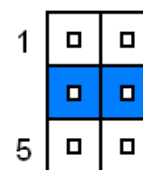
2.6.1 Serial port 1/2/3/4/5/6 pin9 signal select (JRI1/JRI2/JRI3/JRI4/JRI5/JRI6)



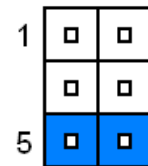
Ring*



+5V

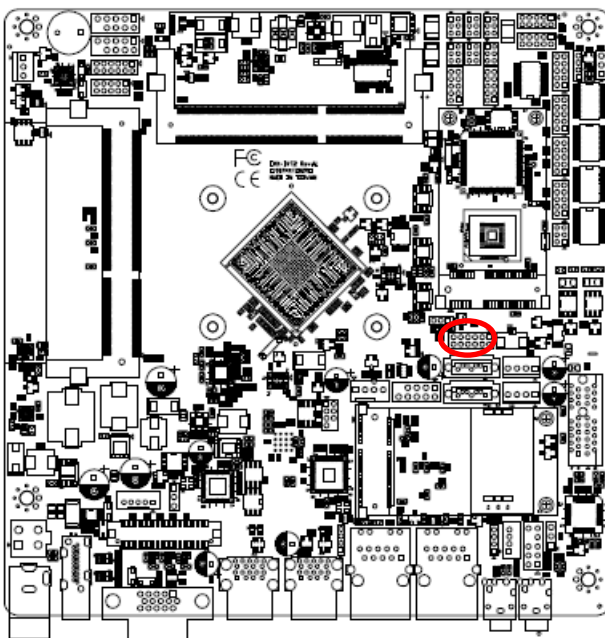


+12V



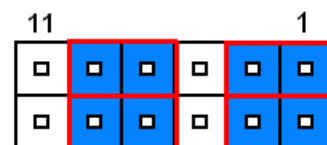
* Default

2.6.2 SATA2/MSATA1 mPCIe slot selector (JMSW1)



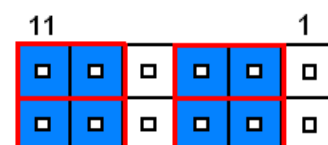
SATA2 Connector *

(SATA2 Connector enabled, MSATA1 slot Disabled)



MSATA1 mPCIe slot

(MSATA1 slot enabled, SATA2 Connector Disabled)

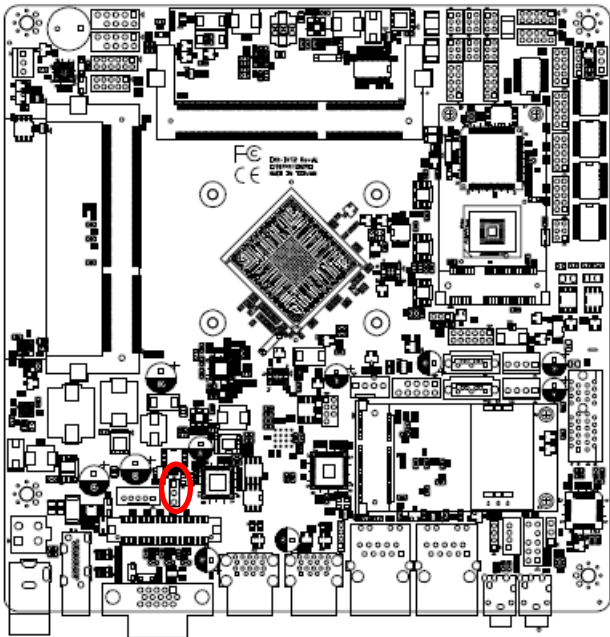


* Default

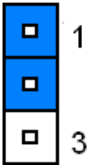
Note:

SATA2/MSATA1 shared SATA signal, can not be used simultaneously.

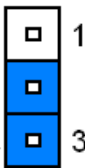
2.6.3 LVDS Back Light power selection (JSBKL1)



PWM Mode*(Max current: 2A)

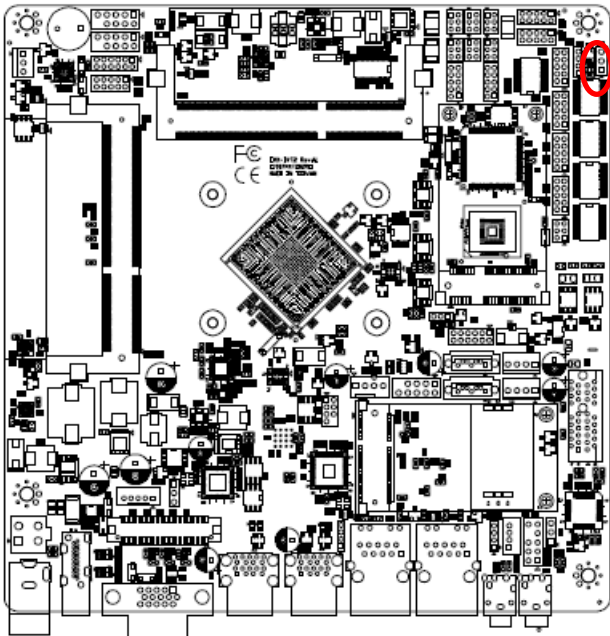


DC Mode(Max current: 2A)

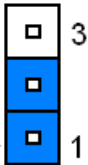


* Default

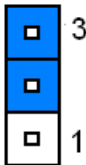
2.6.4 AT/ATX Power Mode Select (JSATX1)



ATX*

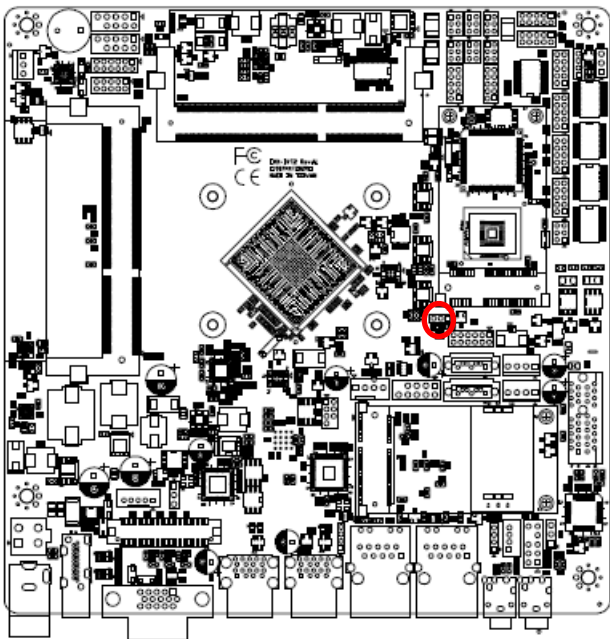


AT



* Default

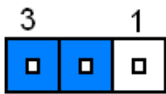
2.6.5 Clear CMOS (CMOS1)



Protect*

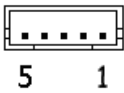
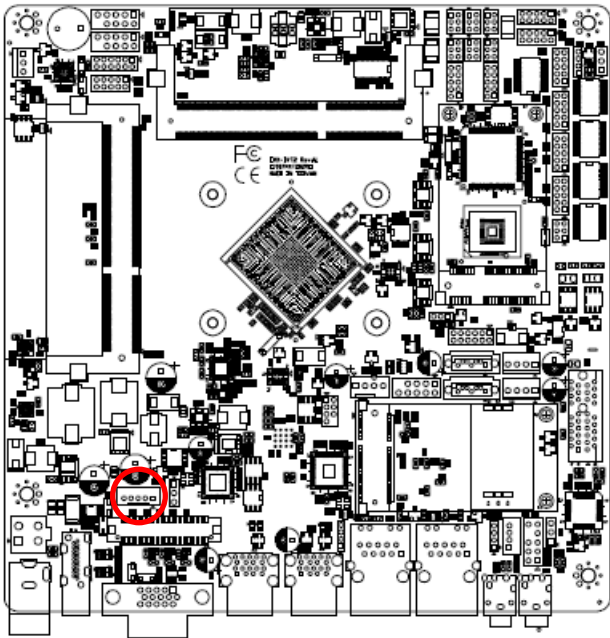


Clear CMOS



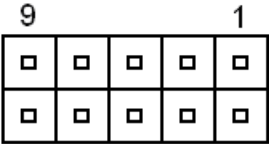
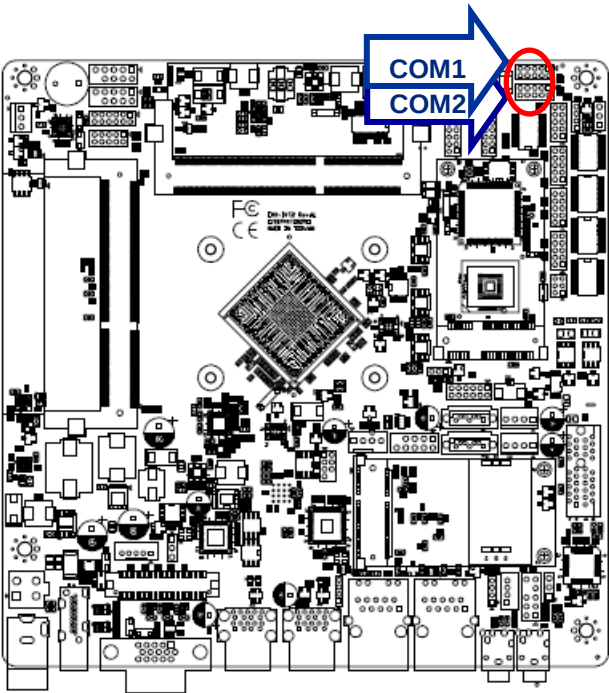
* Default

2.6.6 LCD Inverter connector (JBKL1)



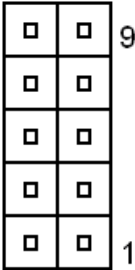
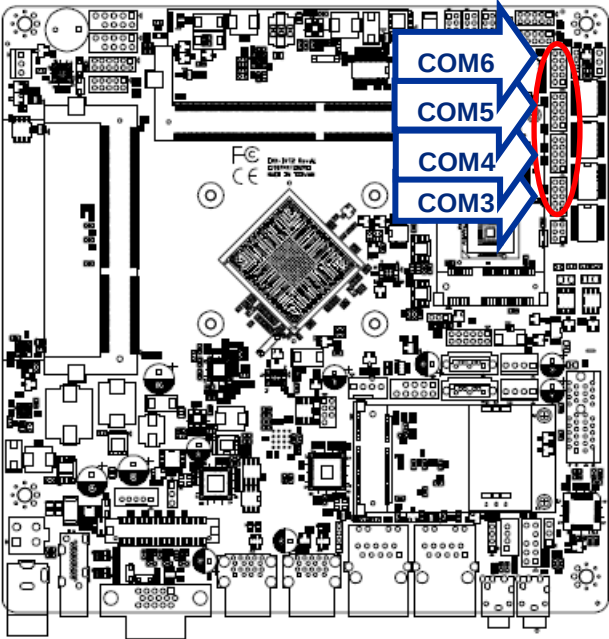
PIN	Signal	Max current
1	+12V	2A
2	GND	
3	LVDS_BKLTEN	
4	LVDS_BKLADJ	
5	+5V	2A

2.6.7 Serial port 1/2 connector (COM1/2)



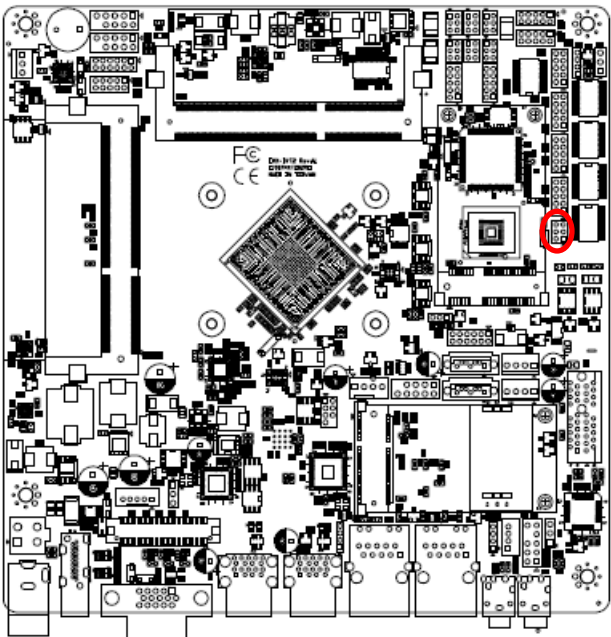
Signal	PIN	PIN	Signal
DCD	1	2	RXD
TXD	3	4	DTR
GND	5	6	DSR
RTS	7	8	CTS
RI	9	10	NC

2.6.8 Serial port 3/4/5/6 connector (COM3/4/5/6)



Signal	PIN	PIN	Signal
NC	10	9	RI
CTS	8	7	RTS
DSR	6	5	GND
DTR	4	3	TXD
RXD	2	1	DCD

2.6.9 Serial Port 1 RS485/422 Mode connector (JRS485)



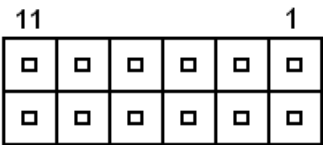
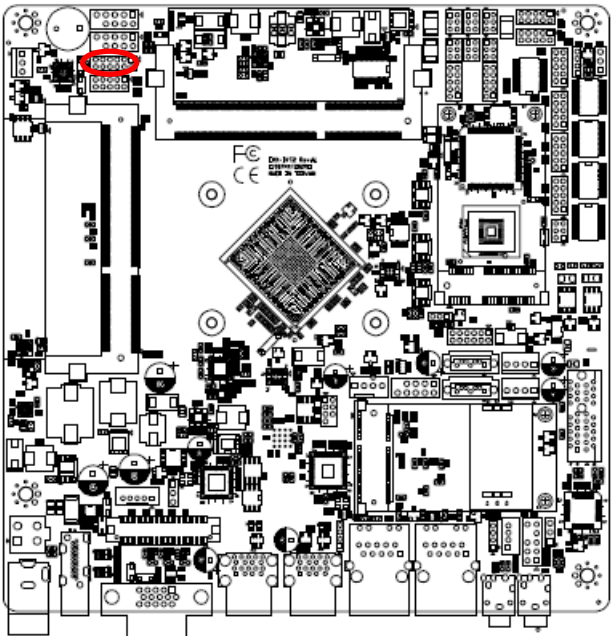
RS-422

Signal	PIN	PIN	Signal
GND	6	5	+5V
422RX+	4	3	422TX+
422RX-	2	1	422TX-

RS-485

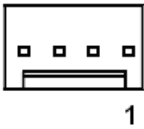
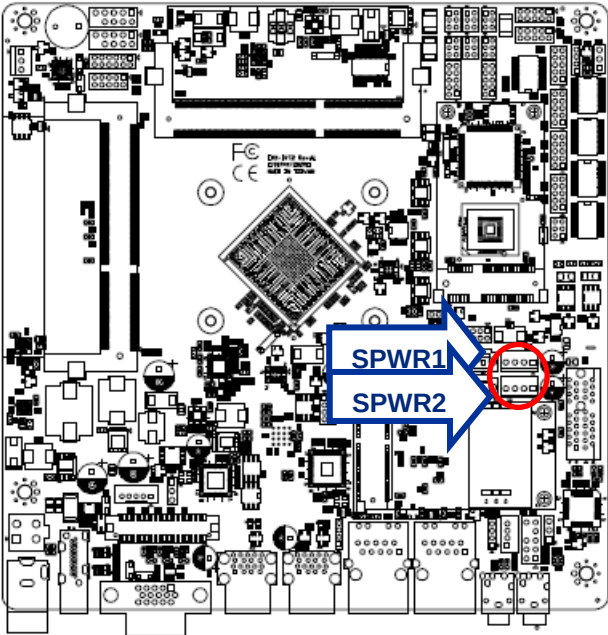
Signal	PIN	PIN	Signal
GND	6	5	+5V
NC	4	3	485TX+
NC	2	1	485TX-

2.6.10 General purpose I/O connector (DIO1)



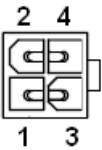
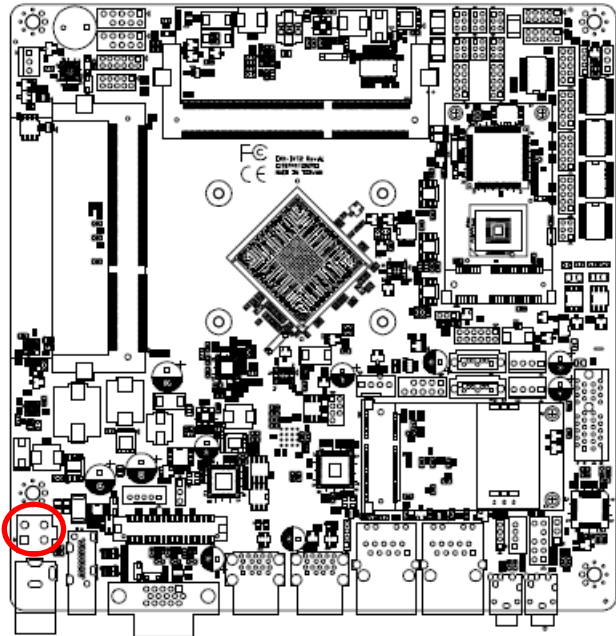
Signal	PIN	PIN	Signal
DI0	1	2	DO0
DI1	3	4	DO1
DI2	5	6	DO2
DI3	7	8	DO3
SMB_CLK	9	10	SMB_DATA
GND	11	12	+5V

2.6.11 SATA Power connector 1/2 (SPWR1/2)



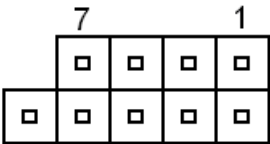
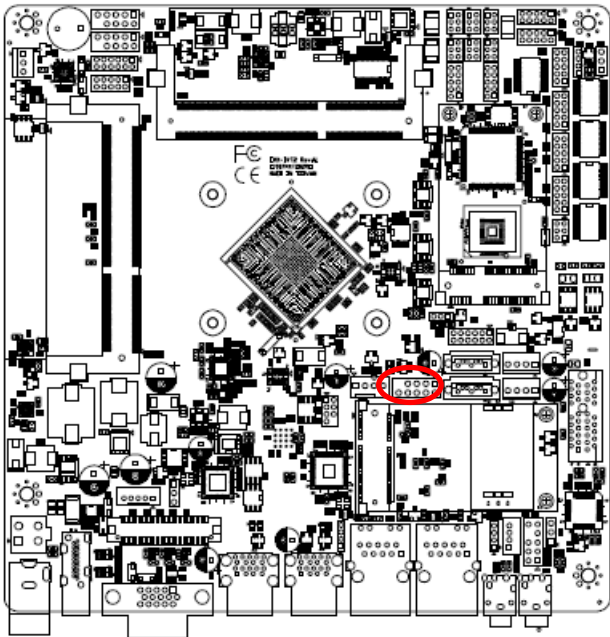
PIN	Signal	Max current
1	+V5S_SATA	3A
2	GND	
3	GND	
4	+V12S_SATA	3A

2.6.12 Power connector (PWR1)



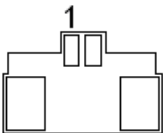
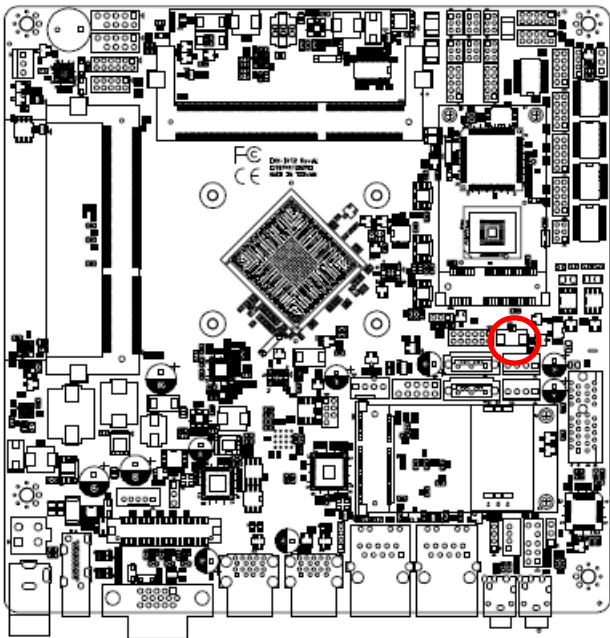
Signal	PIN	PIN	Signal
GND	1	2	GND
+VIN_12V	3	4	+VIN_12V

2.6.13 USB connector 3 (USB3)



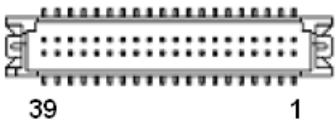
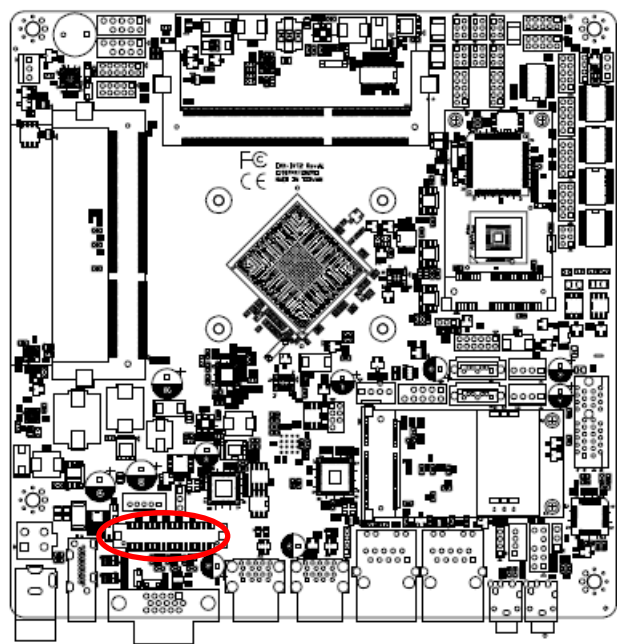
Signal	PIN	PIN	Signal
+V5A_USB01	1	2	+V5A_USB01
USB_DN0	3	4	USB_DN1
USB_DP0	5	6	USB_DP1
GND	7	8	GND
		10	NC

2.6.14 Battery connector (BT1)



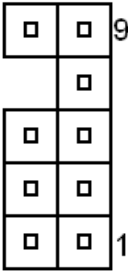
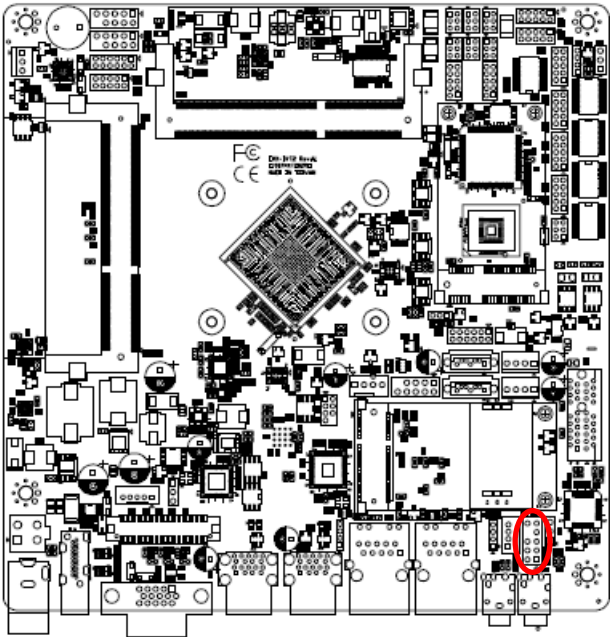
Signal	PIN
+3V	1
GND	2

2.6.15 LVDS connector (JLVDS1)



Signal	PIN	PIN	Signal
+3.3V	1	2	+5V
+3.3V	3	4	+5V
EDP_DDC_SCL	5	6	EDP_DDC_DAT
GND	7	8	GND
LVDS_DATAP1	9	10	LVDS_DATAP0
LVDS_DATAN1	11	12	LVDS_DATAN0
GND	13	14	GND
LVDS_DATAP3	15	16	LVDS_DATAP2
LVDS_DATAN3	17	18	LVDS_DATAN2
GND	19	20	GND
LVDS_DATAP5	21	22	LVDS_DATAP4
LVDS_DATAN5	23	24	LVDS_DATAN4
GND	25	26	GND
LVDS_DATAP7	27	28	LVDS_DATAP6
LVDS_DATAN7	29	30	LVDS_DATAN6
GND	31	32	GND
LVDS_CLK2P	33	34	LVDS_CLK1P
LVDS_CLK2N	35	36	LVDS_CLK1N
GND	37	38	GND
+12V	39	40	+12V

2.6.16 Audio connector (FAUD1)

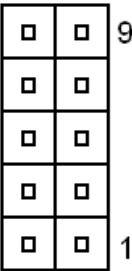
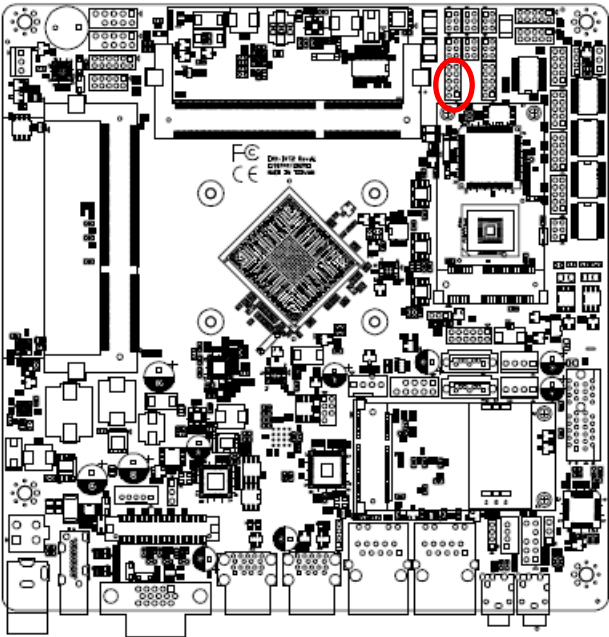


Signal	PIN	PIN	Signal
LINE2_JD	10	9	LINE2_L
		7	SENSE_B
MIC2_JD	6	5	LINE2_R
AUD_FRONT_DET	4	3	MIC2_R
GND	2	1	MIC2_L

2.3.16.1 Signal Description –Front Audio connector (FAUD1)

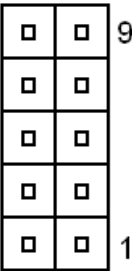
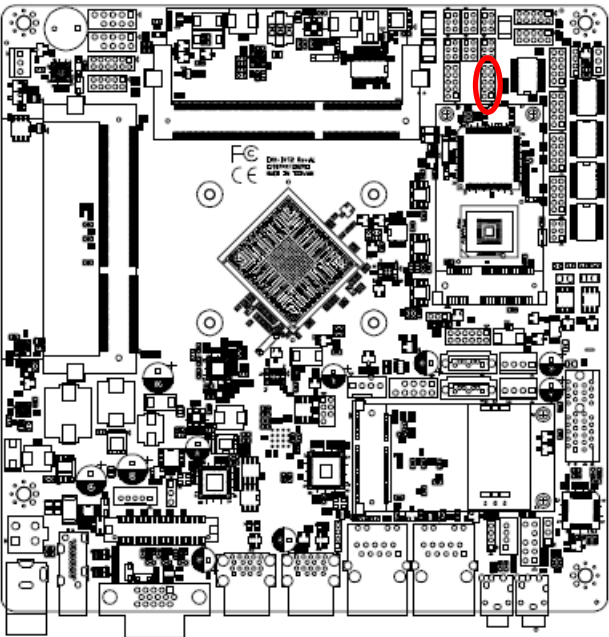
Signal	Signal Description
LINE2_JD	AUDIO IN (LINE_RIN/LIN)sense pin
MIC2_JD	MIC IN (MIC_RIN/LIN) sense pin

2.6.17 LPC connector (JLPC1)



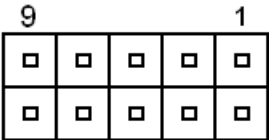
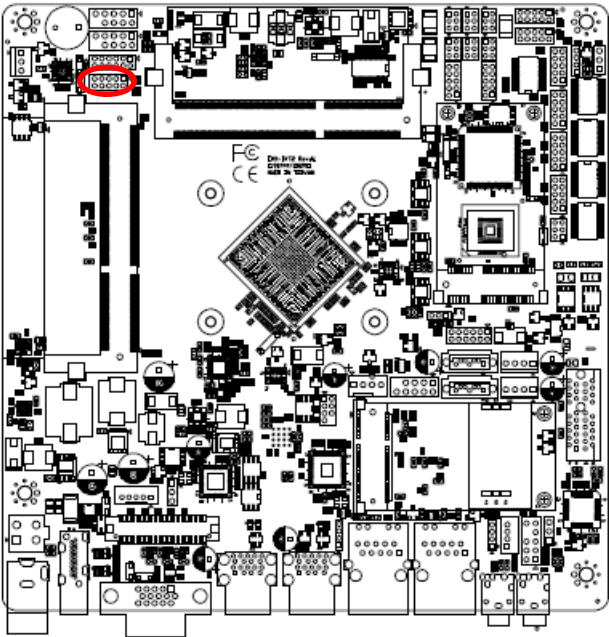
Signal	PIN	PIN	Signal
GND	10	9	LPC_SERIRQ
LPC_DEG_CLK	8	7	LPC_AD3
LPC_FRAME#	6	5	LPC_AD2
PLT_RST#	4	3	LPC_AD1
+3.3V	2	1	LPC_AD0

2.6.18 EC_Program (EC1)



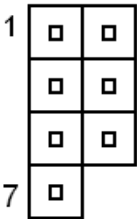
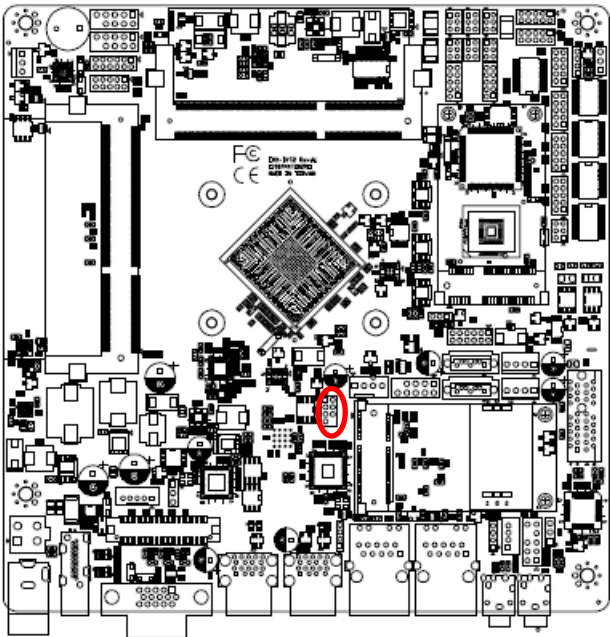
Signal	PIN	PIN	Signal
EC_SMDATA_DBG	10	9	EC_SMCLK_DBG
NC	8	7	EC_HOLD#
EC_FSMOSI	6	5	EC_FSMIOSO
EC_FSCK	4	3	EC_FSCE#
GND	2	1	+3.3A_ECSPi

2.6.19 PS/2 keyboard & mouse connector (KBMS1)



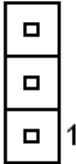
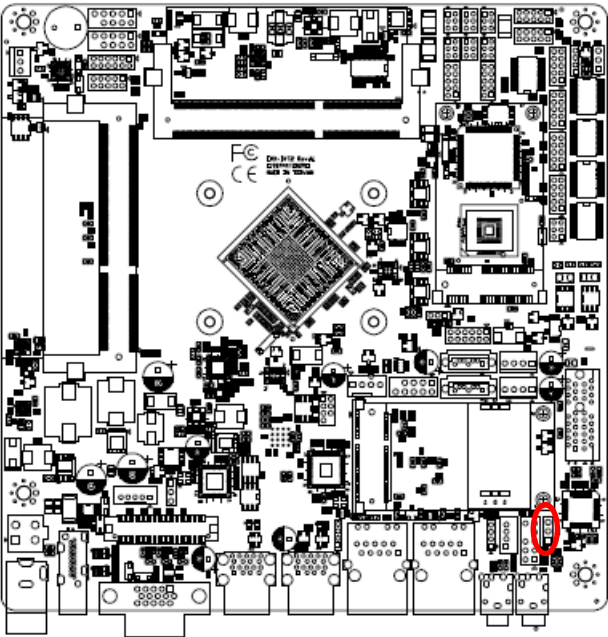
Signal	PIN	PIN	Signal
KBDAT	1	2	KBCK
GND	3	4	+5VSB
MSDAT	5	6	MSCK
NC	7	8	NC
NC	9	10	NC

2.6.20 BIOS connector (BIOS1)



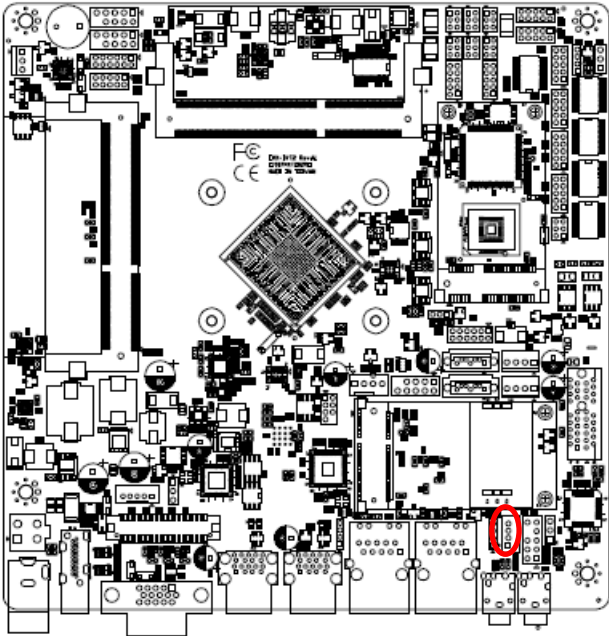
Signal	PIN	PIN	Signal
+V1.8A_SPI	1	2	GND
SPI_ROM_CS0#	3	4	SPI_ROM_CLK
SPI_ROM_R_MISO	5	6	SPI_ROM_MOSI
SPI_ROM_HOLD#	7		

2.6.21 Sony/Philips Digital Interface (SPDIF1)



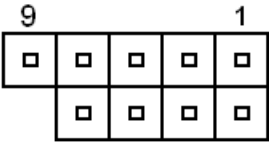
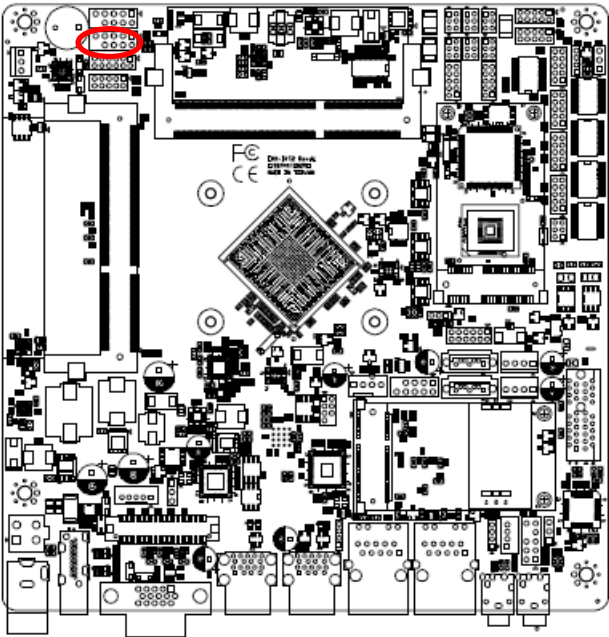
Signal	PIN
GND	3
SPDIF_OUT	2
+5V	1

2.6.22 Speaker connector (SPK1)



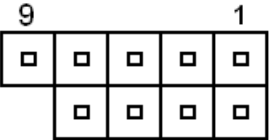
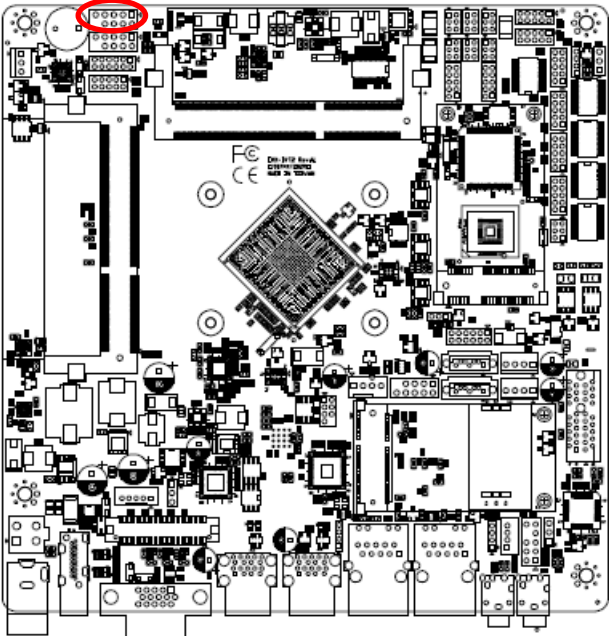
Signal	PIN
RSPK-	4
RSPK+	3
LSPK-	2
LSPK+	1

2.6.23 Miscellaneous setting connector 1 (FPT1)



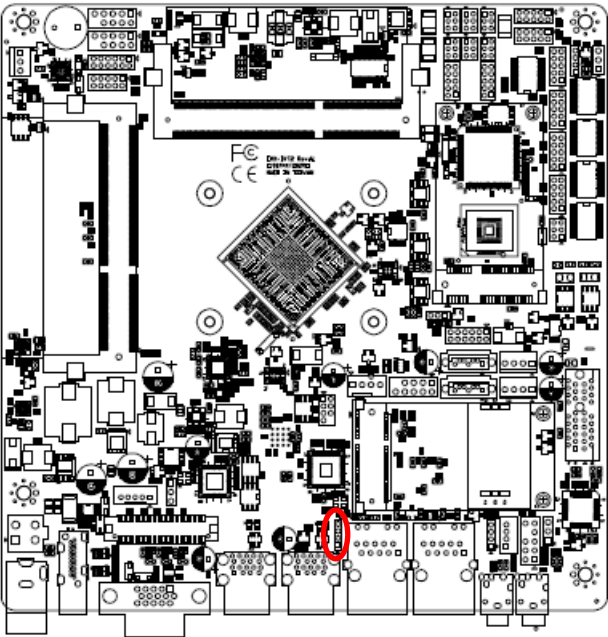
Signal	PIN	PIN	Signal
+HD_LED	1	2	+PWR_LED
-HD_LED	3	4	-PWE_LED
+Reset	5	6	+PWR_BNT
-Reset	7	8	-PWR_BNT
NC	9		

2.6.24 Miscellaneous setting connector 2 (FPT2)



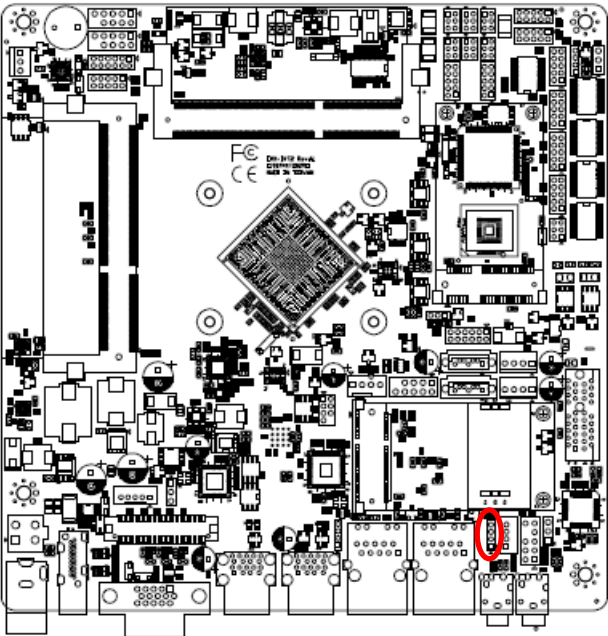
Signal	PIN	PIN	Signal
Speaker+	1	2	BLK_VR(10K)
NC	3	4	BLK_UP
NC	5	6	BLK_DN
Speaker-	7	8	GND
NC	9	10	

2.6.25 LED indicator connector 1 (LED1)



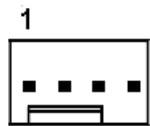
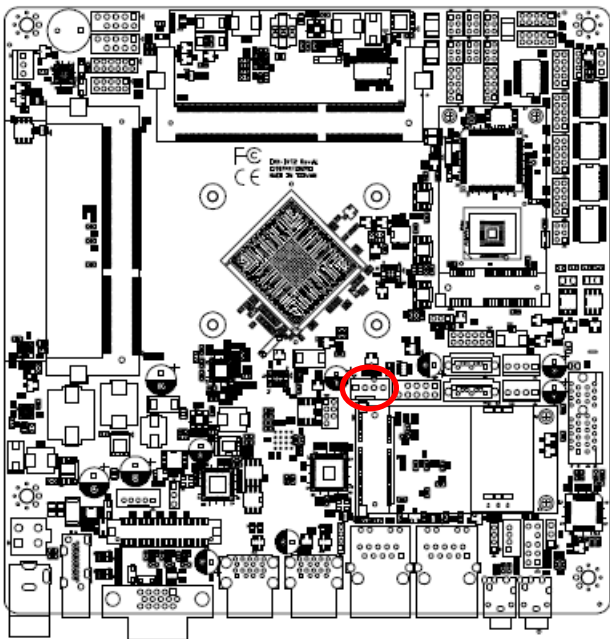
Signal	PIN
L1_1000#_LED	4
L1_100#_LED	3
L1_ACT_N	2
L1_ACT_P	1

2.6.26 LED indicator connector 2 (LED2)



Signal	PIN
L2_1000#_LED	4
L2_100#_LED	3
L2_ACT_N	2
L2_ACT_P	1

2.6.27 CPU fan connector (FAN1)



Signal	PIN
GND	1
+12V	2
CPU_FANIN	3
CPU_FANOUT	4

3. BIOS Setup

3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

3.2 Starting Setup

The AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing or <F2> immediately after switching the system on, or

By pressing the or <F2> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

Press or <F2> to enter SETUP

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to.

Press F1 to Continue, DEL to enter SETUP

3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑	Move to previous item
↓	Move to next item
←	Move to the item in the left hand
→	Move to the item in the right hand
Esc key	Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Previous Values.
F3 key	Optimized defaults
F4 key	Save & Exit Setup

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



Note: Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “➤” pointer marks all sub menus.

3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the F1 key again.

3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the NVRAM settings which resets your system to its defaults.

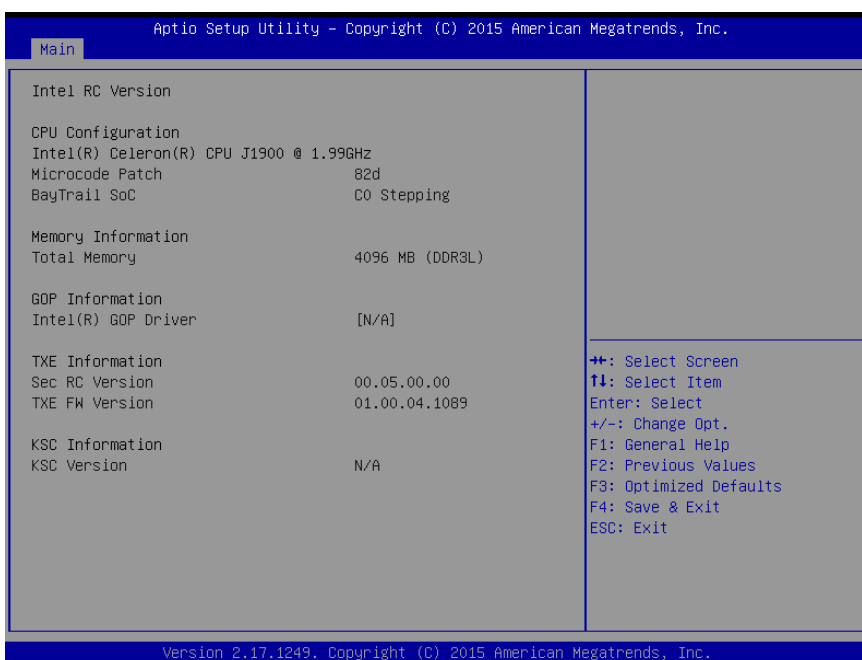
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both BIOS Vendor and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

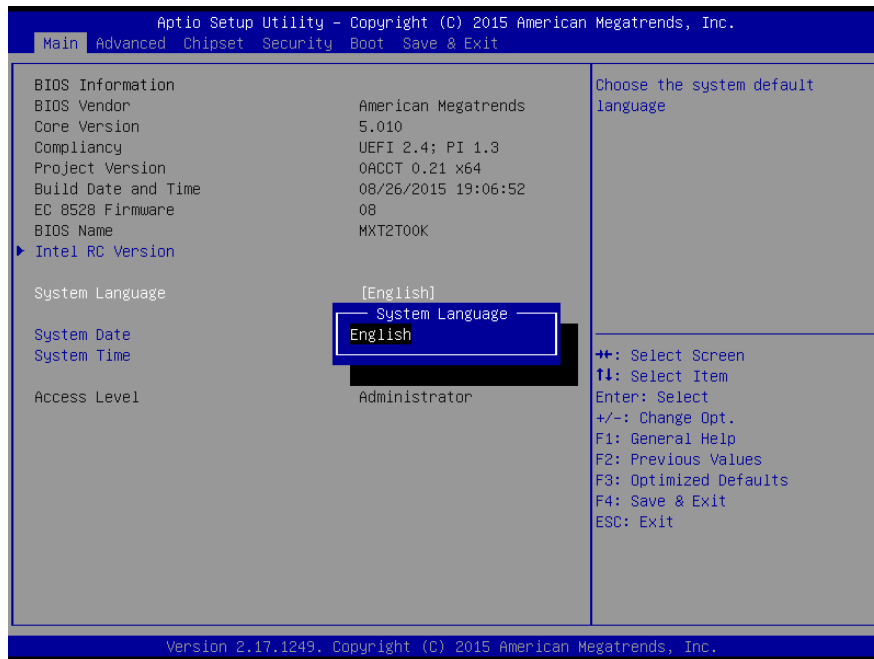
3.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.





3.6.1.1 System Language

This option allows choosing the system default language.

3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the day, month and year.

3.6.1.3 System Time

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

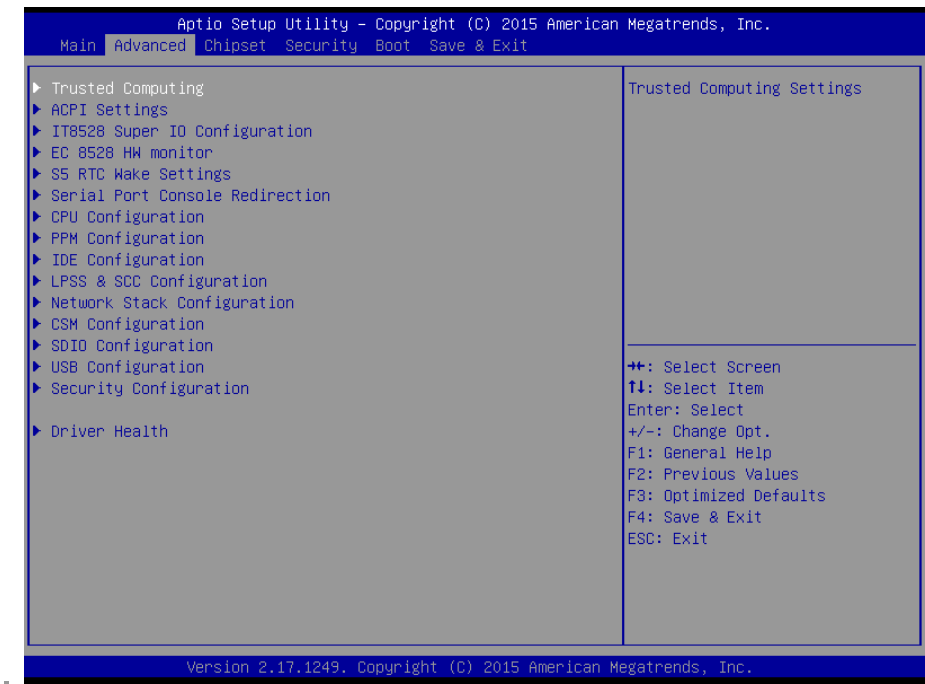


Note: The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.

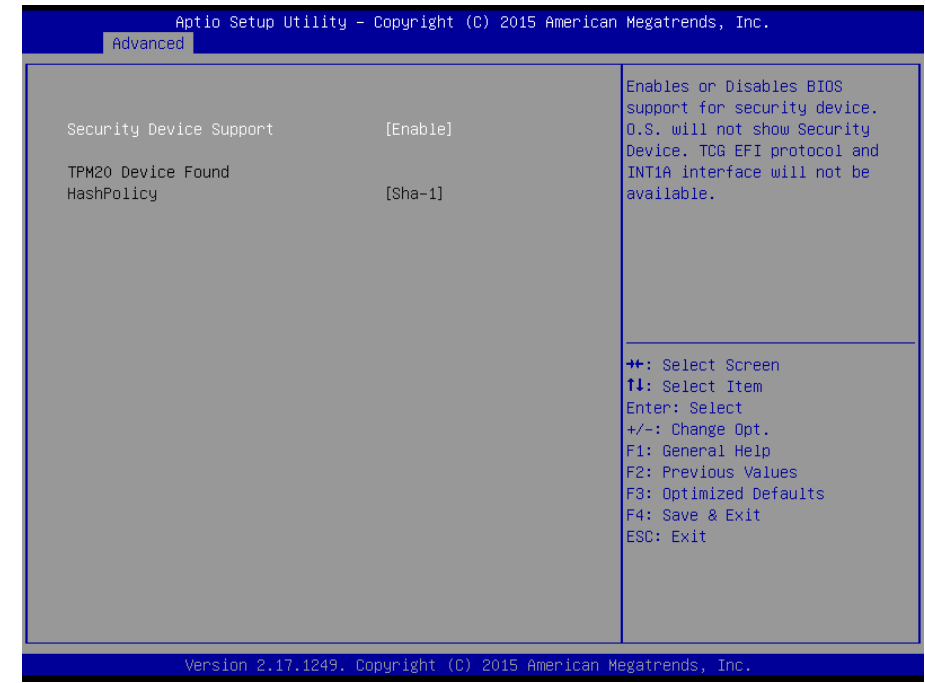
Visit the Avalue website (www.avalue.com.tw) to download the latest product and BIOS information.

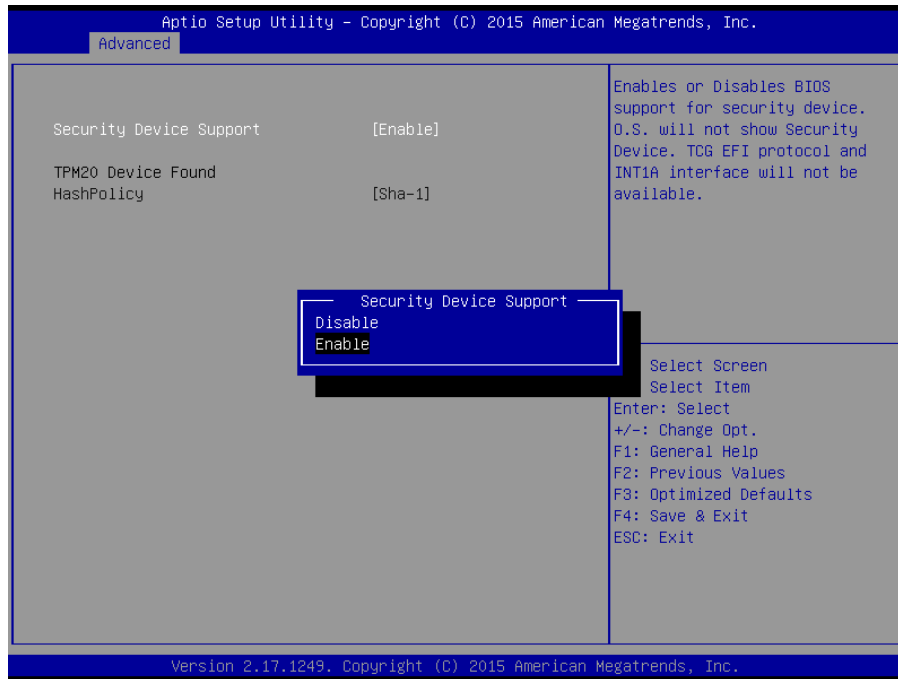
3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



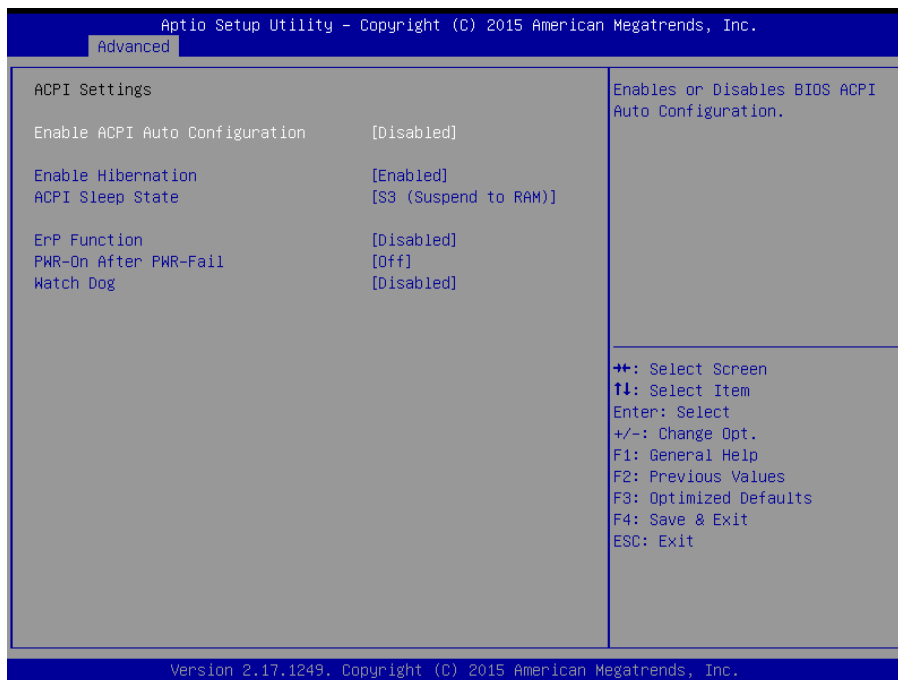
3.6.2.1 Trusted Computing

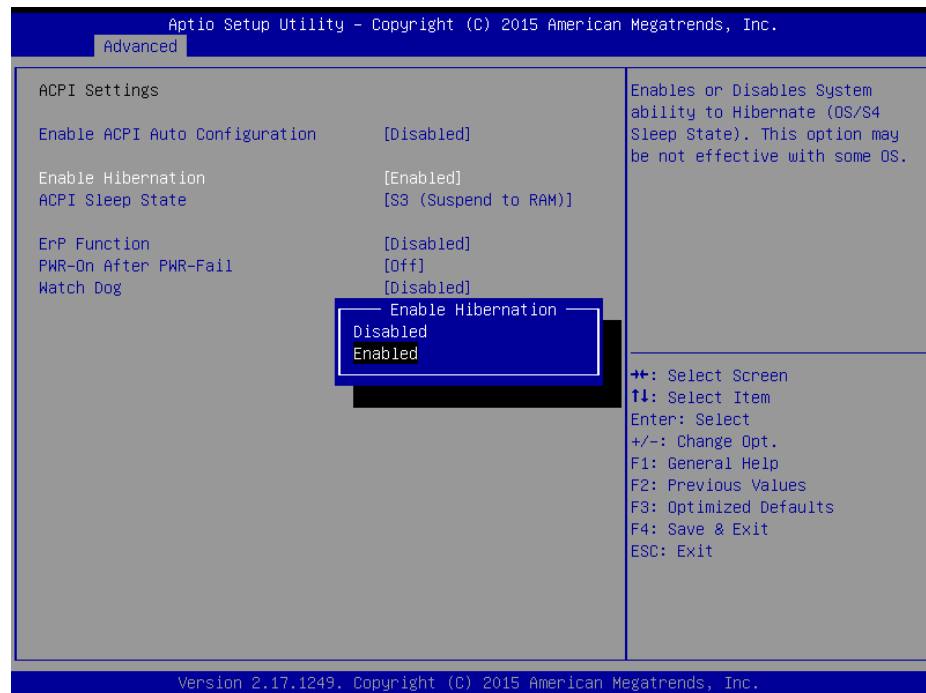
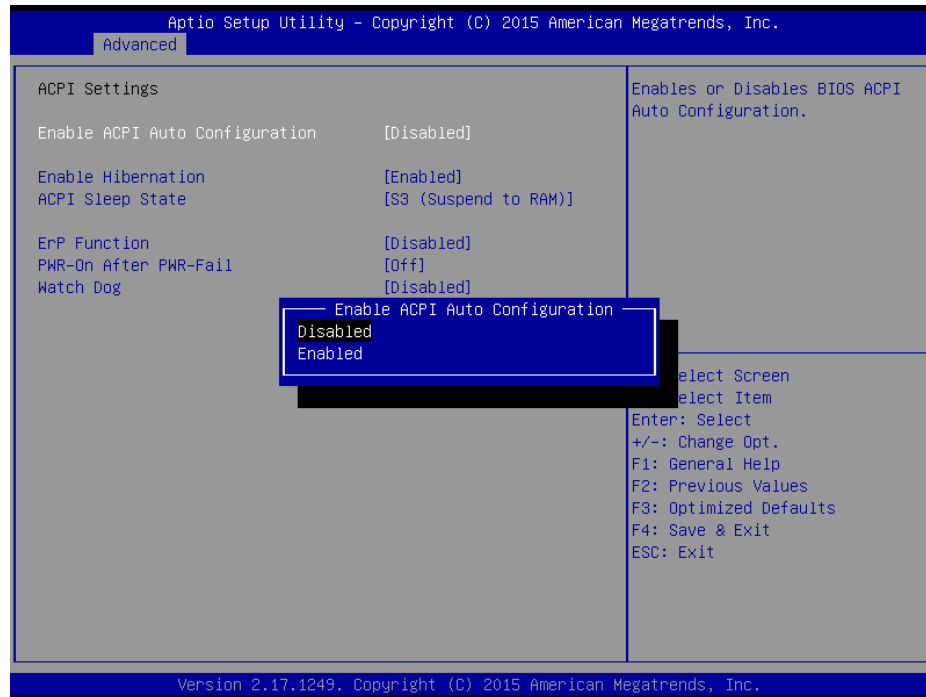


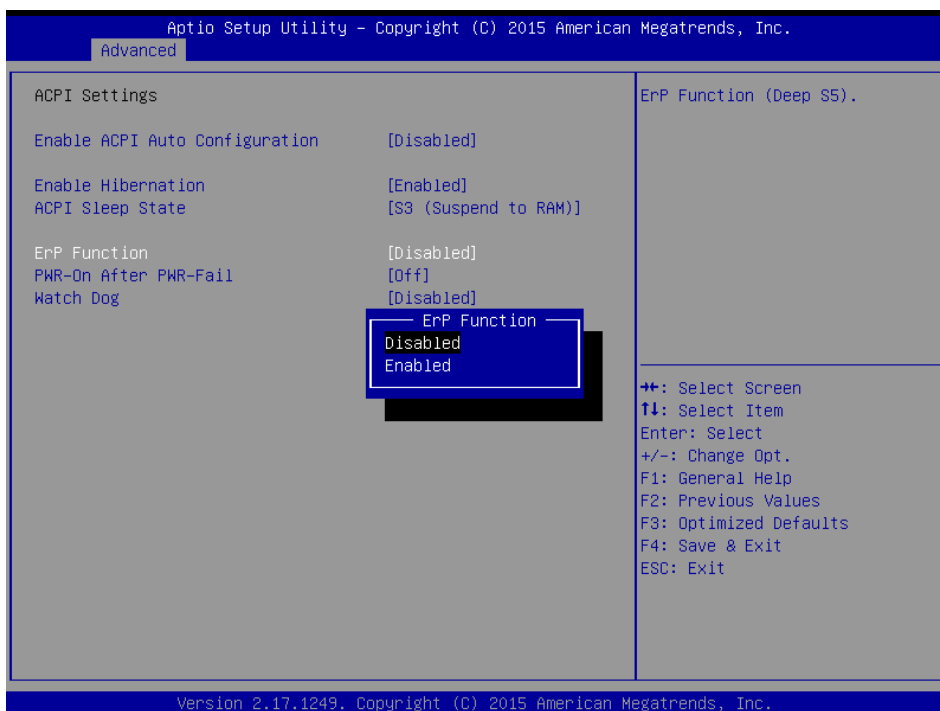
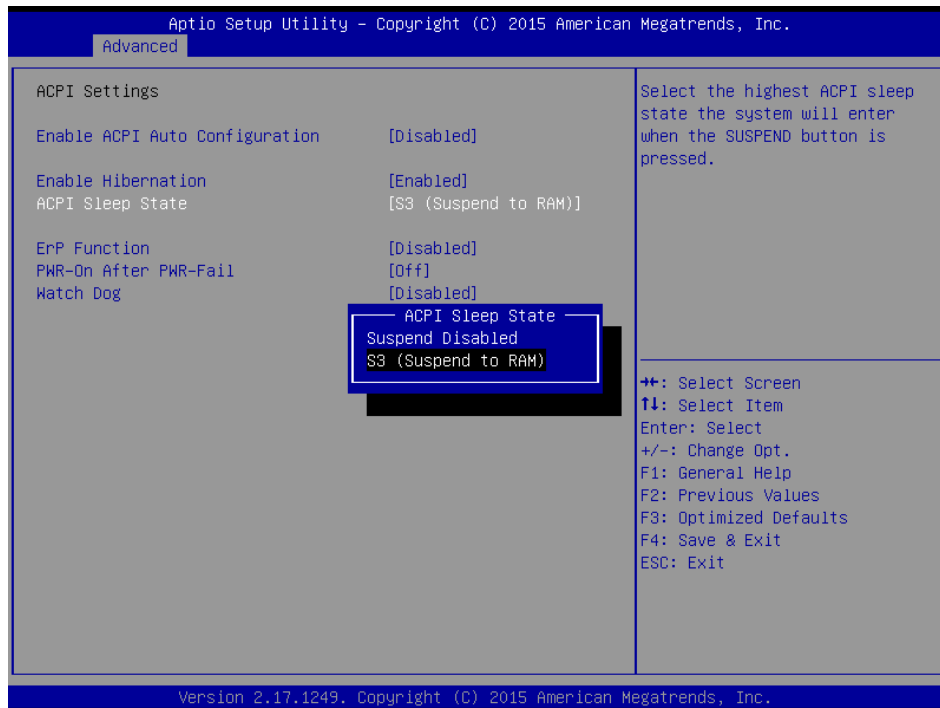


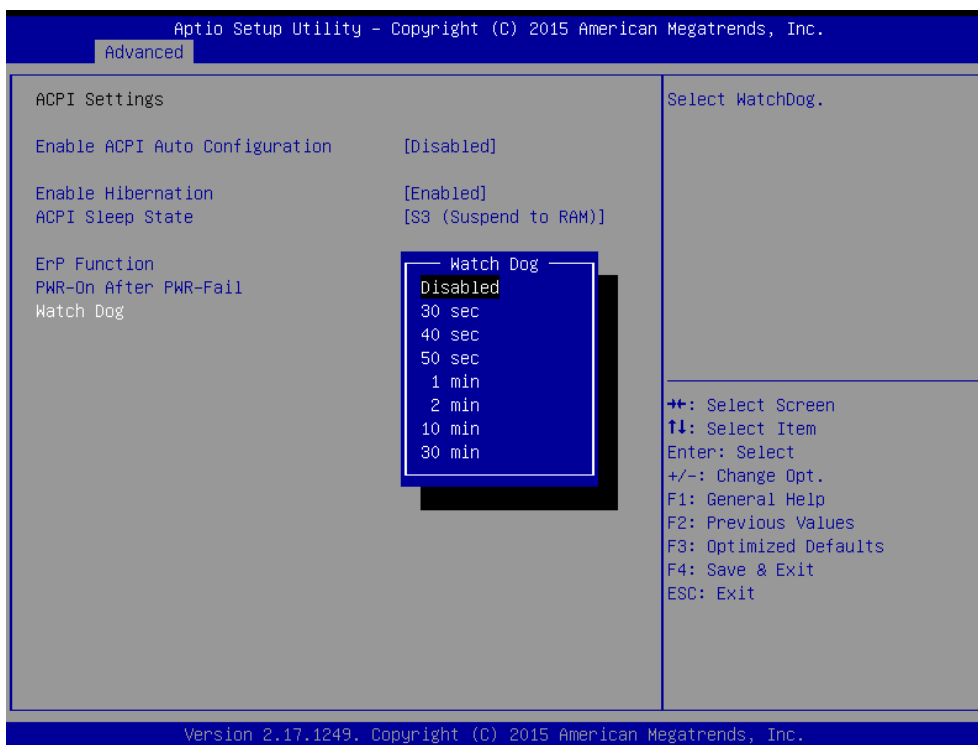
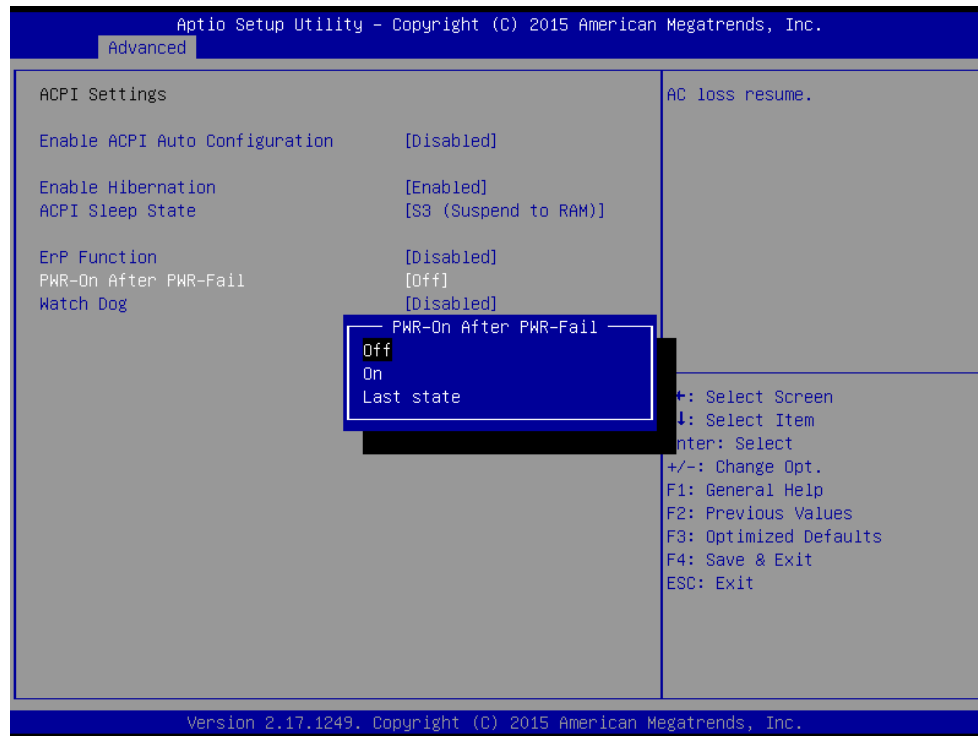
Item	Options	Description
Security Device Support	Disable, Enable [Default]	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

3.6.2.2 APCI Settings







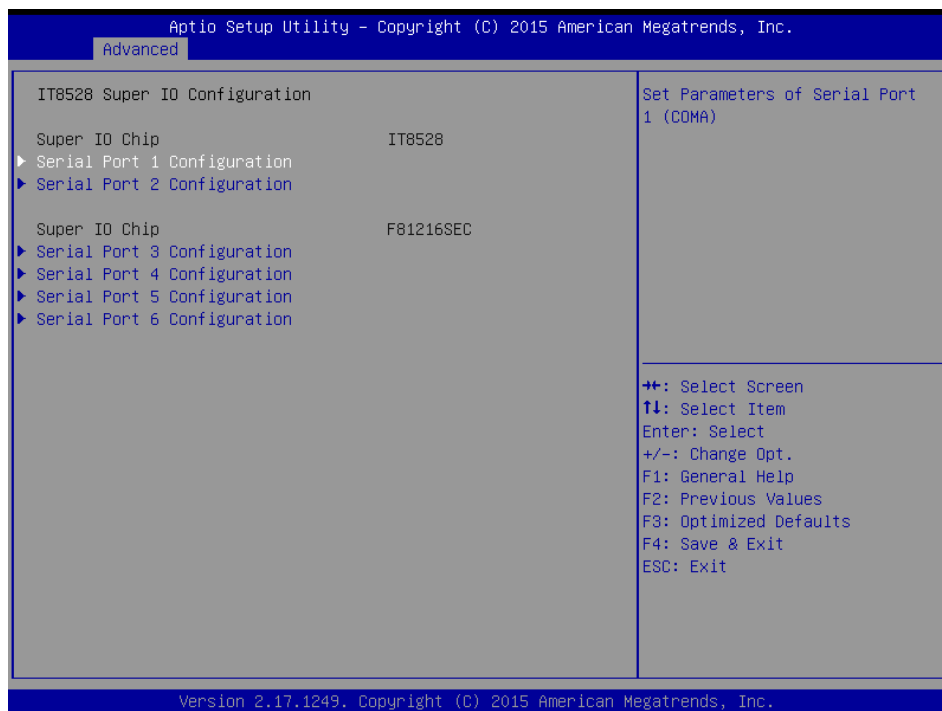


Item	Options	Description
Enable ACPI Auto Configuration	Disabled[Default], Enabled	Enables or Disables BIOS ACPI Auto Configuration.
Enable Hibernation	Disabled Enabled[Default],	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some

		OS.
ACPI Sleep State	Suspend Disabled, S3 (Suspend to RAM)[Default]	Select the highest ACPI sleep state the system will enter when the SUSPEDN button is pressed.
ErP Function	Disabled[Default], Enabled	ErP Function (Deep S5).
PWR-On After PWR-Fail	Off[Default] On Last state	AC loss resume.
Watch Dog	Disabled[Default], 30 sec 40 sec 50 sec 1 min 2 min 10 min 30 min	Select WatchDog.

3.6.2.3 IT8528 Super IO Configuration

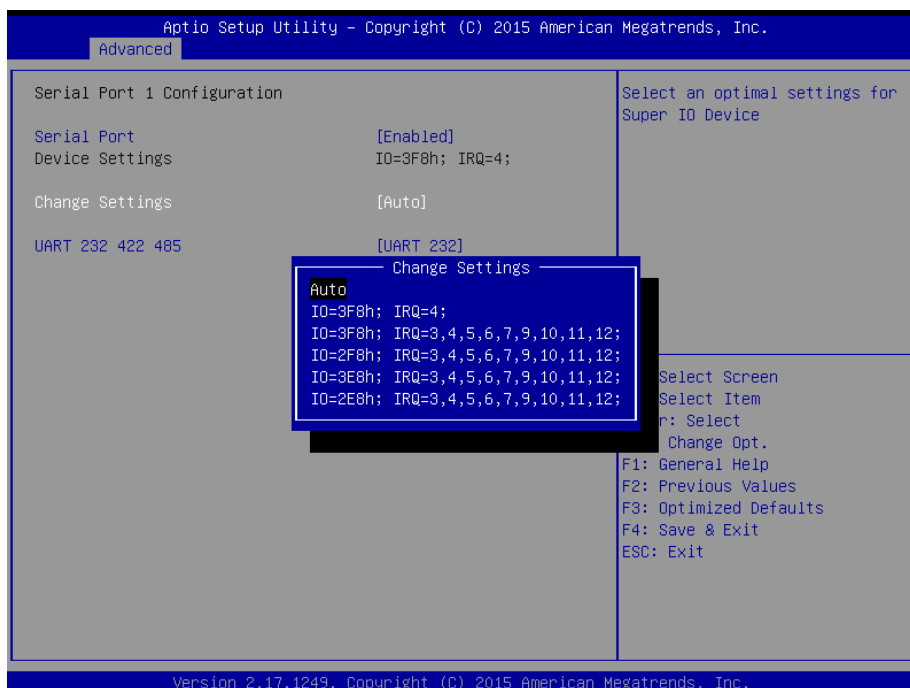
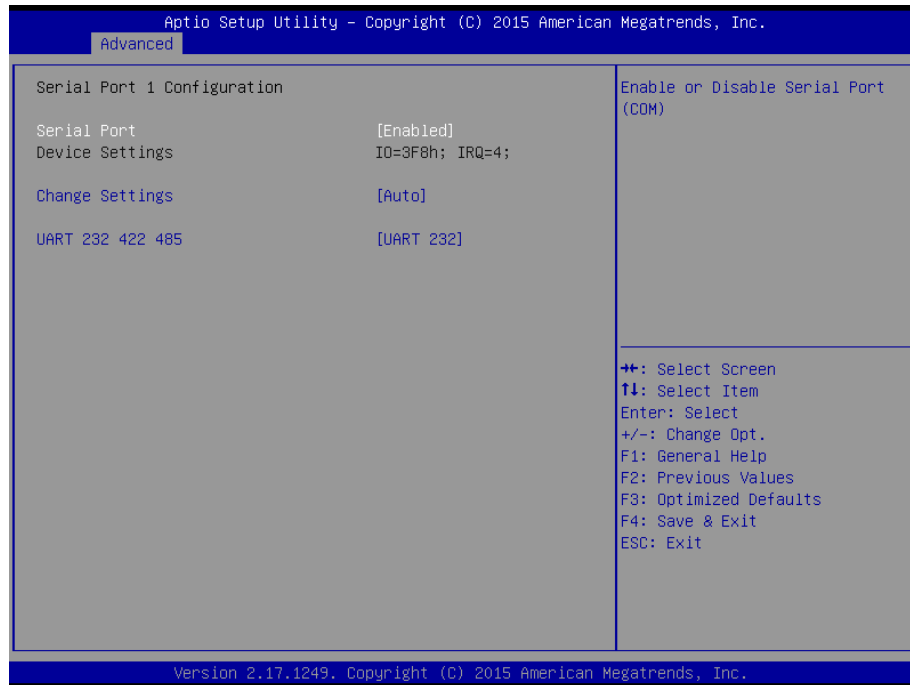
You can use this item to set up or change the IT8528 Super IO configuration for serial ports. Please refer to 3.6.2.3.1~ 3.6.2.3.6 for more information.

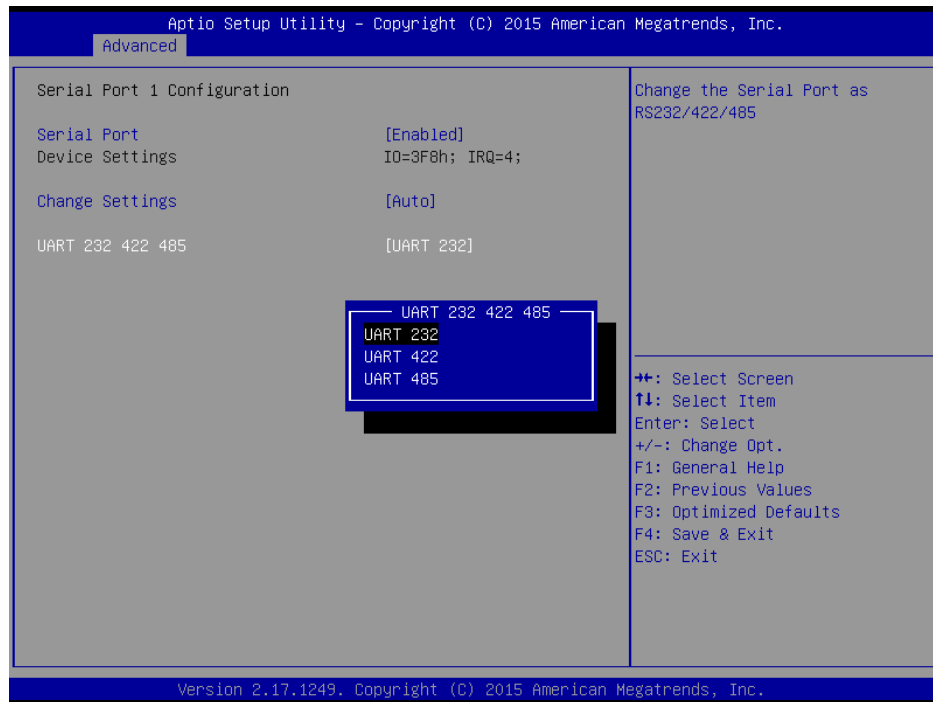


Item	Description
Serial Port 1 Configuration	Set Parameters of Serial Port 1 (COMA).
Serial Port 2 Configuration	Set Parameters of Serial Port 2 (COMB).
Serial Port 3 Configuration	Set Parameters of Serial Port 3 (COMC).
Serial Port 4 Configuration	Set Parameters of Serial Port 4 (COMD).

Serial Port 5 Configuration	Set Parameters of Serial Port 5 (COME).
Serial Port 6 Configuration	Set Parameters of Serial Port 6 (COMF).

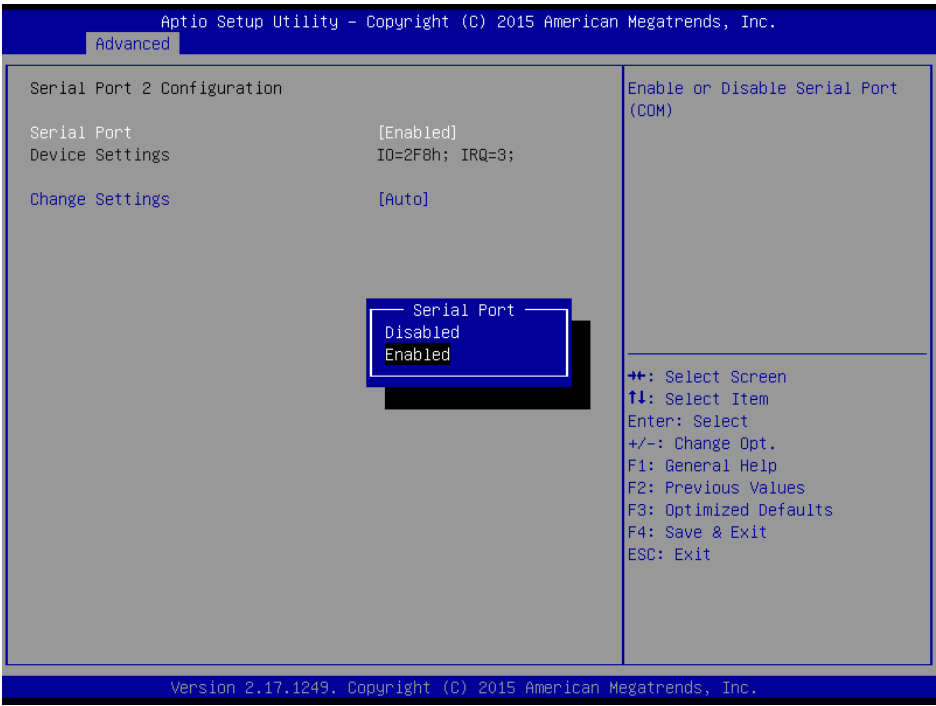
3.6.2.3.1 Serial Port 1 Configuration

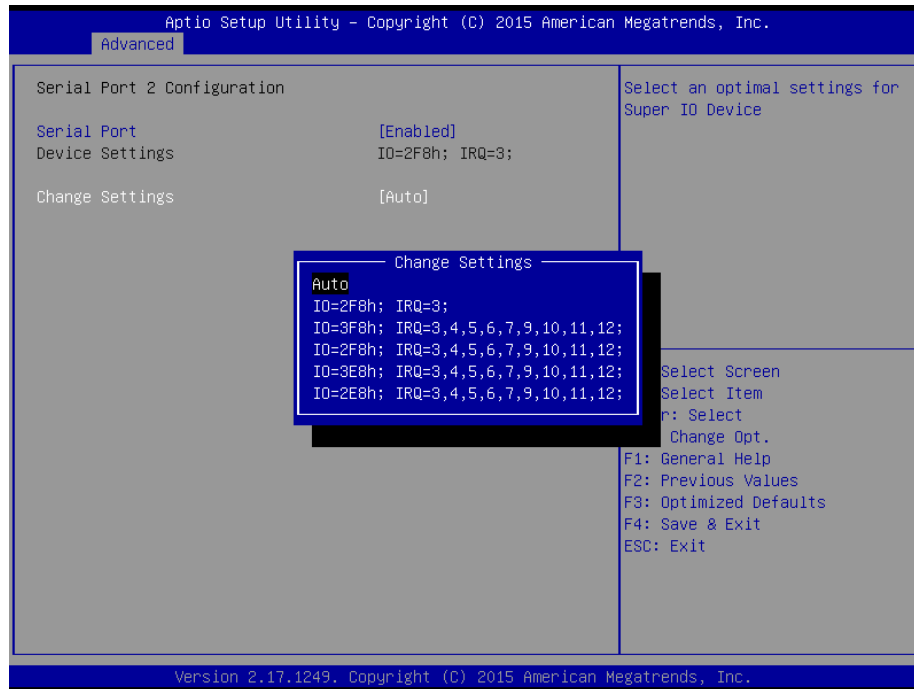




Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default] IO=3F8h; IRQ=4; IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	Select an optimal setting for Super IO Device.
UART 232 422 485	UART 232[Default], UART 422, UART 485	Change the Serial Port as RS232/ 422/ 485

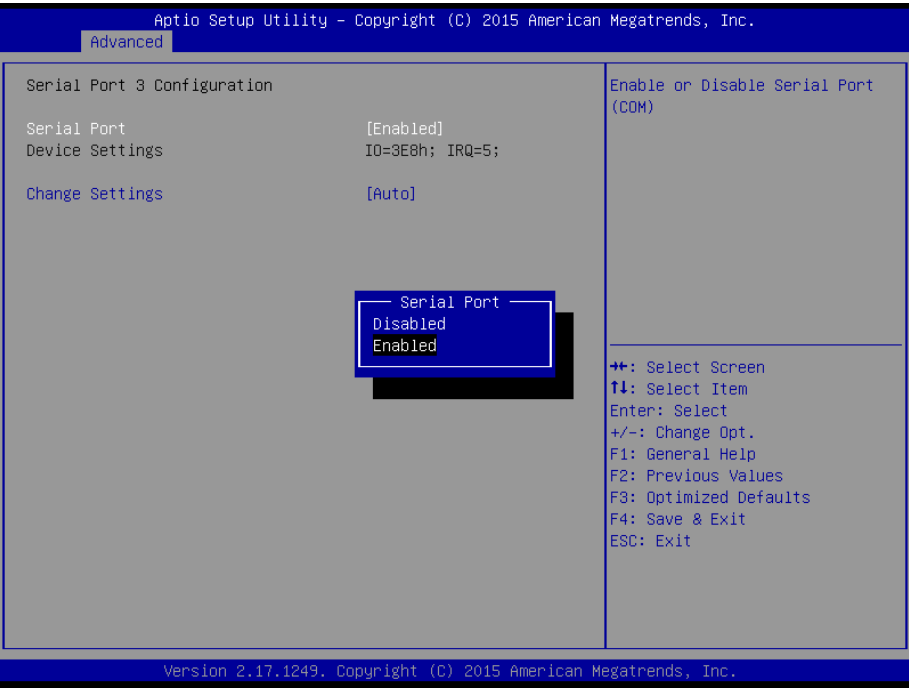
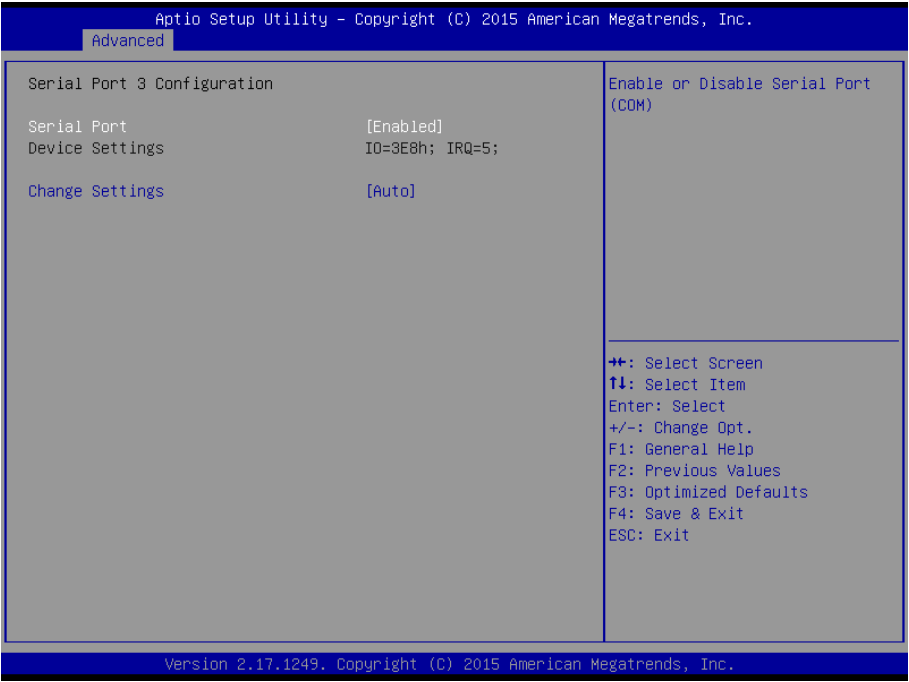
3.6.2.3.2 Serial Port 2 Configuration

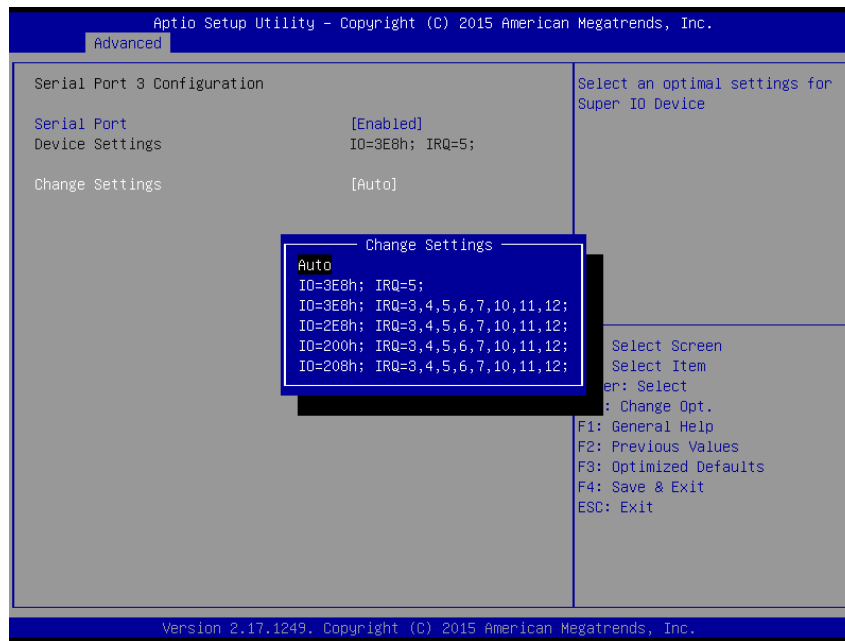




Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default] IO=2F8h; IRQ=3; IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	Select an optimal setting for super IO Device.

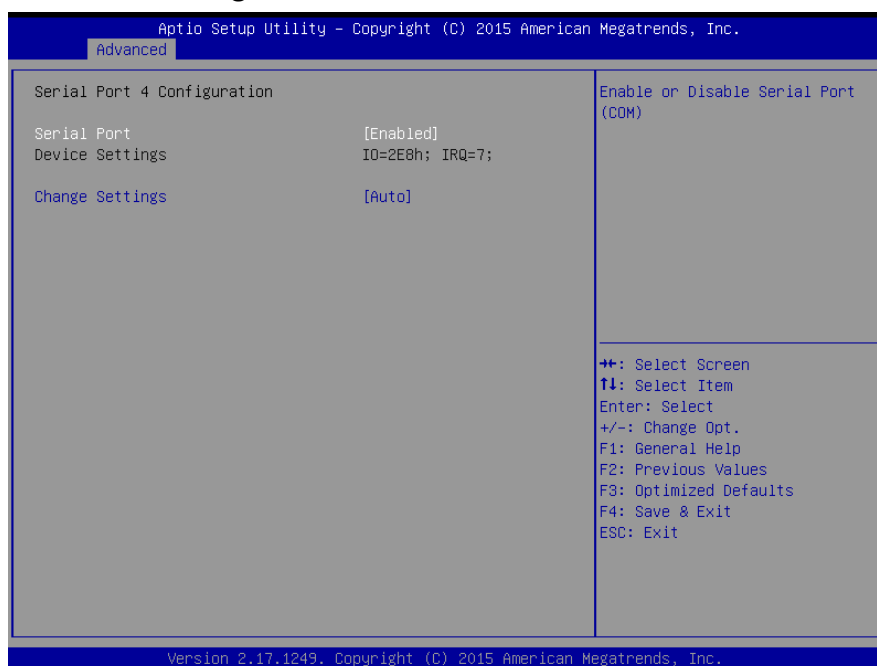
3.6.2.3.3 Serial Port 3 Configuration

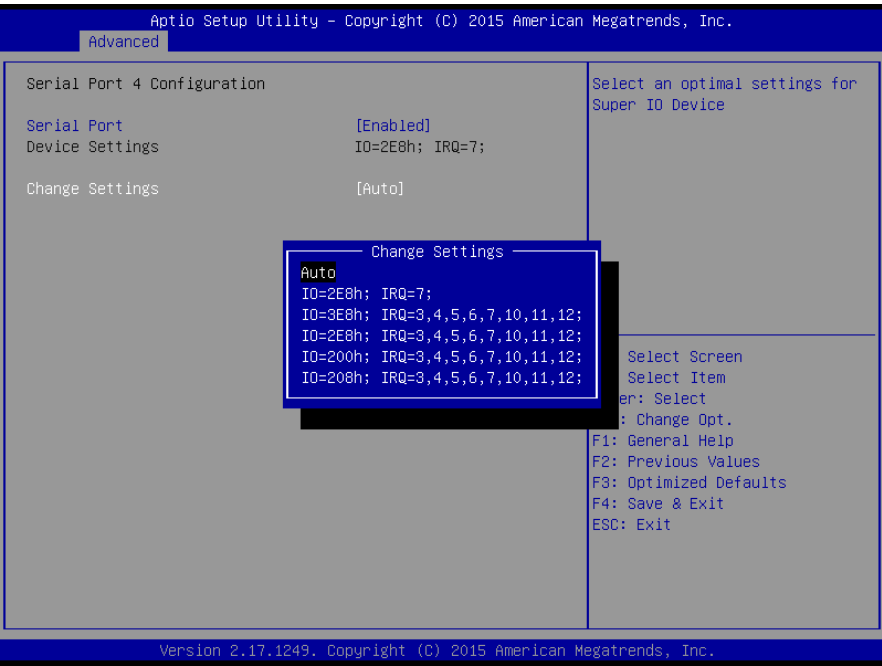
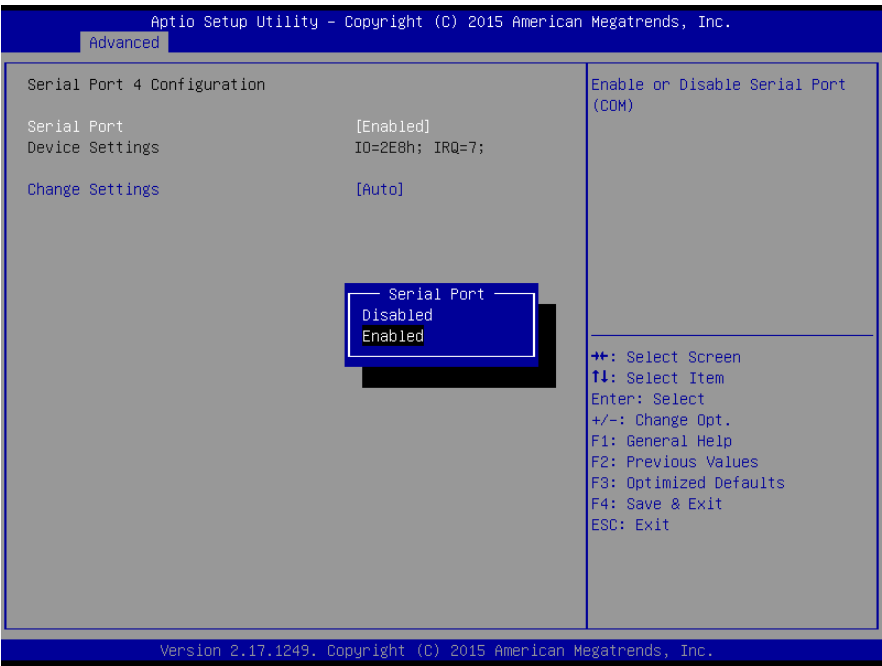




Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default] IO=3E8h; IRQ=5; IO=3E8h; IRQ=3,4,5,6,7,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,10,11,12; IO=200h; IRQ=3,4,5,6,7,10,11,12; IO=208h; IRQ=3,4,5,6,7,10,11,12;	Select an optimal setting for super IO Device.

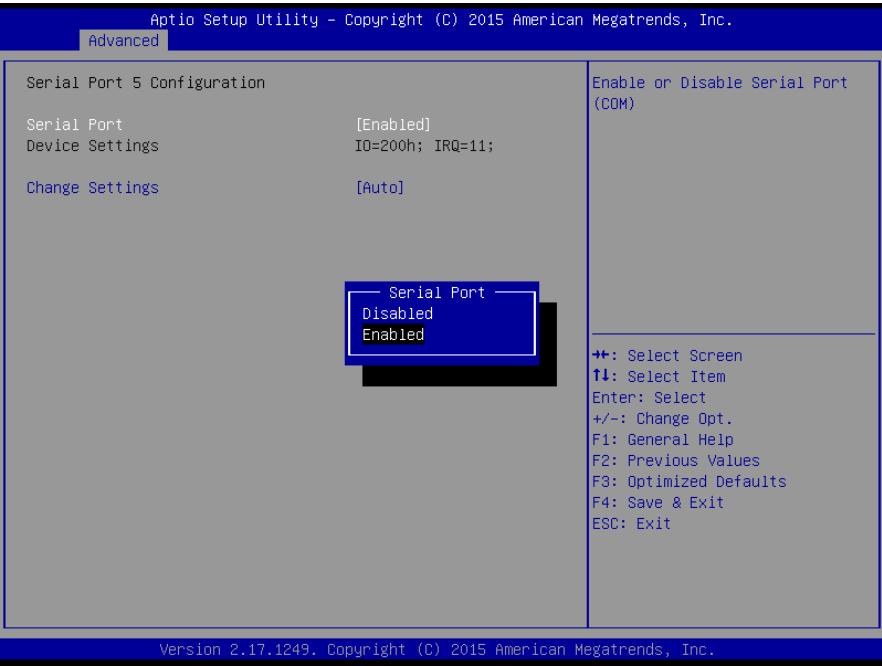
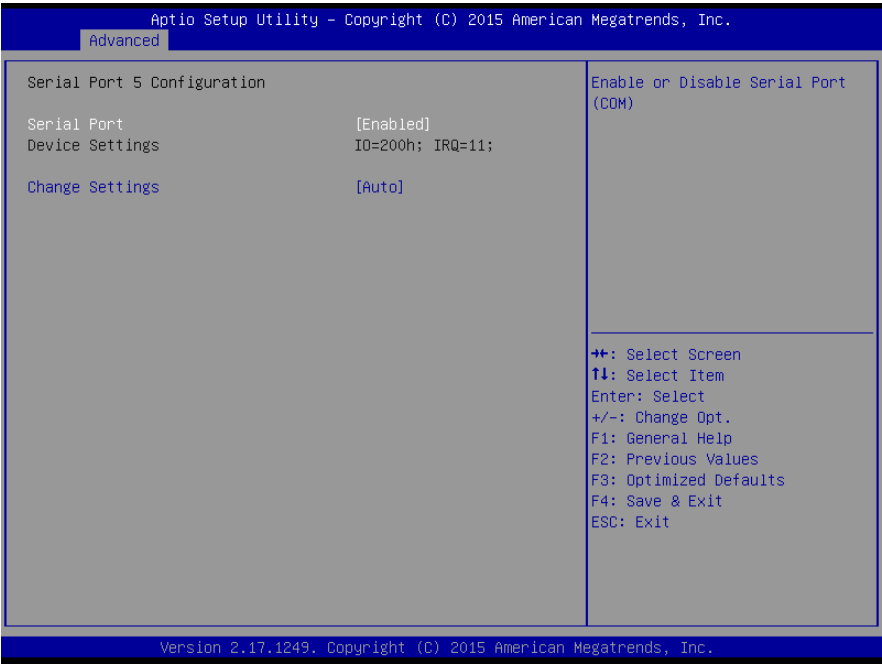
3.6.2.3.4 Serial Port 4 Configuration

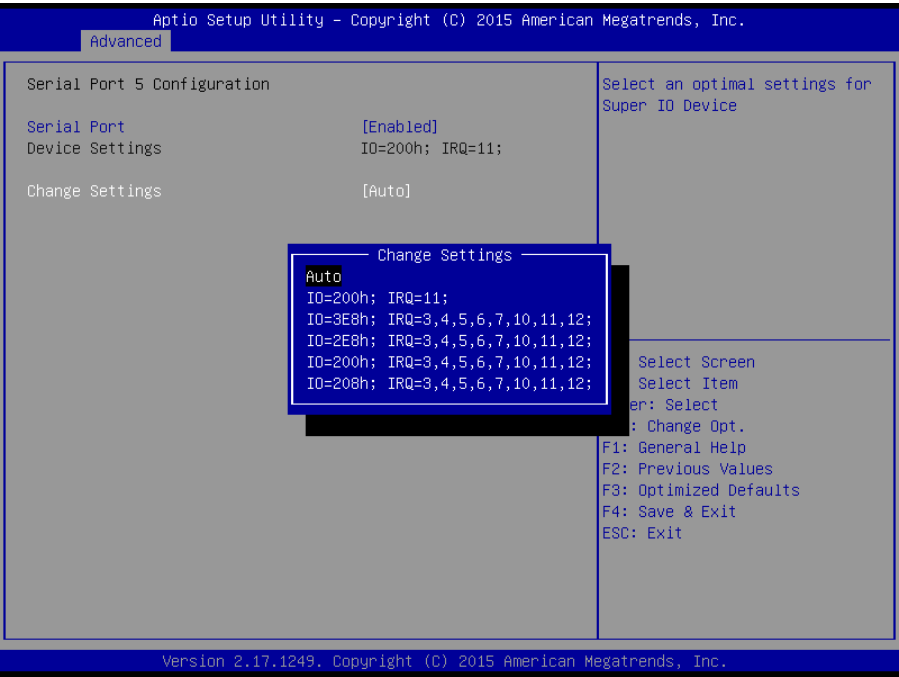




Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default] IO=2E8h; IRQ=7; IO=3E8h; IRQ=3,4,5,6,7,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,10,11,12; IO=200h; IRQ=3,4,5,6,7,10,11,12; IO=208h; IRQ=3,4,5,6,7,10,11,12;	Select an optimal setting for super IO Device.

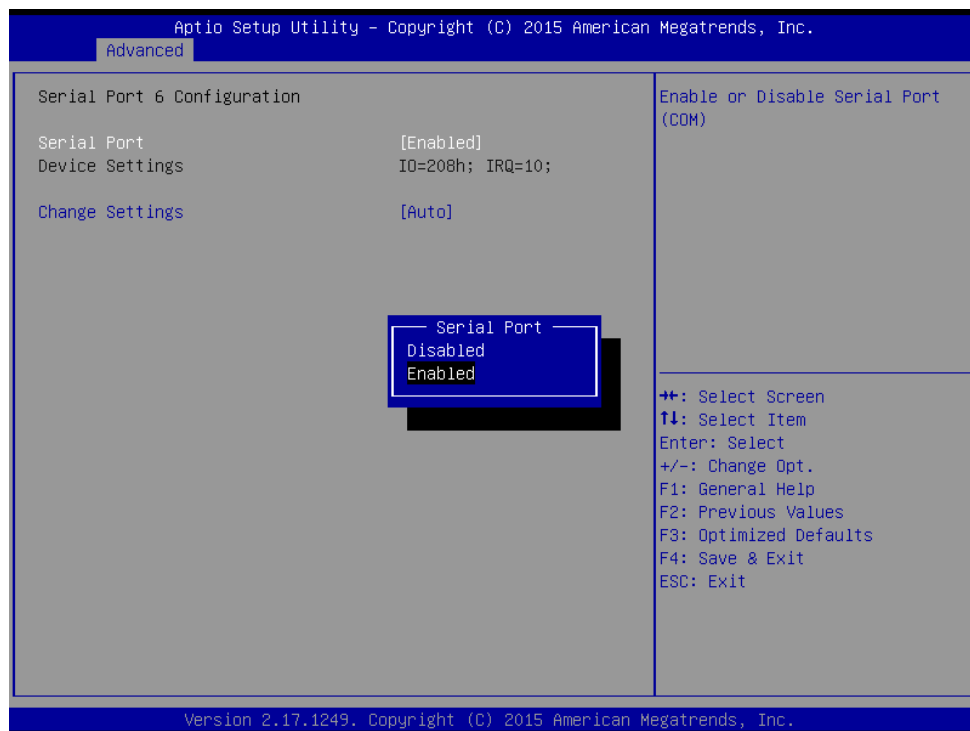
3.6.2.3.5 Serial Port 5 Configuration





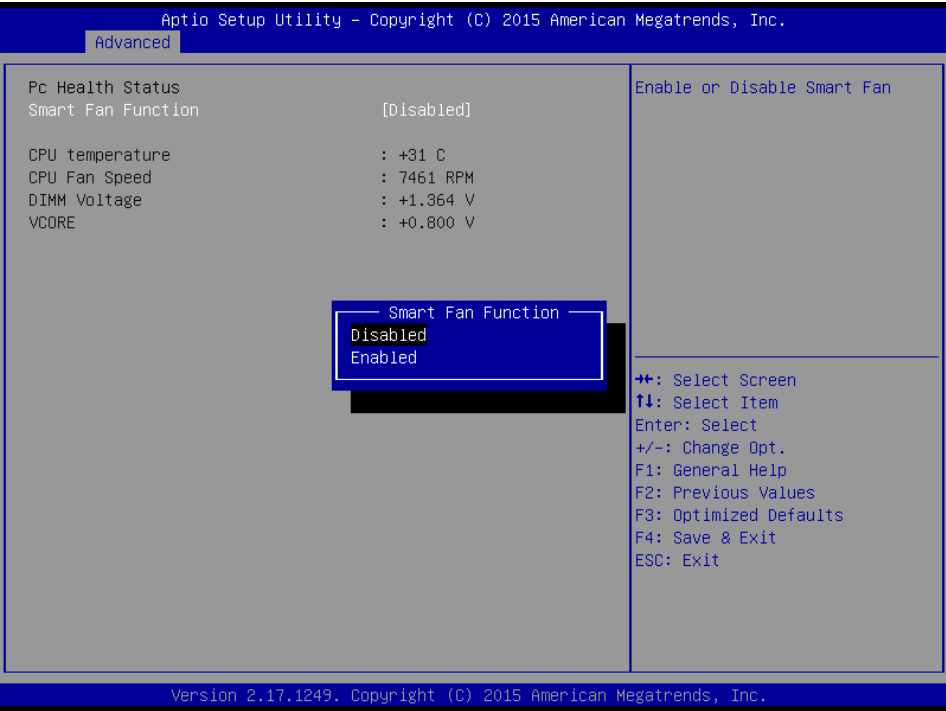
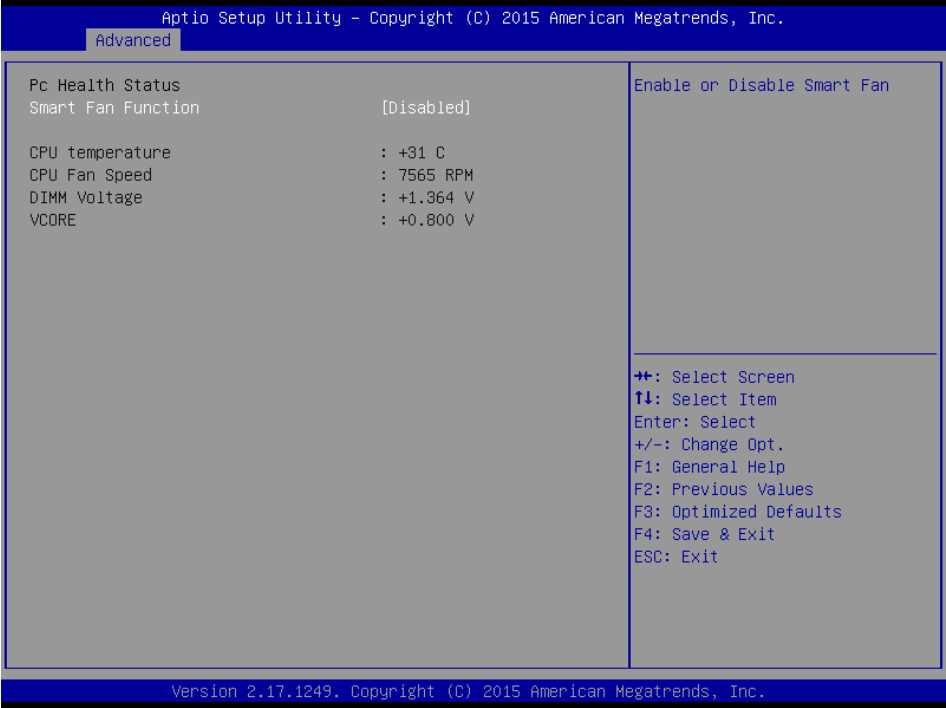
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default] IO=200h; IRQ=11; IO=3E8h; IRQ=3,4,5,6,7,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,10,11,12; IO=200h; IRQ=3,4,5,6,7,10,11,12; IO=208h; IRQ=3,4,5,6,7,10,11,12;	Select an optimal setting for super IO Device.

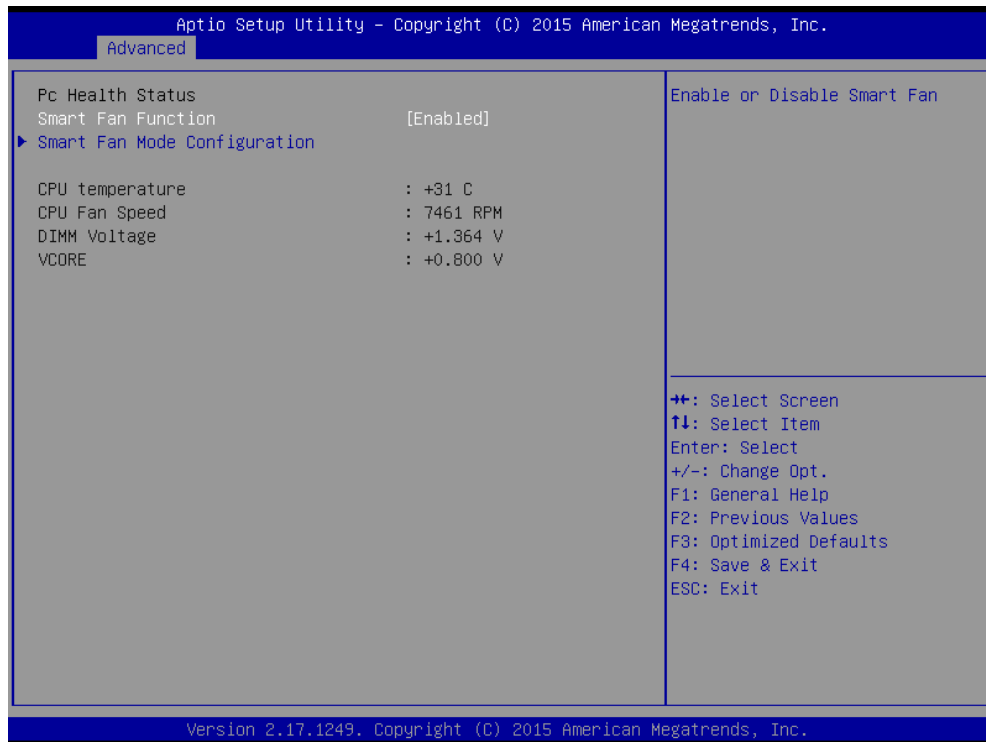
3.6.2.3.6 Serial Port 6 Configuration



Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).
Change Settings	Auto[Default] IO=208h; IRQ=10; IO=3E8h; IRQ=3,4,5,6,7,10,11,12; IO=2E8h; IRQ=3,4,5,6,7,10,11,12; IO=200h; IRQ=3,4,5,6,7,10,11,12; IO=208h; IRQ=3,4,5,6,7,10,11,12;	Select an optimal setting for super IO Device.

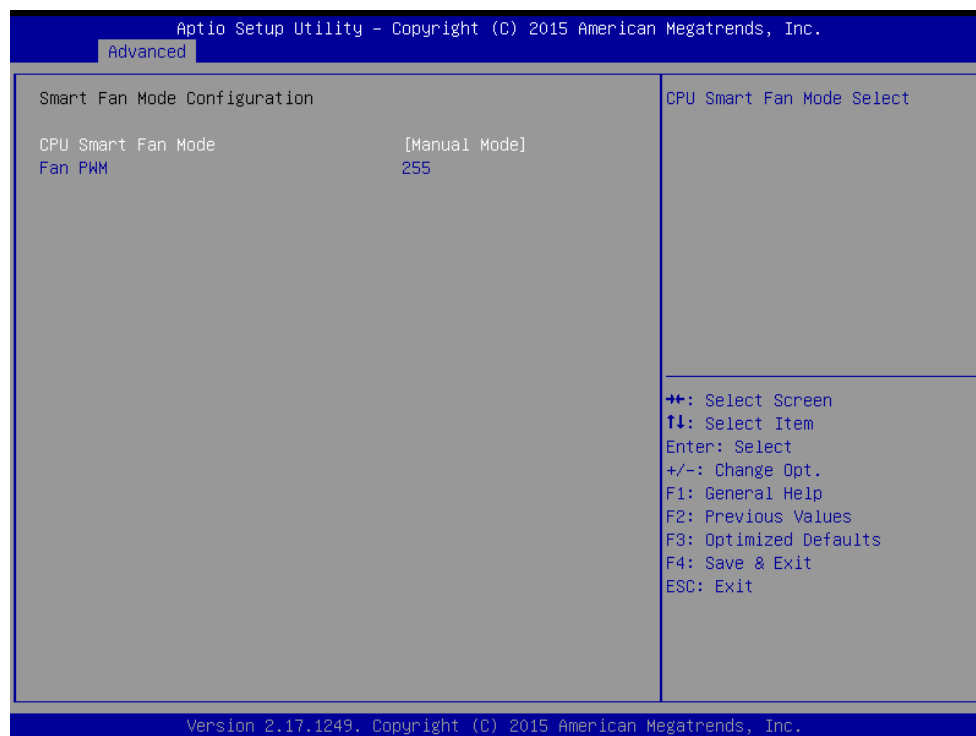
3.6.2.4 H/W Monitor

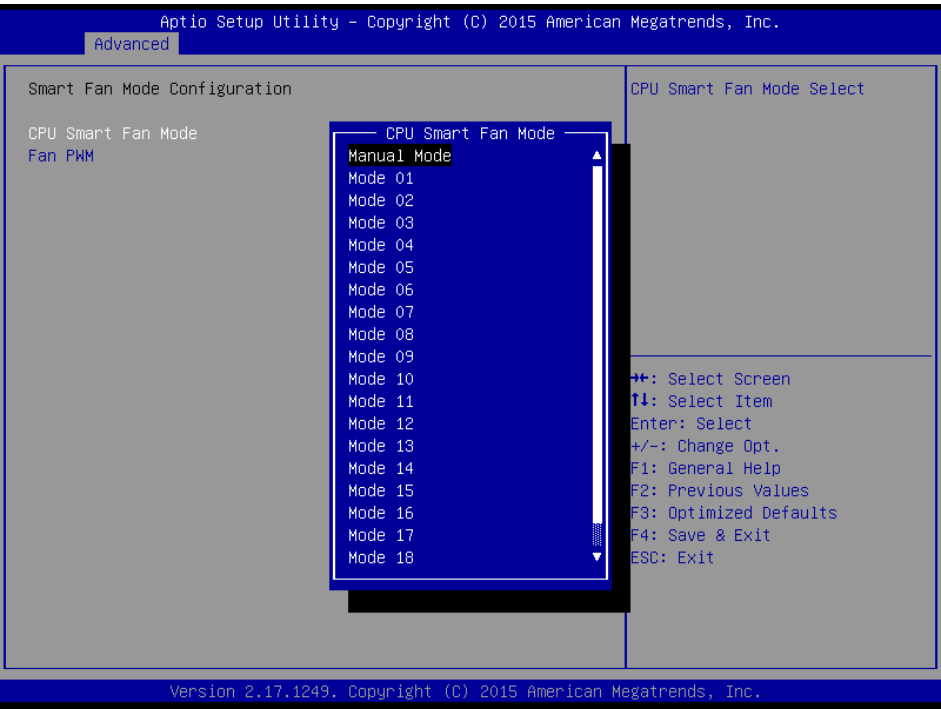




Item	Options	Description
Smart Fan Function	Enabled, Disabled[Default]	Enable or Disable Smart Fan.

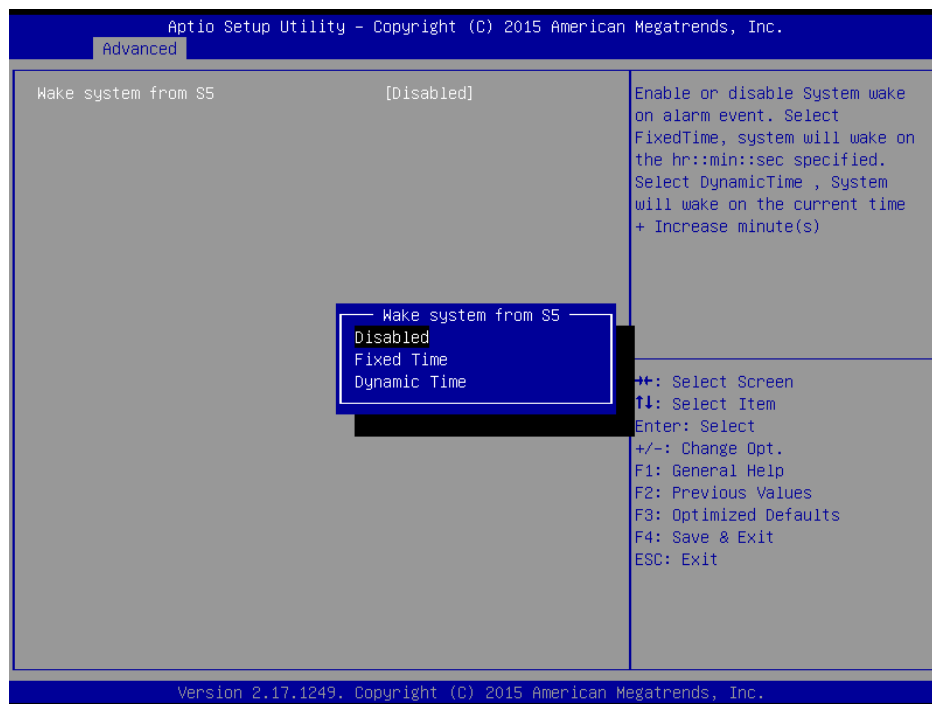
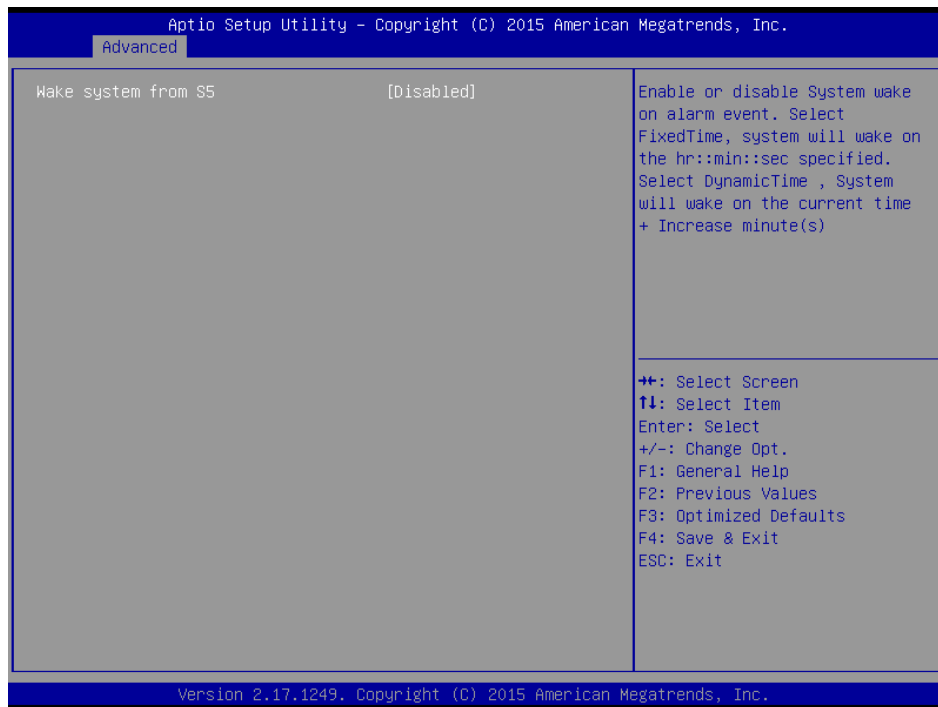
3.6.2.4.1 Smart Fan Mode Configuration

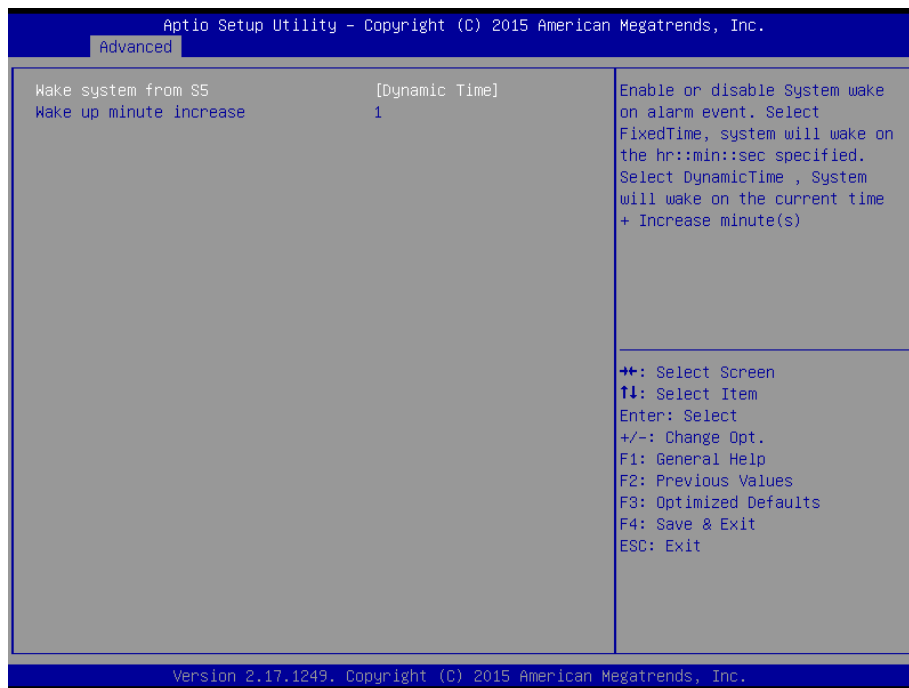
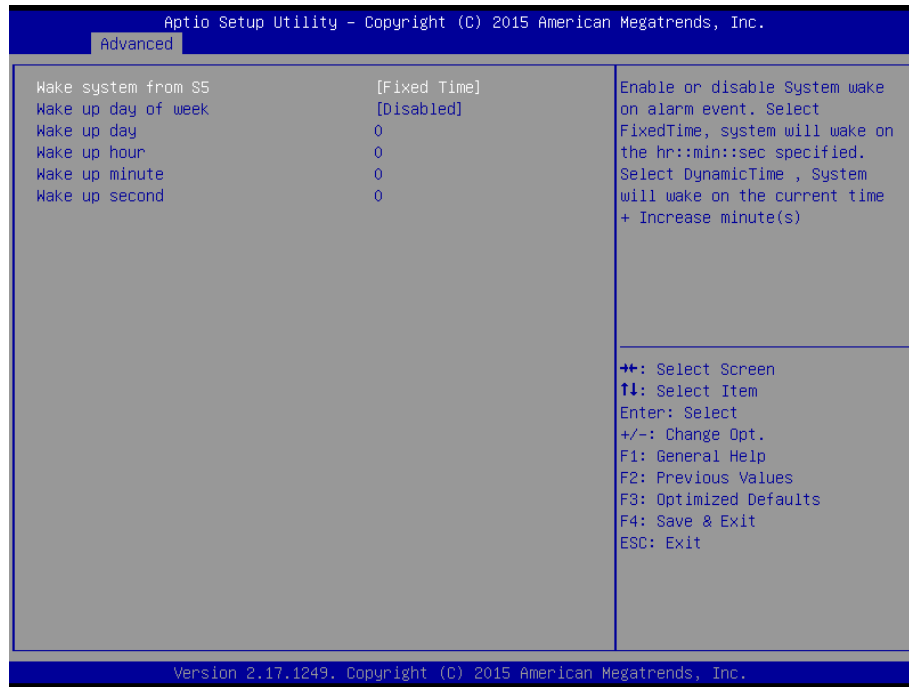




Item	Option	Description
CPU Smart Fan Mode	Manual Mode[Default],	CPU Smart Fan Mode Select.
	Mode 01	
	Mode 02	
	Mode 03	
	Mode 04	
	Mode 05	
	Mode 06	
	Mode 07	
	Mode 08	
Fan PWM	0-255	Fan PWM duty.

3.6.2.5 S5 RTC Wake Settings

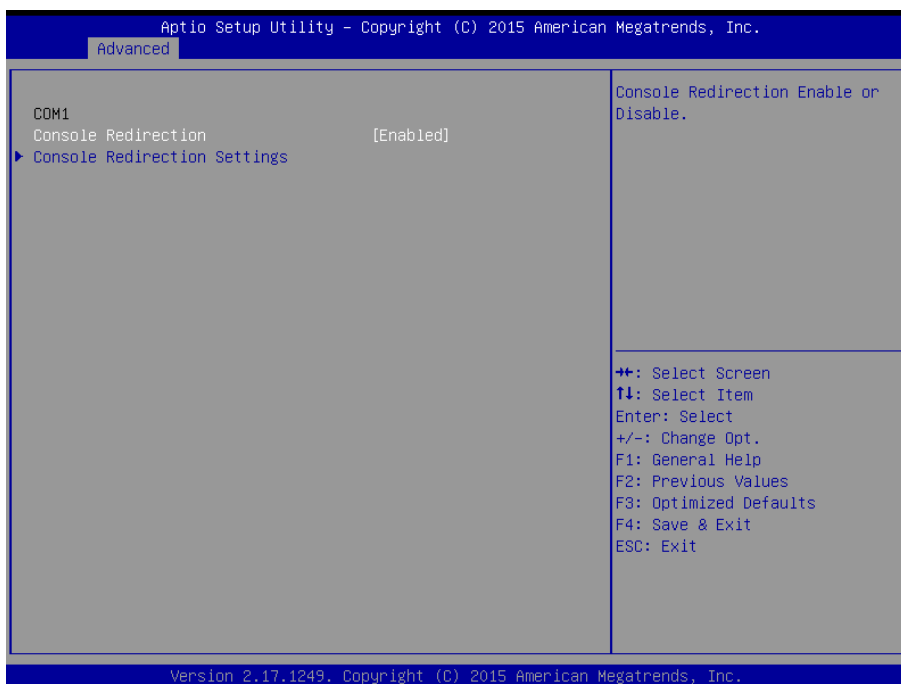
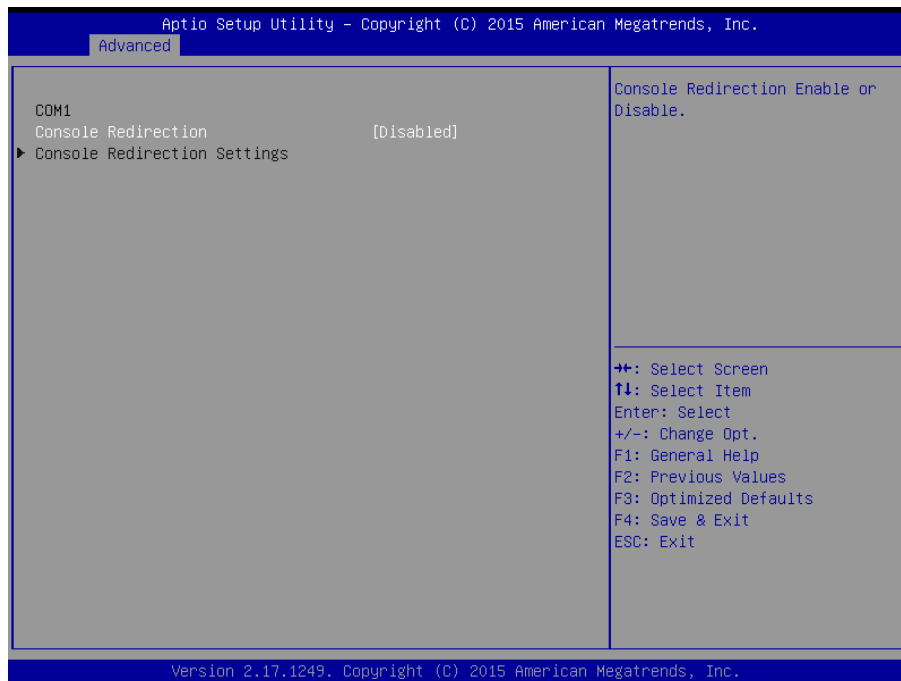




Item	Options	Description
Wake system from S5	Disabled[Default], Fixed Time Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).
Wake up day of week	Disabled[Default], Monday-Friday Monday-Saturday	Wake up day of week. (Monday-Friday) or (Monday-Saturday).
Wake up day	1-31	Select 0 for daily system wake up 1-31 for

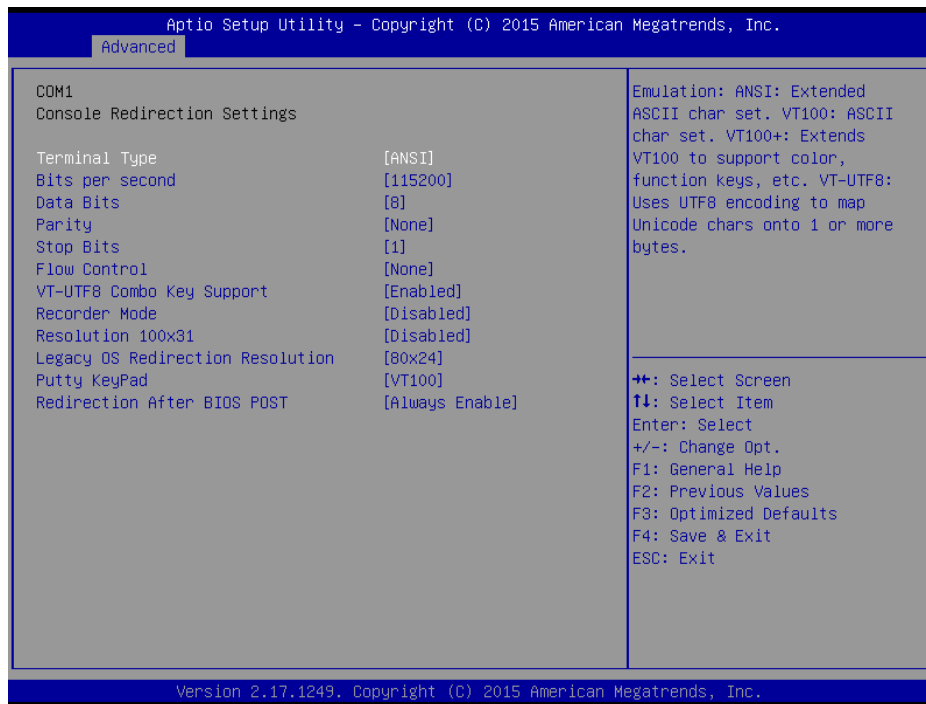
		which day of the month that you would like the system to wake up.
Wake up hour	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.
Wake up minute	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.
Wake up second	0-23	Select 0-23 For example enter 3 for 3am and 15 for 3pm.
Wake up minute increase	1-5	1 – 5.

3.6.2.6 Serial Port Console Redirection



Item	Options	Description
Console Redirection	Disabled[Default], Enabled	Console Redirection Enable or Disable.

3.6.2.6.1 COM1



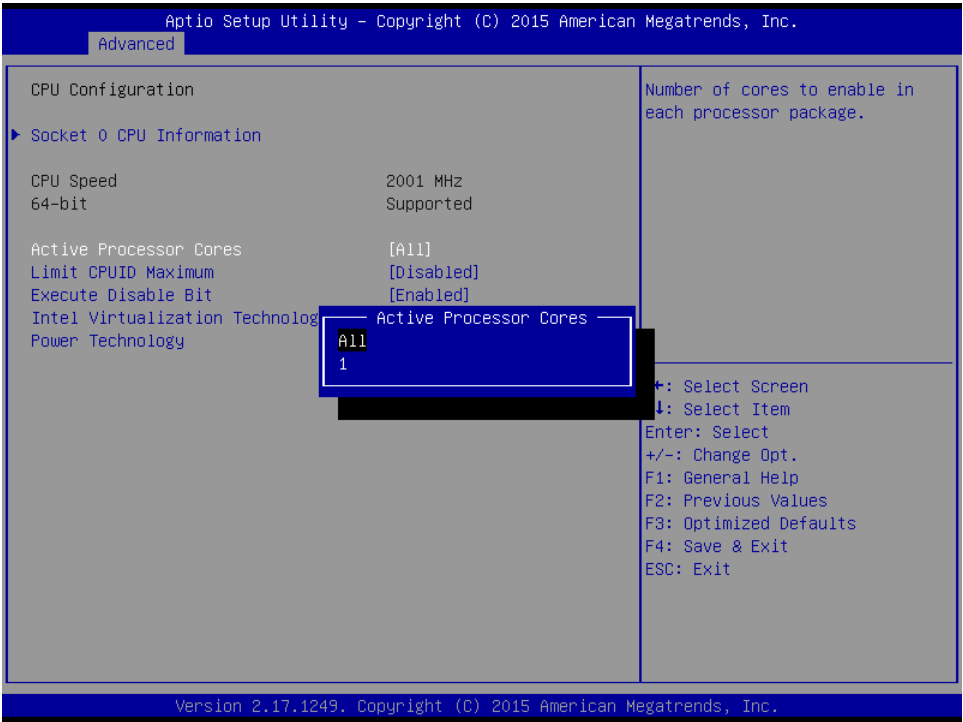
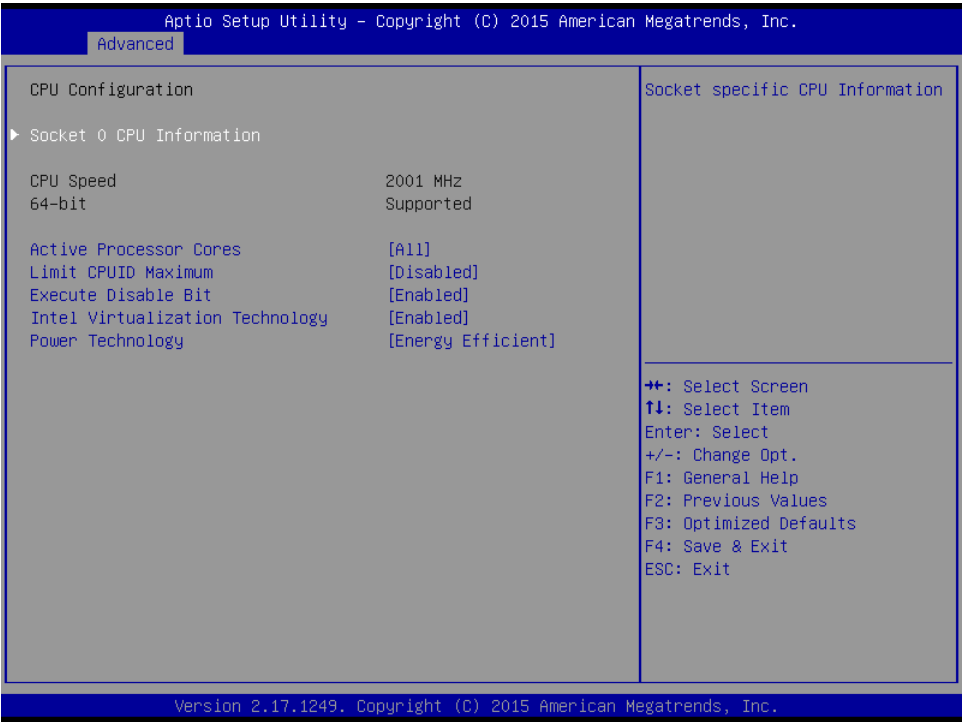
Item	Option	Description
Terminal Type	VT100 VT100+ VT-UTF8 ANSI[Default]	Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support color, function keys, etc. VT-UTF8 encoding to map Unicode chars onto 1 or or more bytes.
Bits per second	9600 19200 38400 57600 115200[Default]	Select serial port transmission speed. The speed must be matched on the other side, Long or noisy lines may require lower speeds.
Data Bits	7 8[Default]	Data Bits.
Parity	None[Default] Even Odd Mark Space	A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.
Stop Bits	1[Default] 2	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit.

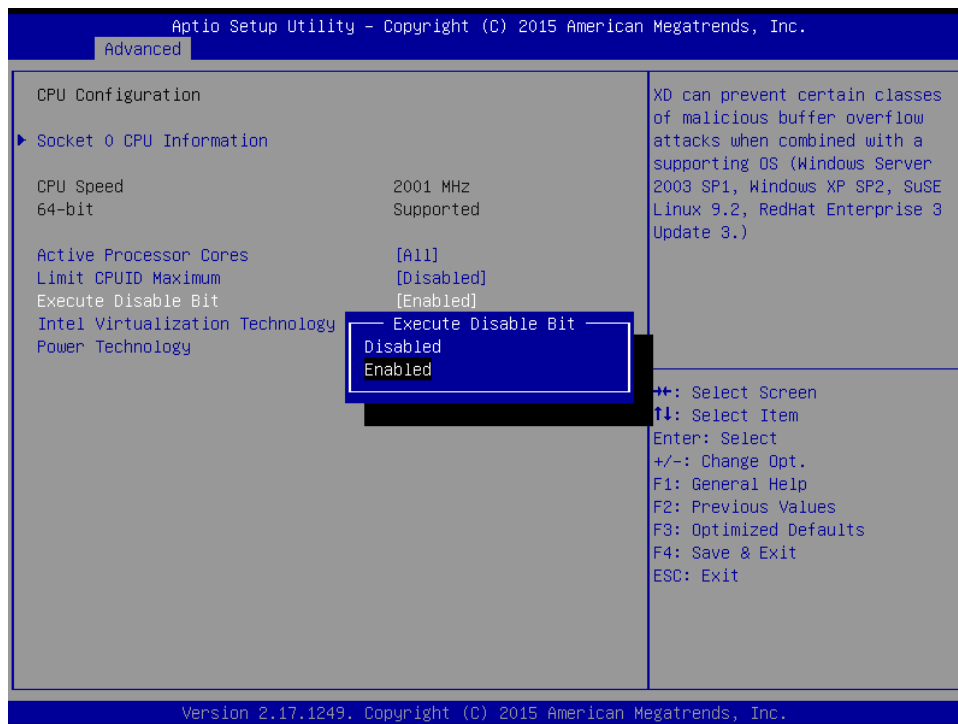
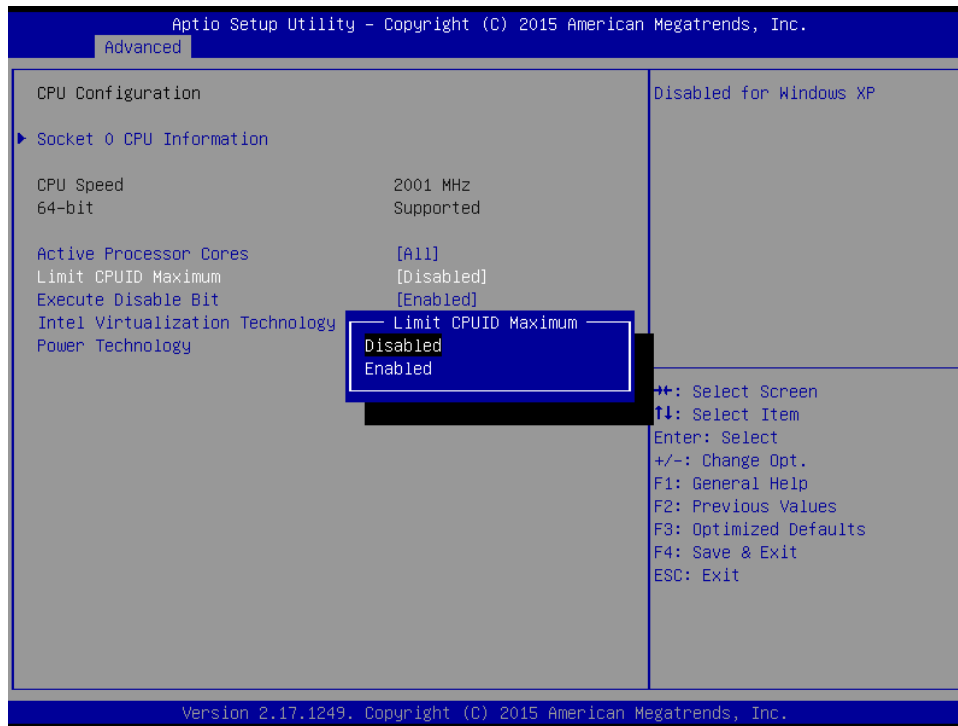
Quick Reference Guide

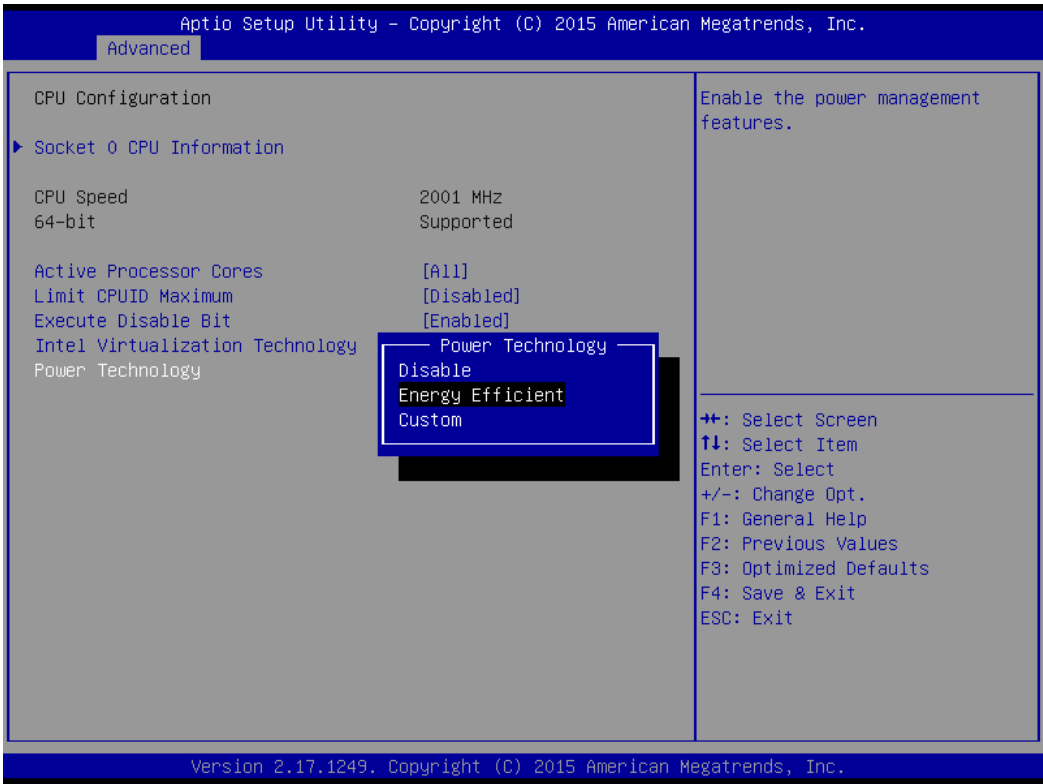
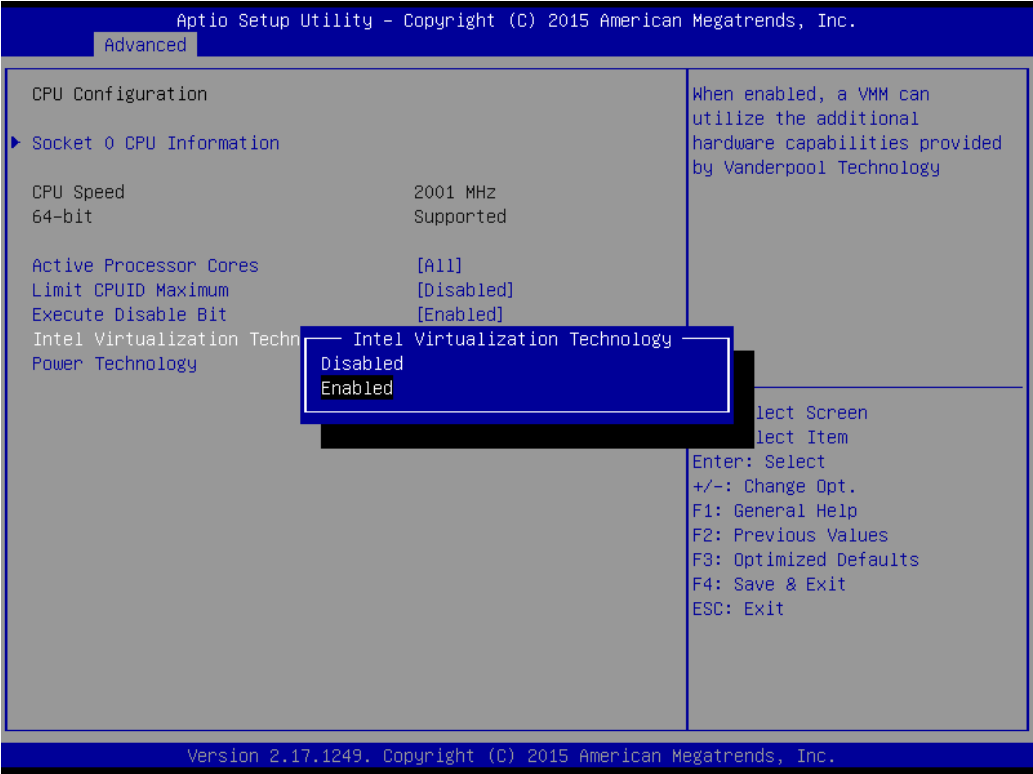
		Communication with slow devices may require more than 1 stop bit.
Flow Control	None[Default] Hardware RTS/CTS	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.
VT-UTF8 Combo Key Support	Disabled Enabled[Default]	Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.
Recorder Mode	Disabled[Default] Enabled	With this mode enabled only text will be sent. This is to capture Terminal data.
Resolution 100x31	Disabled[Default] Enabled	Enables or disables extended terminal resolution.
Legacy OS Redirection Resolution	80x24[Default] 80x25	On Legacy OS, the Number of Rows and Columns supported redirection.
Putty KeyPad	VT100[Default] LINUX XTERMR6 SCO ESCN VT400	Select FunctionKey and KeyPad on Putty.
Redirection After BIOS POST	Always Enable[Default] BootLoader	The Settings specify if BootLoader is selected then Legacy console redirection is disabled before booting to Legacy OS. Default value is Always Enable which means Legacy console Redirection is enabled for Legacy OS.

3.6.2.7 CPU Configuration

Use the CPU configuration menu to view detailed CPU specification and configure the CPU.

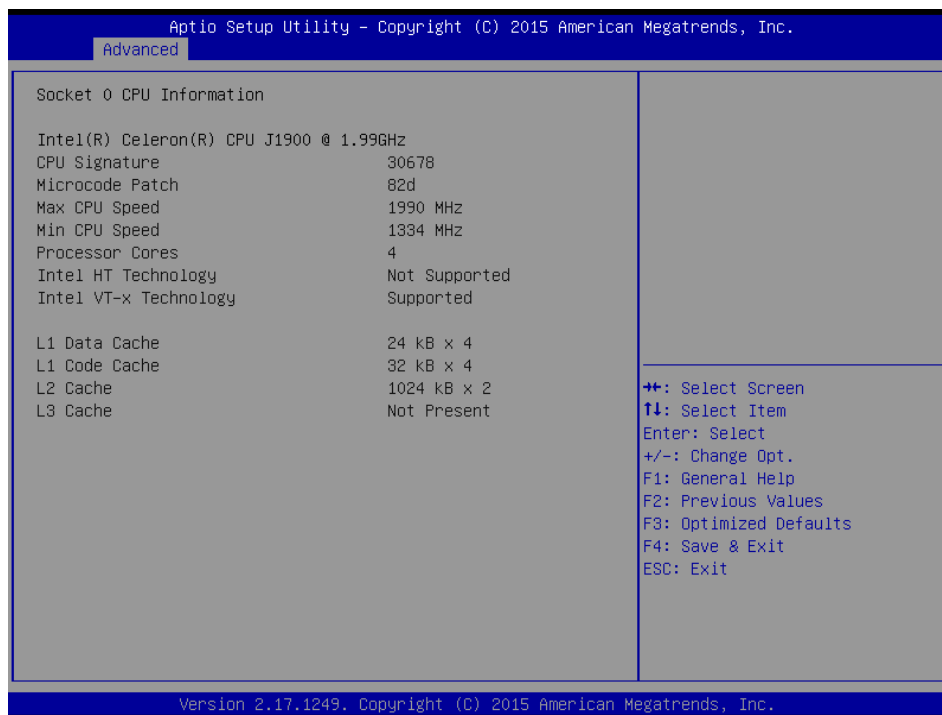




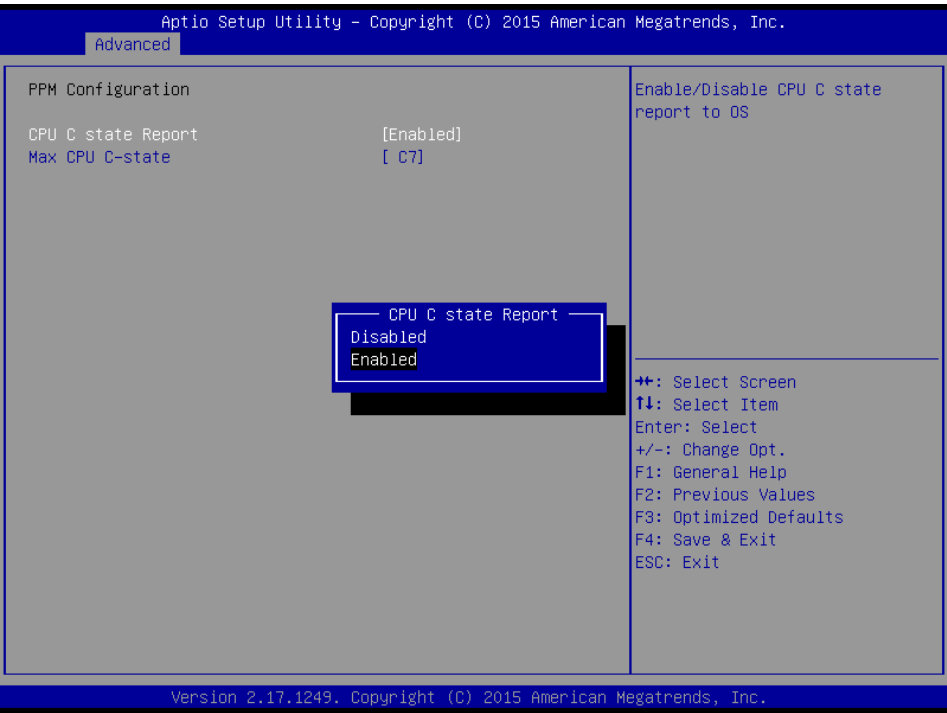
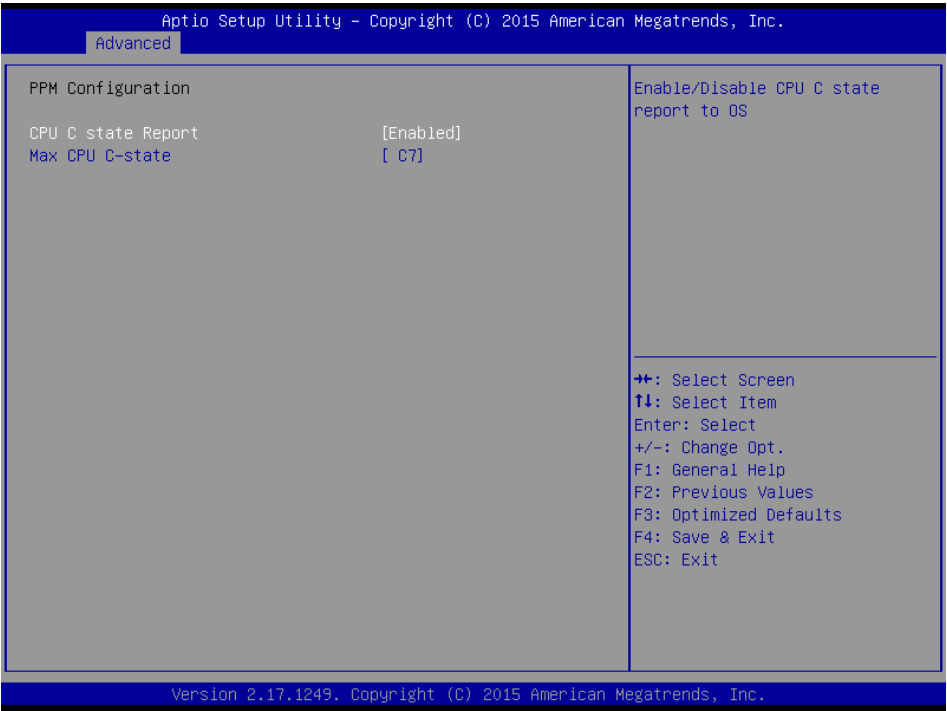


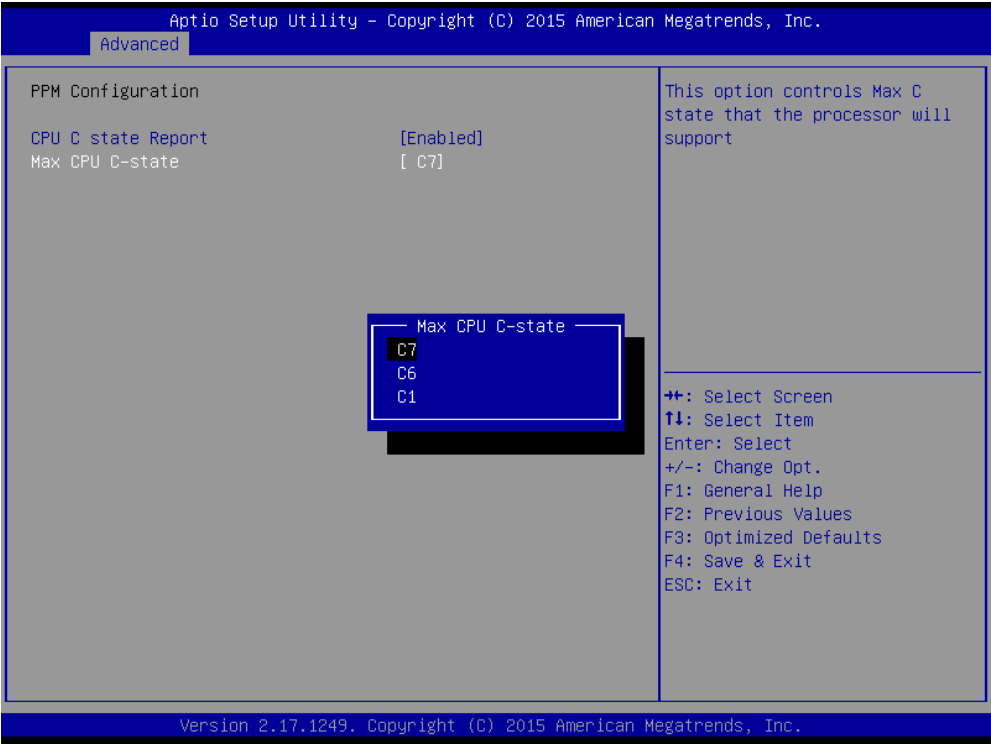
Item	Options	Description
Active Processor Cores	All[Default], 1	Number of cores to enable in each processor package.
Limit CPUID Maximum	Disabled[Default], Enabled	Disabled for Windows XP.
Execute Disable Bit	Disabled, Enabled[Default]	XD can prevent certain classes of malicious buffer overflow attacks when combined with a supporting OS (Windows Server 2003 SP1, Windows XP SP2, SuSE Linux 9.2, RedHat Enterprise 3 Update 3.)
Intel Virtualization Technology	Disabled, Enabled[Default]	When enabled, a VMM can utilize the additional hardware capabilities provided by Virtualization Technology.
Power Technology	Disabled[Default], Energy Efficient Custom	Enable the power management features.

3.6.2.7.1 Socket 0 CPU Information



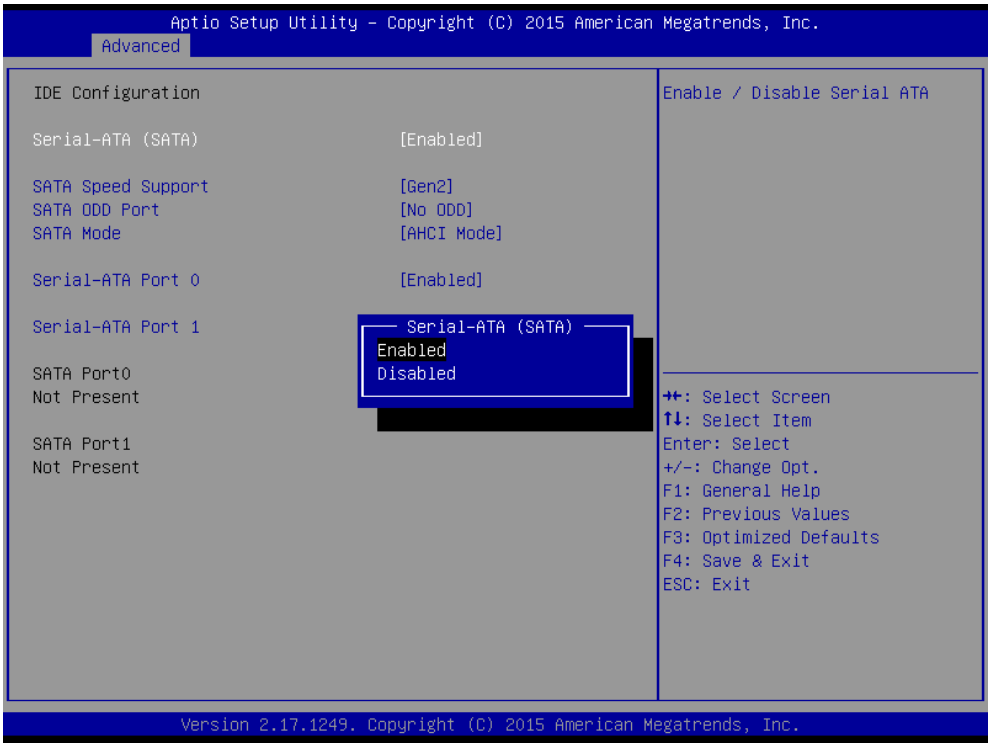
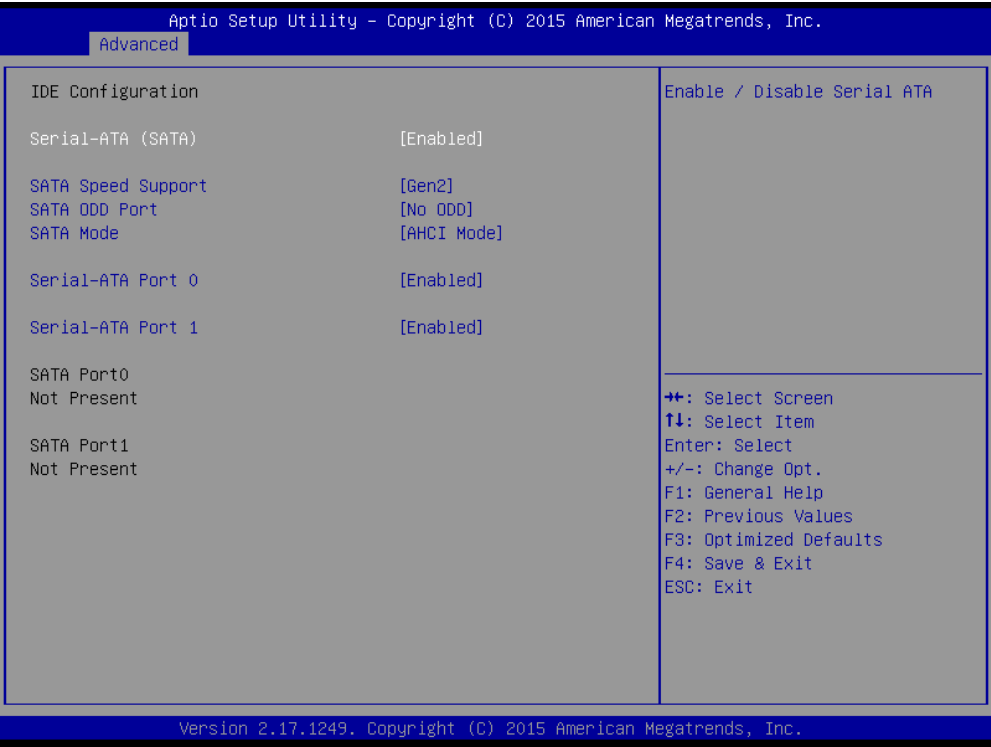
3.6.2.8 PPM Configuration

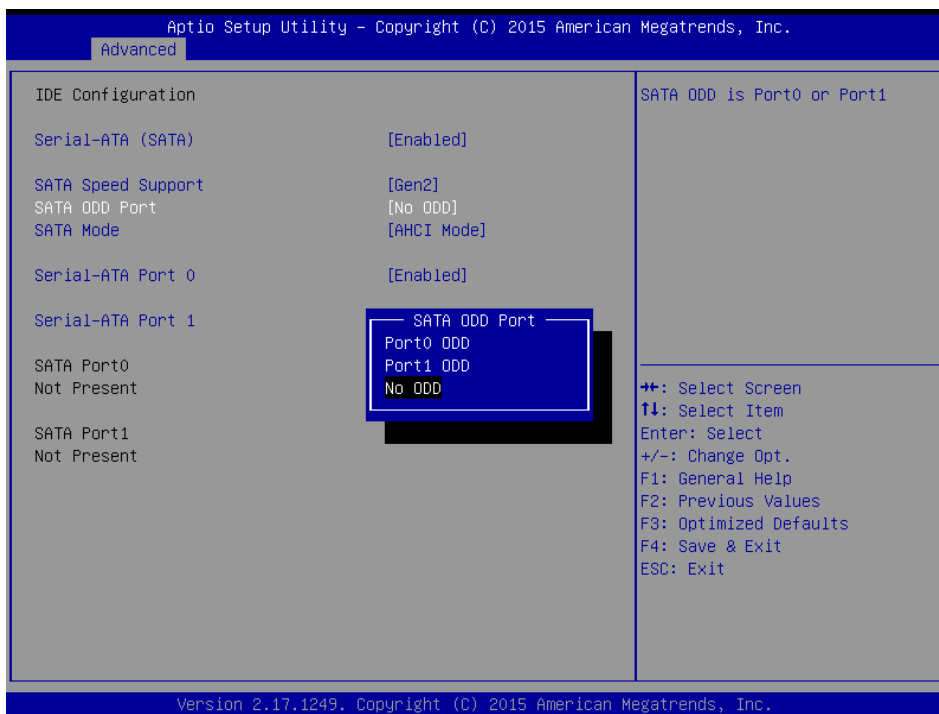
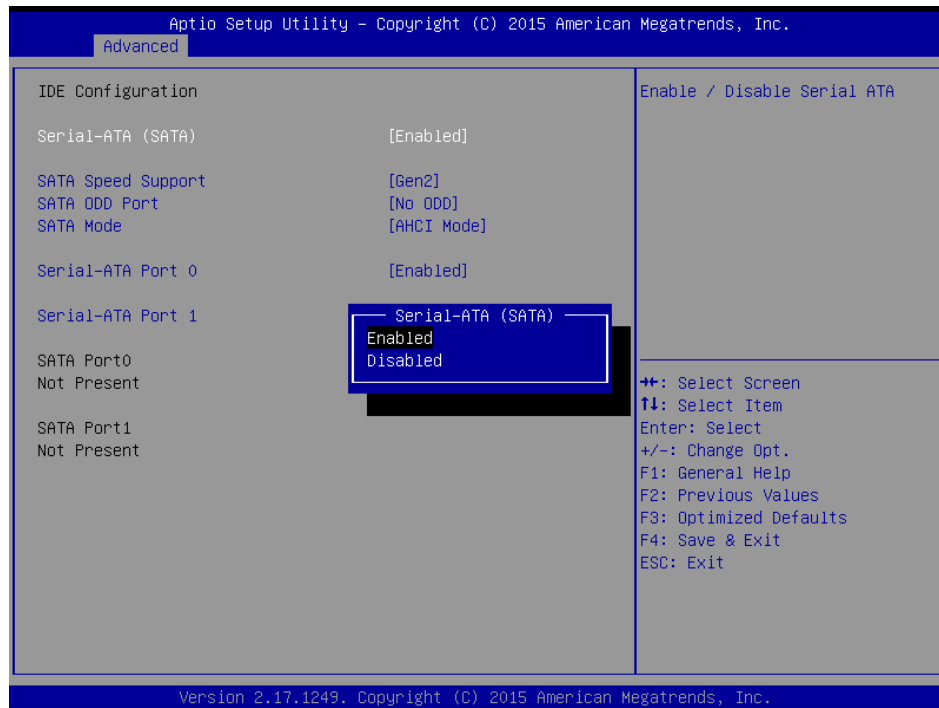


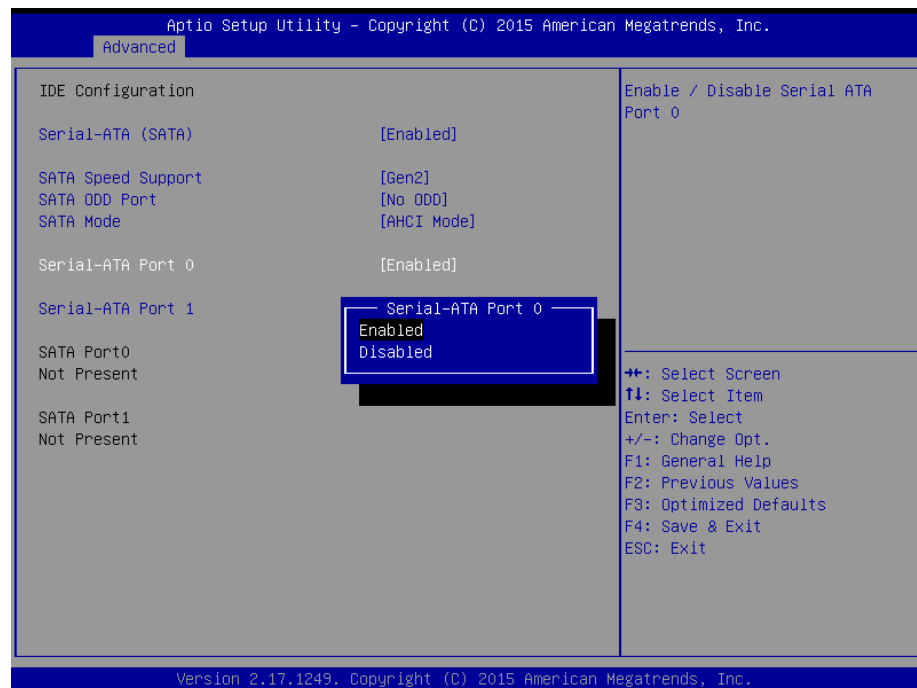
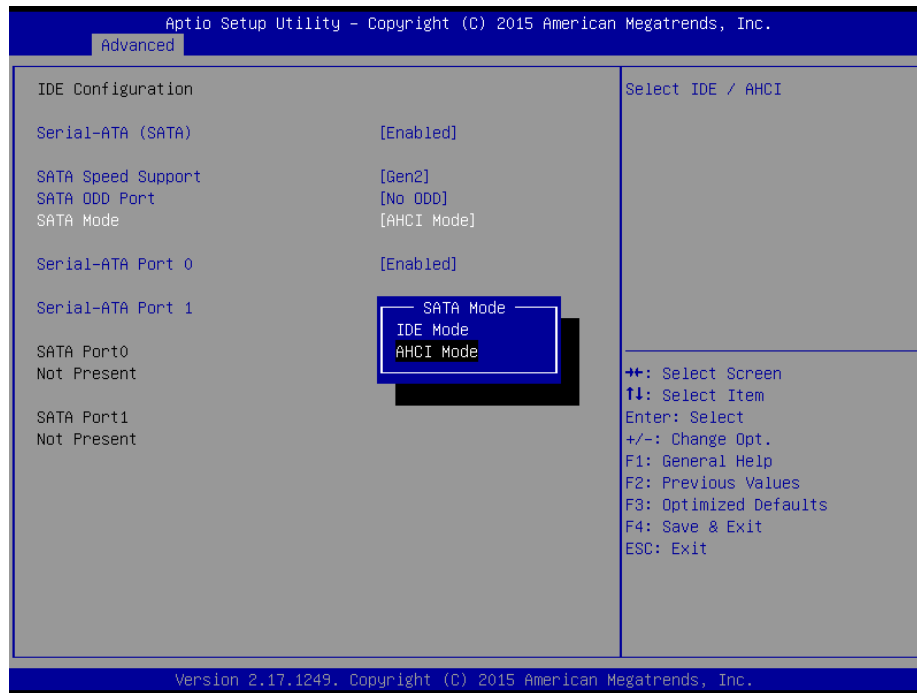


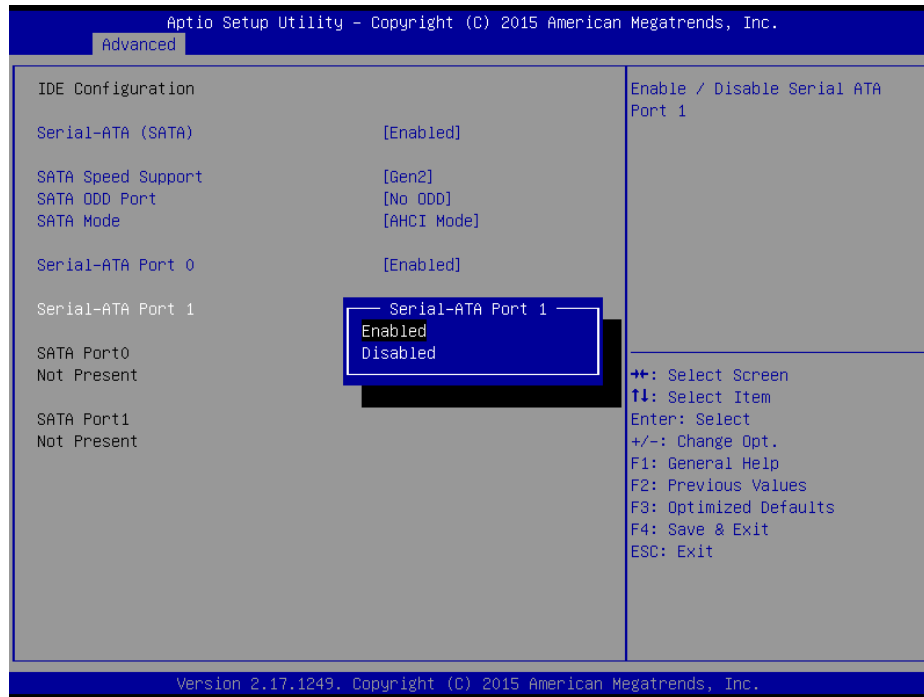
Item	Options	Description
CPU C state Report	Disabled [Default] , Enabled	Enable/Disable CPU C state report to OS.
Max CPU C-state	C1/C6/C7 [Default]	This option controls Max C state that the processor will support.

3.6.2.9 IDE Configuration



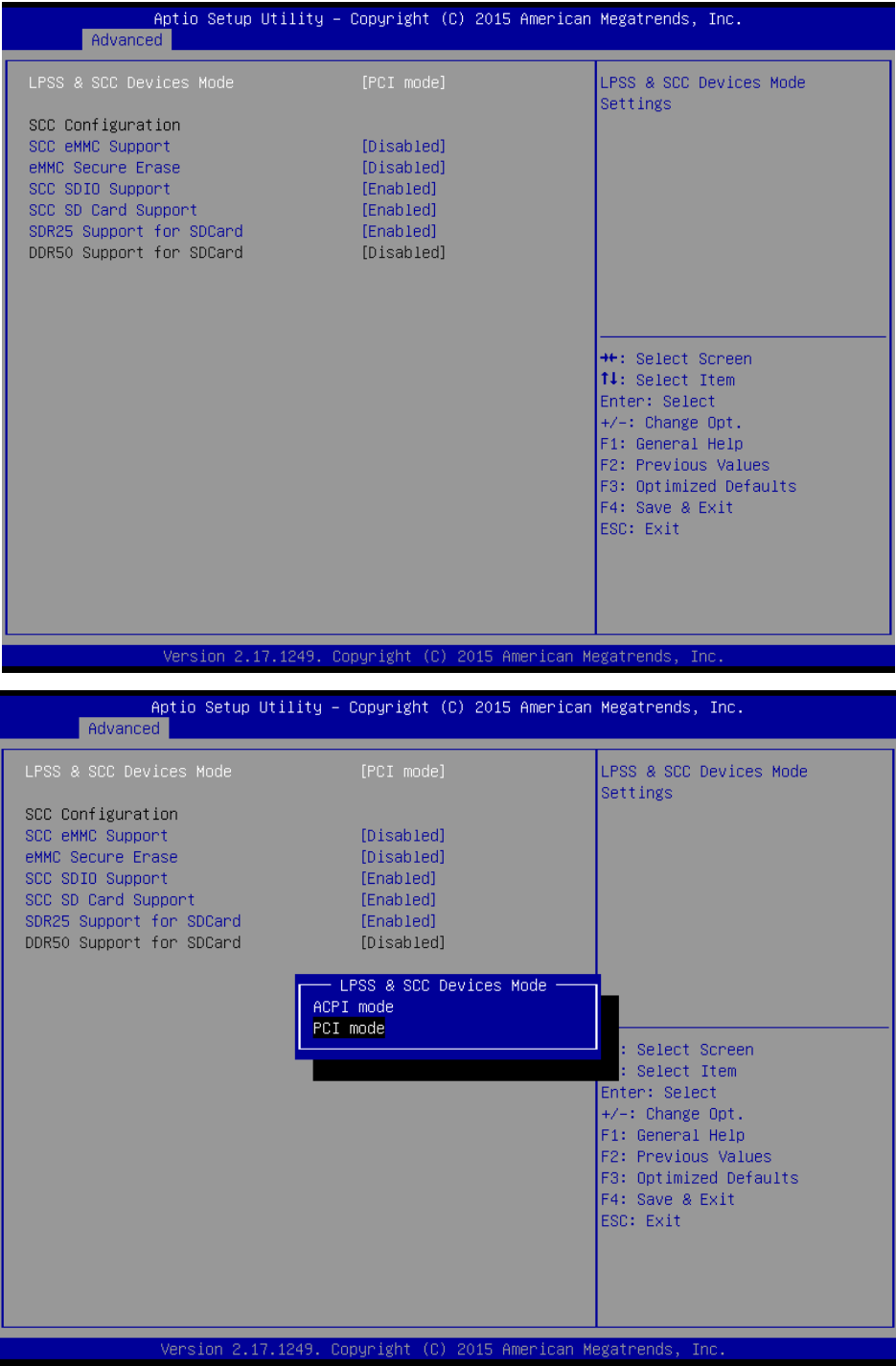


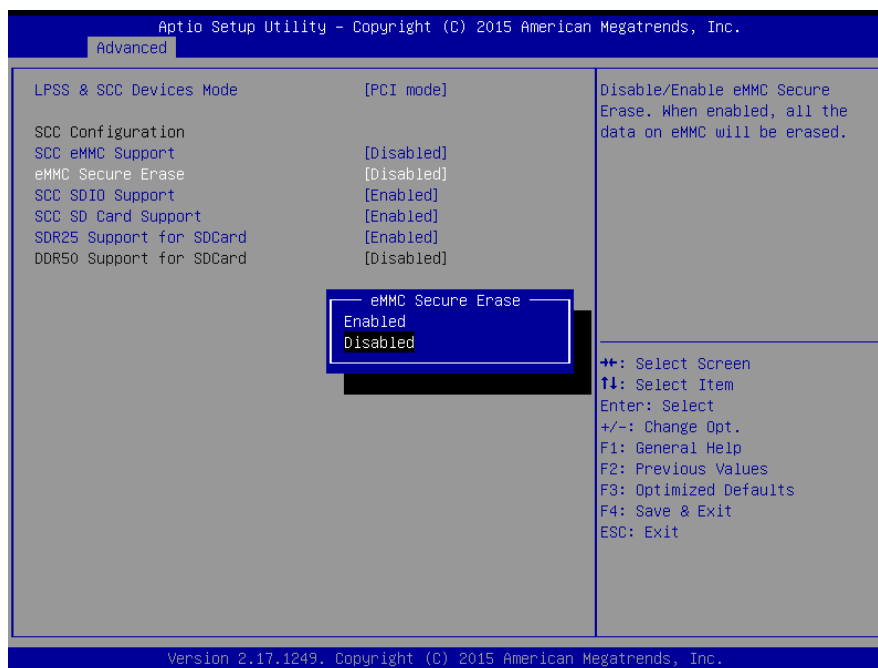
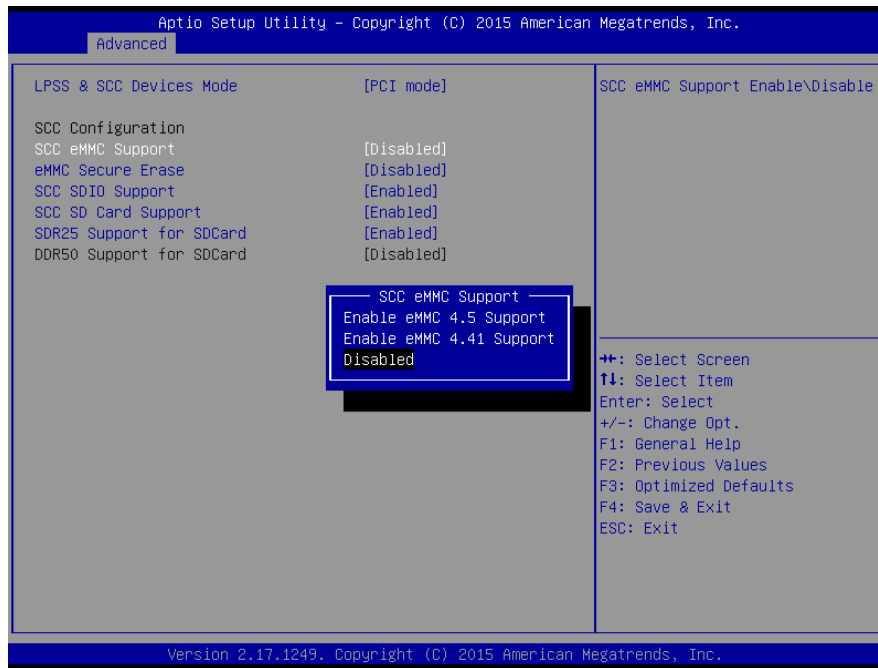


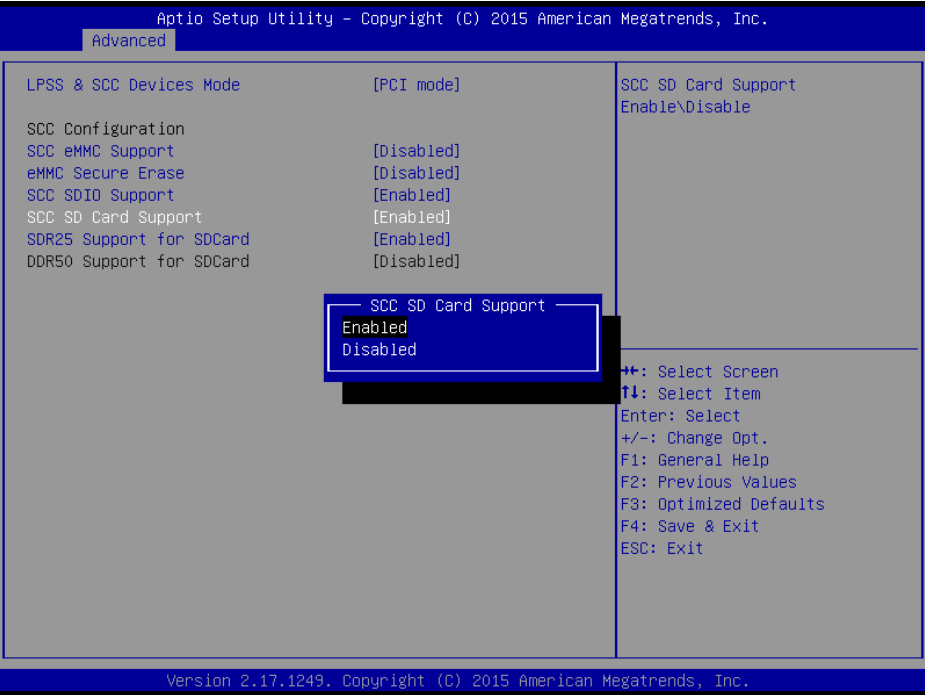
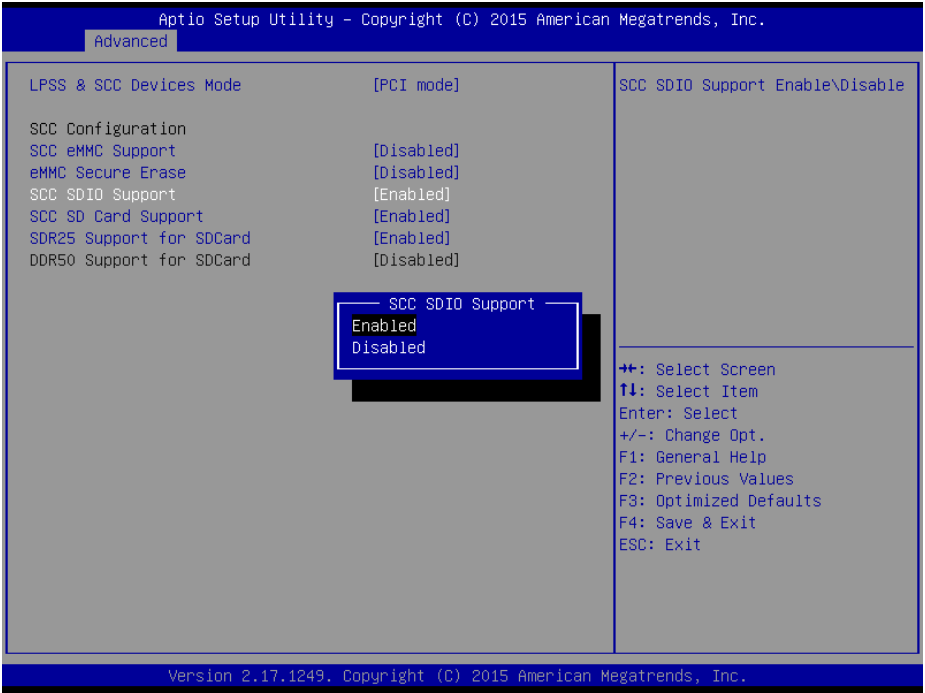


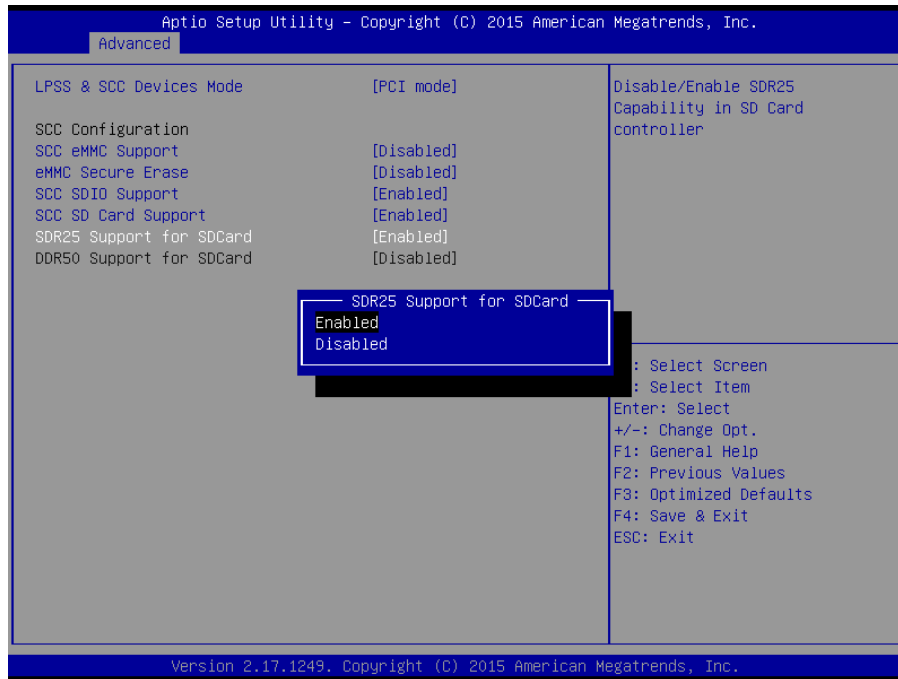
Item	Options	Description
Serial-ATA (SATA)	Enabled[Default] Disabled,	Enable/Disable Serial ATA.
SATA Speed Support	Gen1 Gen2[Default]	SATA Speed Support Gen1 or Gen2.
SATA ODD Port	Port0 ODD Port1 ODD No ODD[Default]	SATA ODD is Port0 or Port1.
SATA Mode	IDE Mode AHCI Mode[Default]	Select IDE/AHCI.
Serial-ATA Port 0/1	Enabled[Default] Disabled,	Enable/Disable Serial ATA Port0/1.

3.6.2.10 LPSS & SCC Configuration



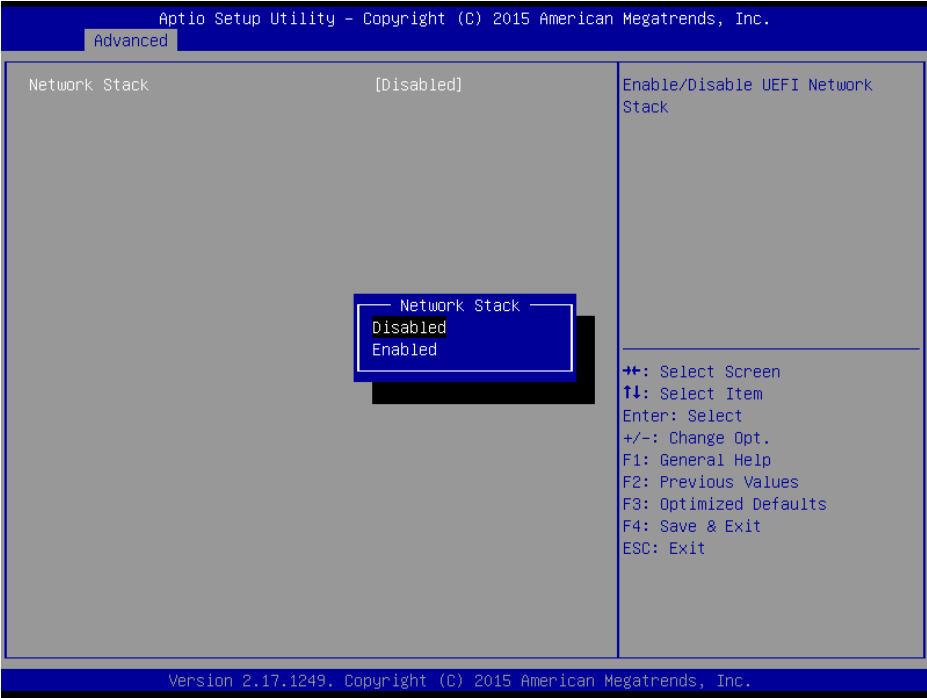
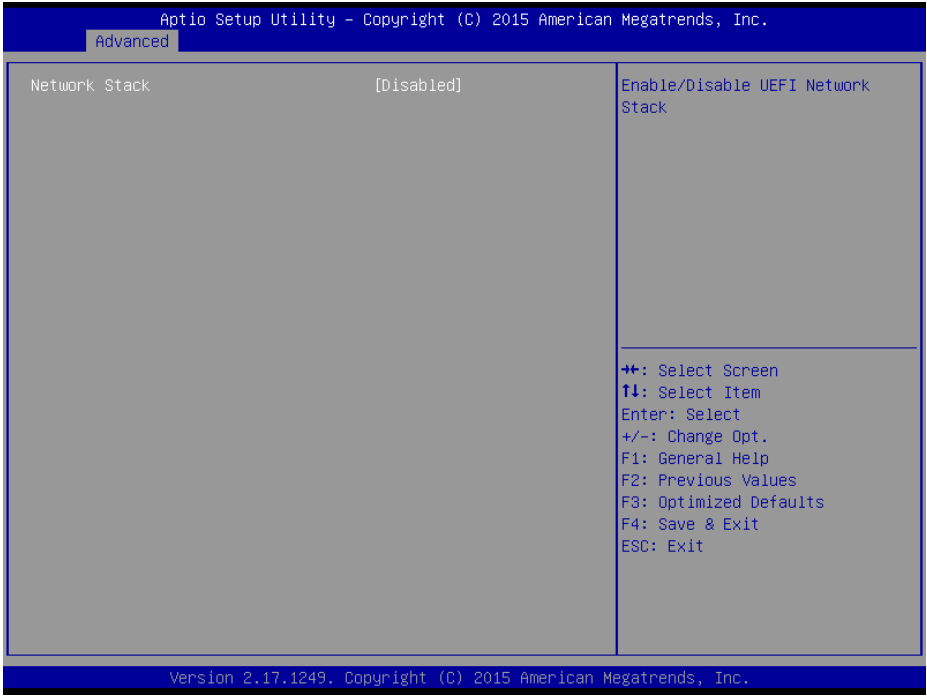






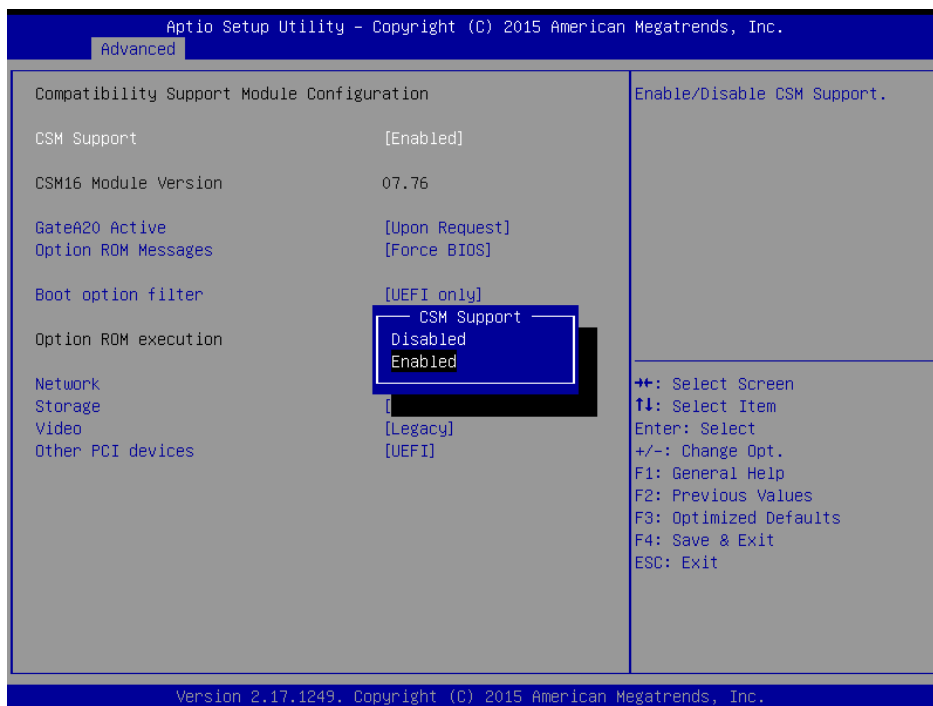
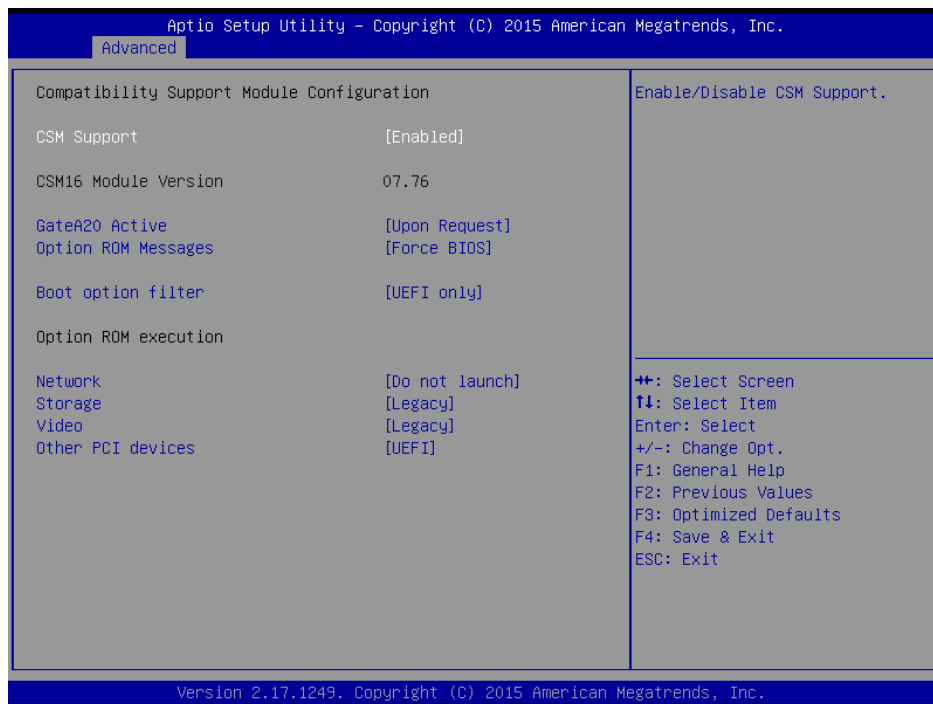
Item	Options	Description
LPSS & SCC Devices Mode	ACPI mode PCI mode[Default]	LPSS SCC Device Mode Settings.
SCC eMMC Support	Enable eMMC 4.5 Support Enable eMMC 4.41 Support Disabled[Default]	SATA Speed Support Gen1 or Gen2.
eMMC Secure Erase	Enabled Disabled[Default]	Disable/Enable eMMC Secure Erase. When enabled, all the data on eMMC will be erased.
SCC SDIO Support	Enabled[Default] Disabled,	SCC SDIO Support Enable/Disable.
SCC SD Card Support	Enabled[Default] Disabled,	SCC SD Card Support Enable/Disable.
SDR25 Support for SDCard	Enabled[Default] Disabled,	Disable/Enable SDR25 Capability in SD Card controller.

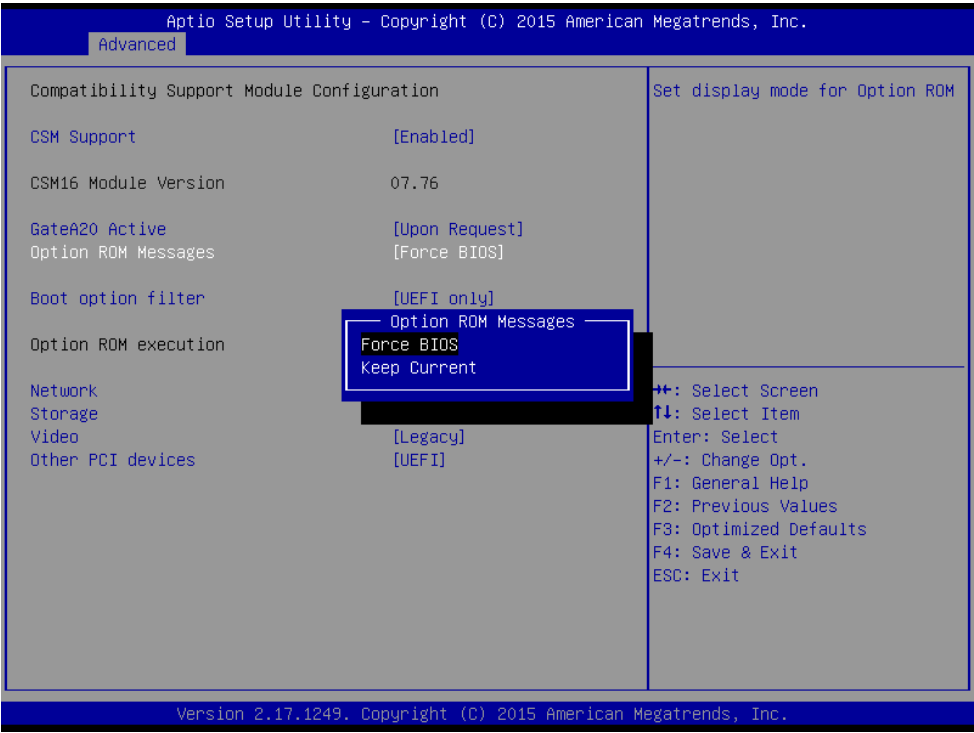
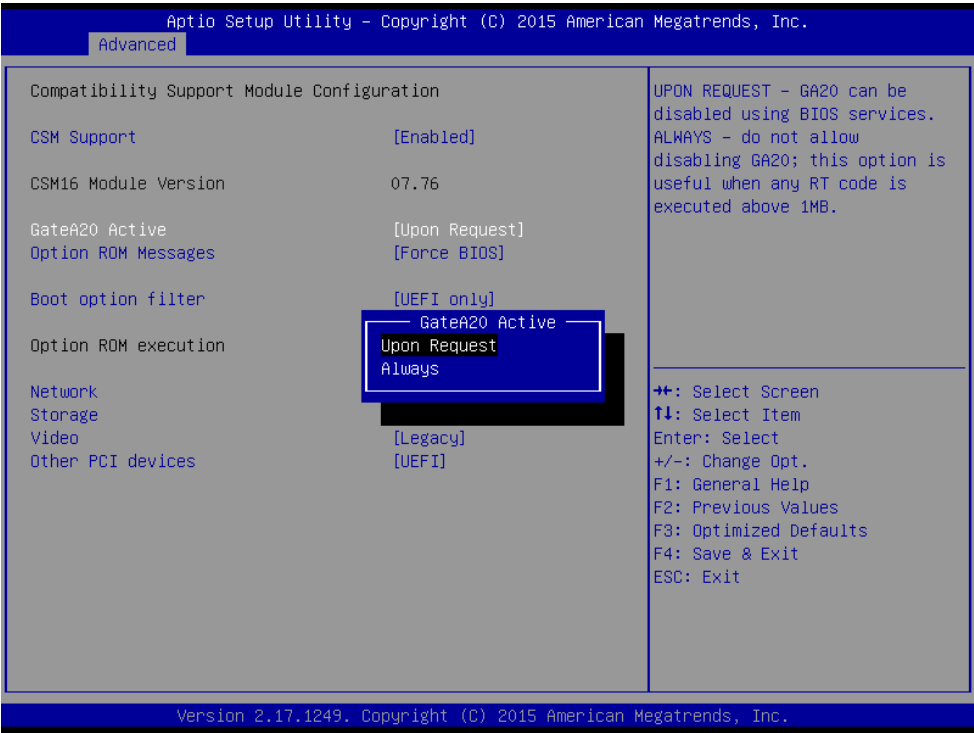
3.6.2.11 Network Stack Configuration

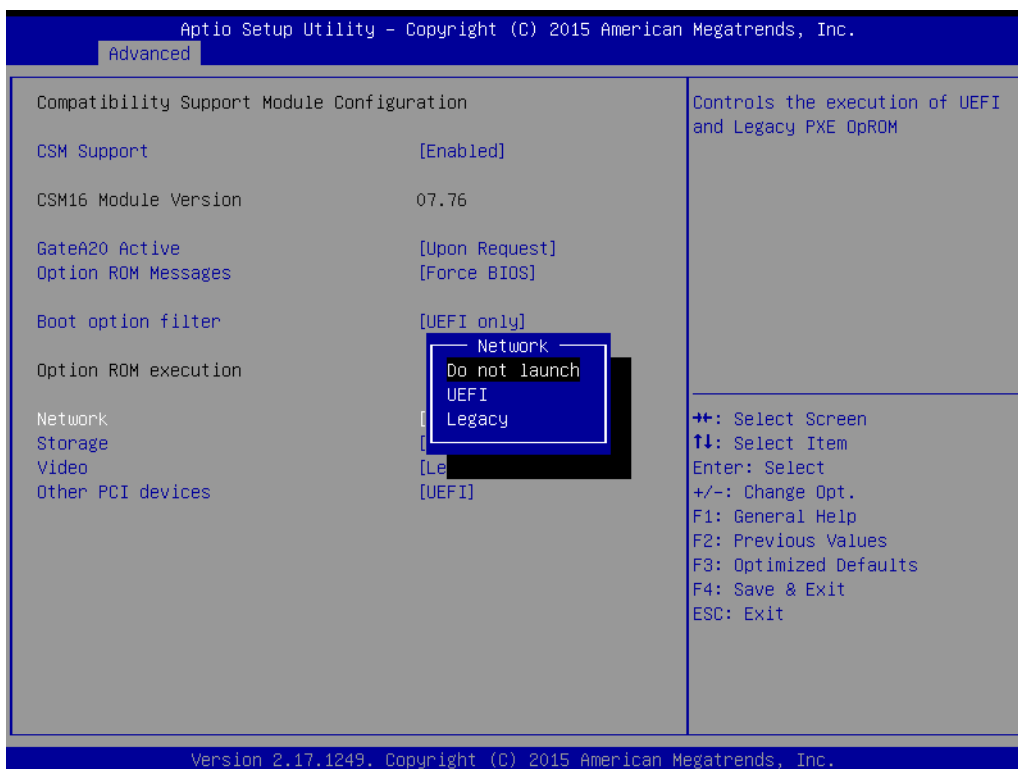
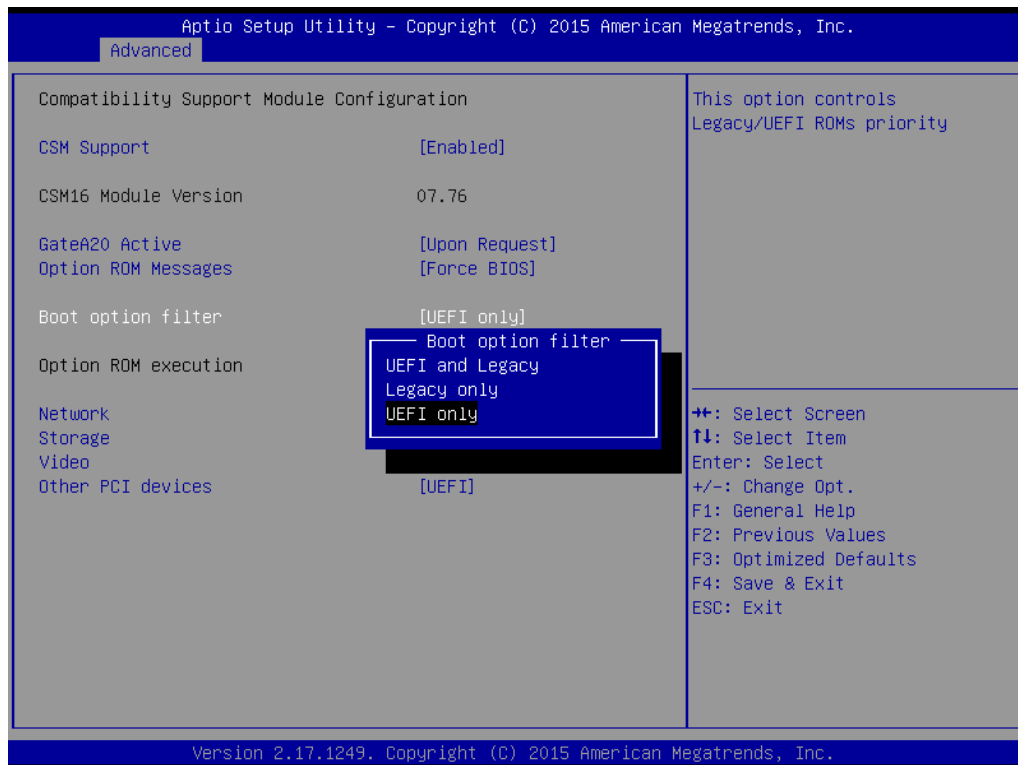


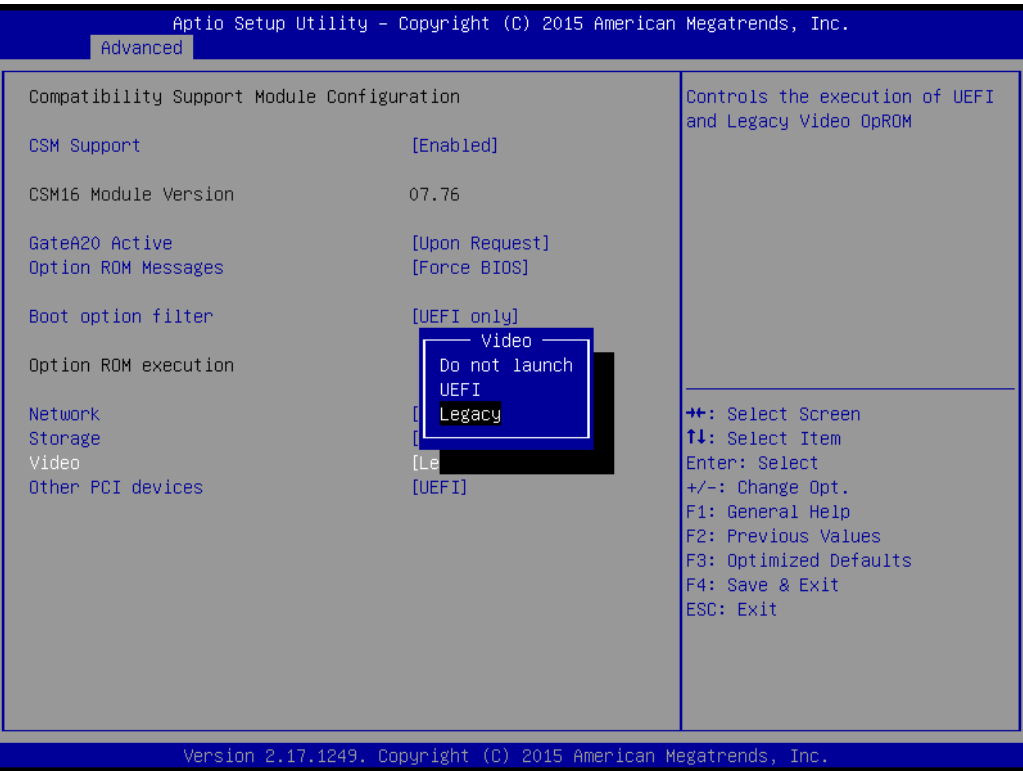
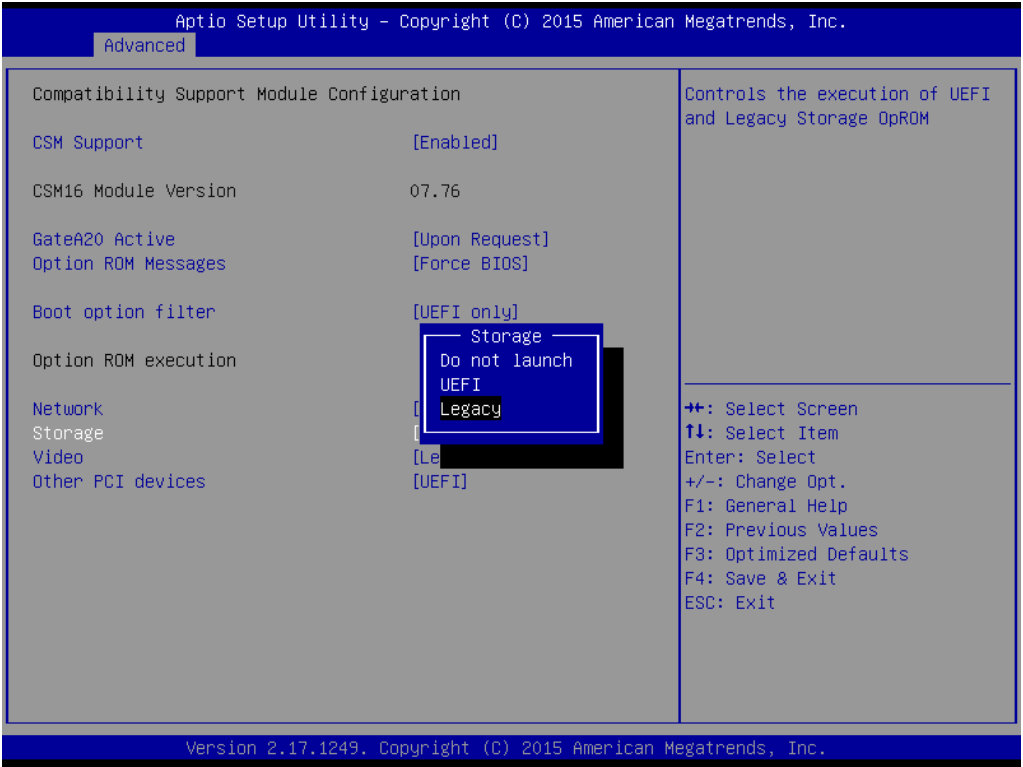
Item	Options	Description
Network Stack	Enabled Disabled [Default]	Enable/Disable UEFI Network Stack.

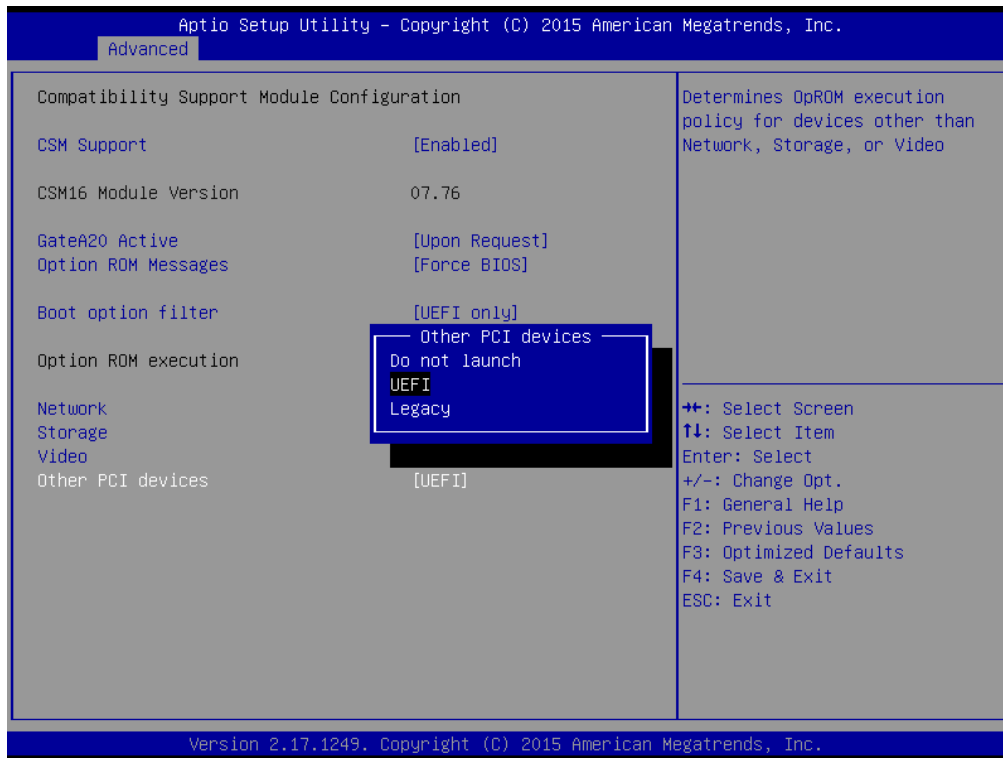
3.6.2.12 CSM Configuration





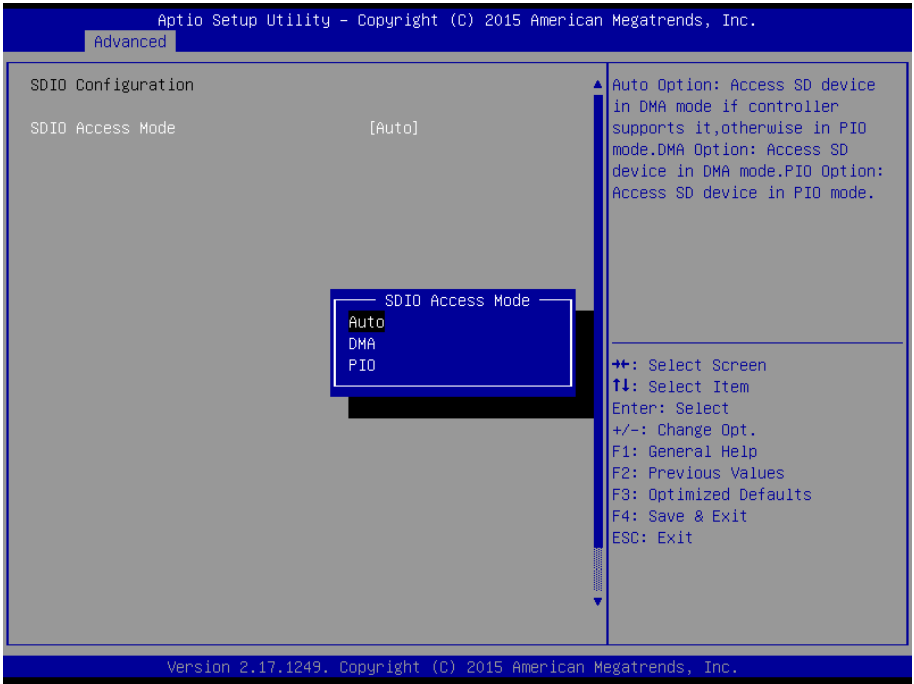
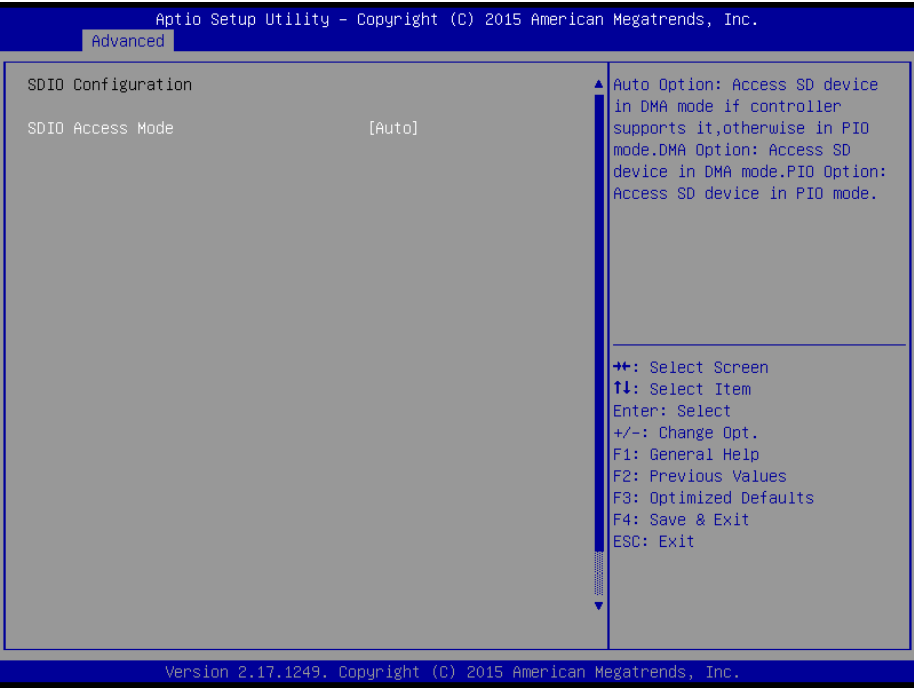






Item	Options	Description
CSM Support	Enabled[Default] Disabled,	Enable/Disable CSM Support.
GateA20 Active	Upon Request[Default] Always	UPON REQUEST – GA20 can be disabled using BIOS services. ALWAYS – go not allow disabling GA20; this option is useful when any RT code is executed above 1MB.
Option ROM Messages	Force BIOS[Default] Keep Current	Set display mode for Option ROM.
Boot option filter	UEFI and Legacy Legacy only[Default] UEFI only	This option controls Legacy/UEFI ROMs priority.
Network	Do not launch[Default] UEFI Legacy	Controls the execution of UEFI and Legacy PXE OpROM.
Storage	Do not launch UEFI Legacy[Default]	Controls the execution of UEFI and Legacy Storage OpROM.
Video	Do not launch UEFI Legacy[Default]	Controls the execution of UEFI and Legacy Video OpROM.
Other PCI devices	Do not launch UEFI[Default] Legacy	Determines OpROM execution policy for devices other than Network, Storage, or Video.

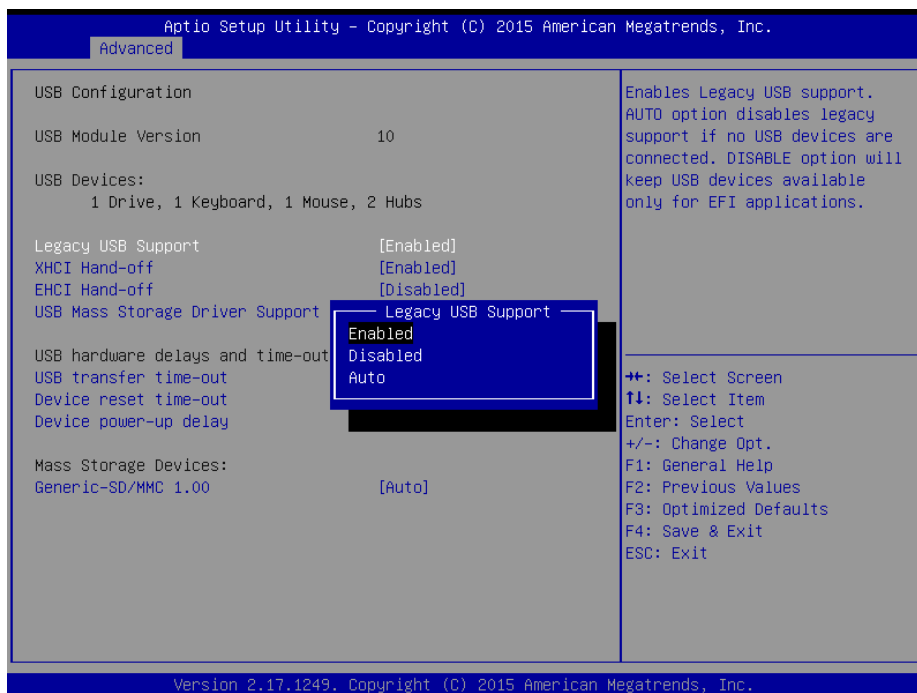
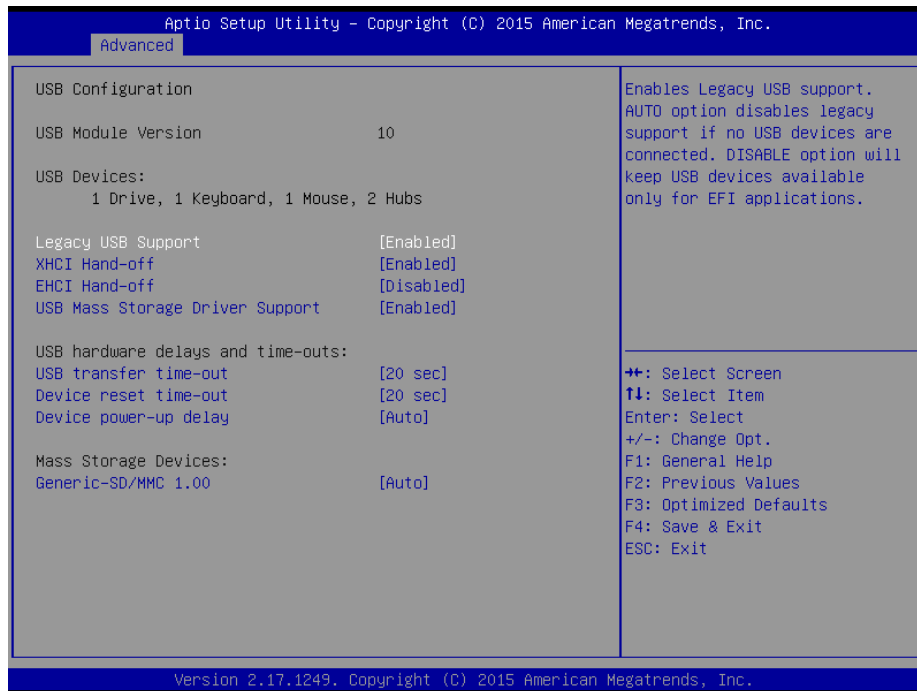
3.6.2.13 SDIO Configuration

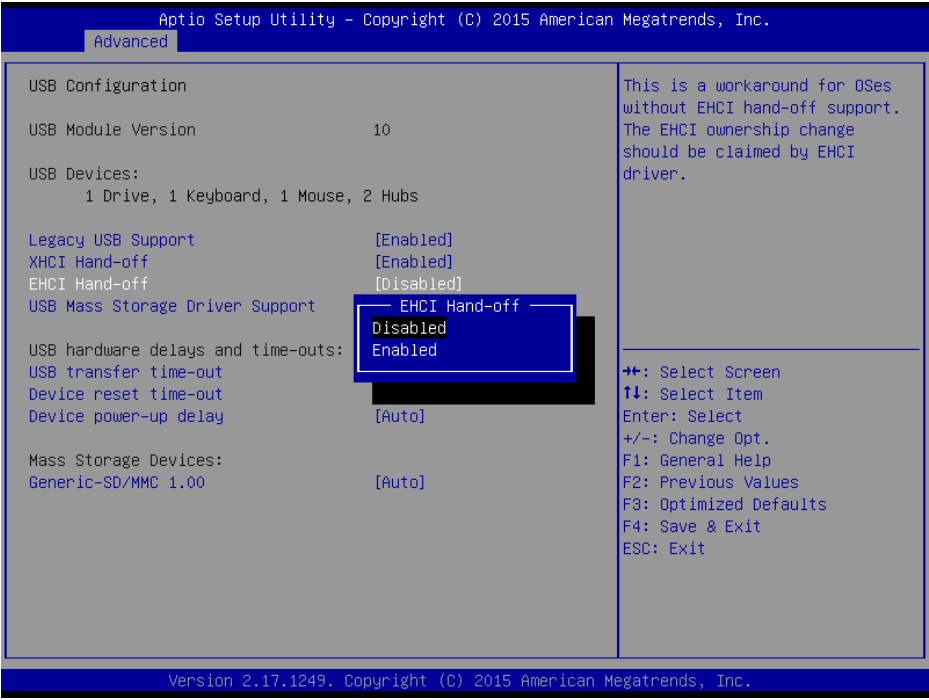
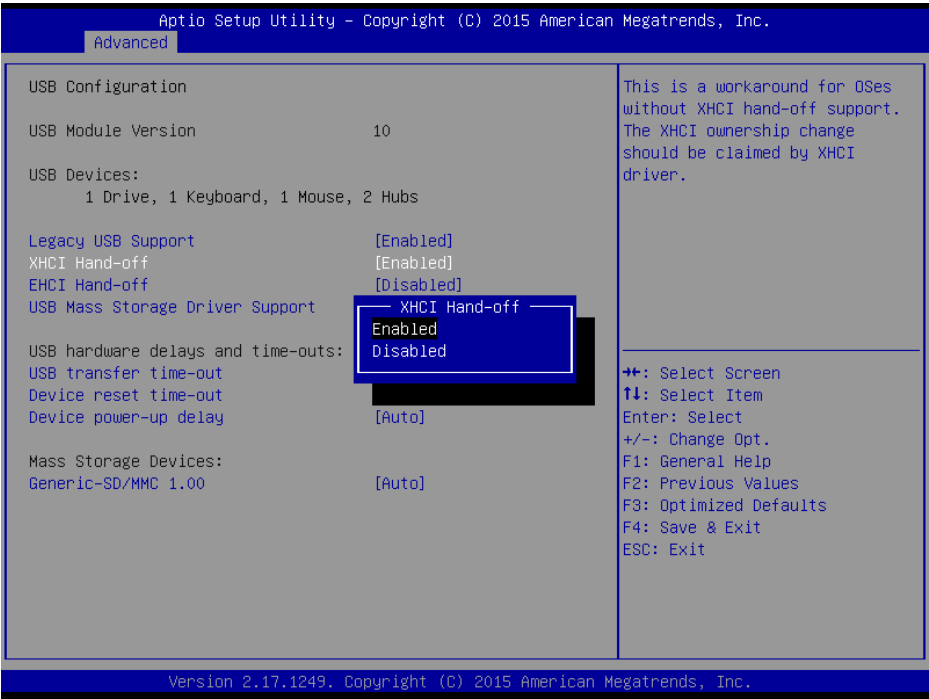


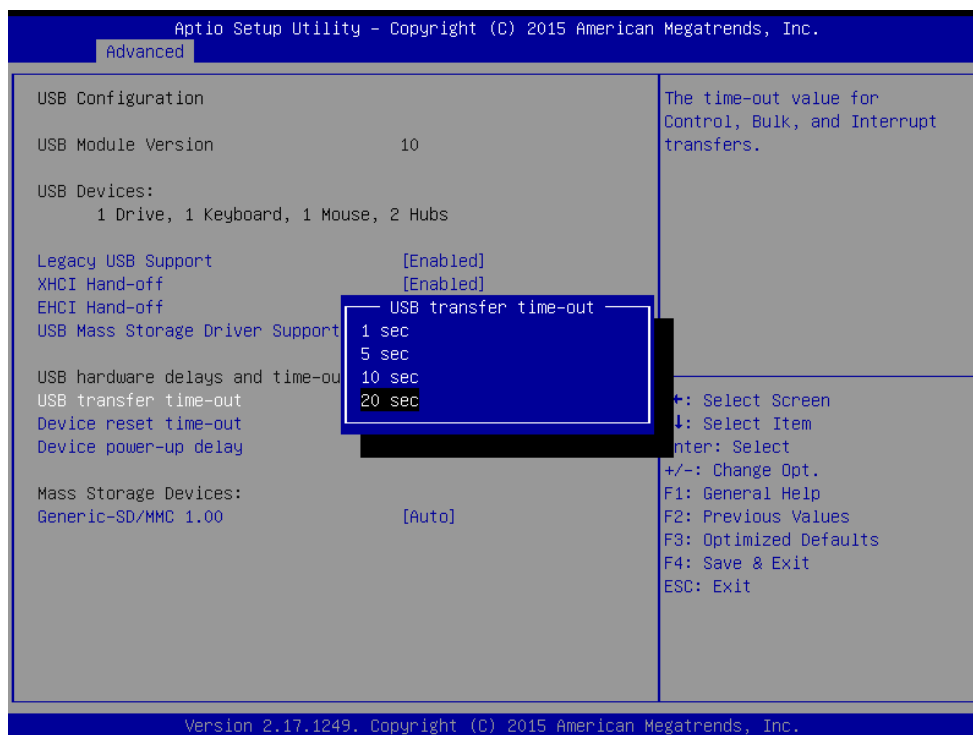
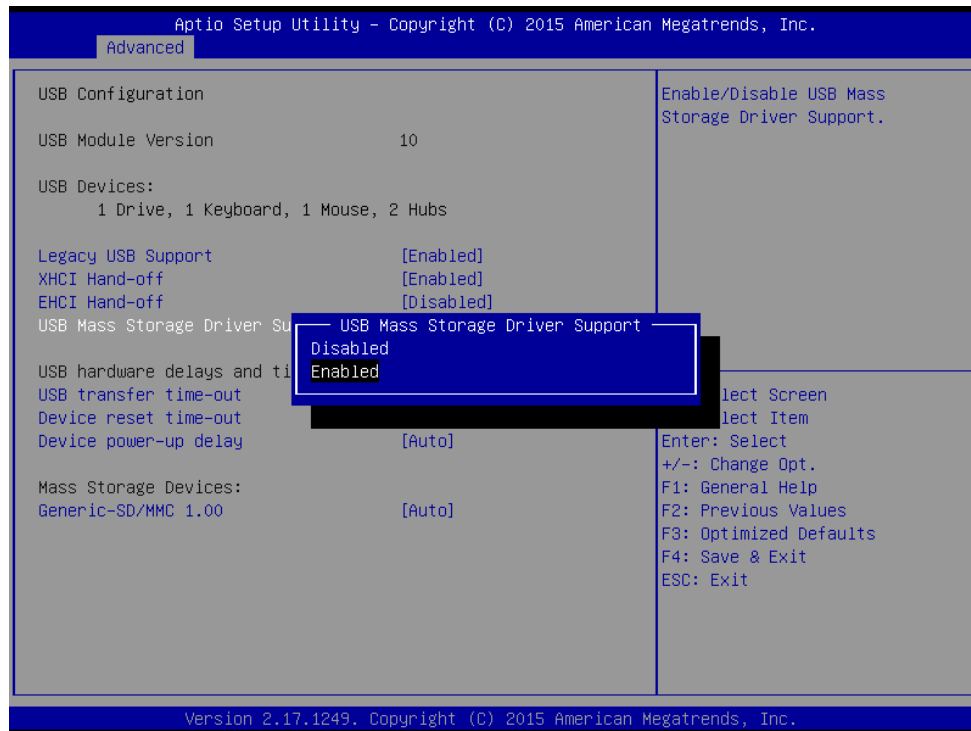
Item	Options	Description
SDIO Access Mode	Auto[Default] DMA PIO	Auto Option: Access SD device in DMA mode if controller supports it , otherwise in PIO mode. DMA Option: Access SD device in DMA mode. PIO Option: Access SD device in PIO mode.

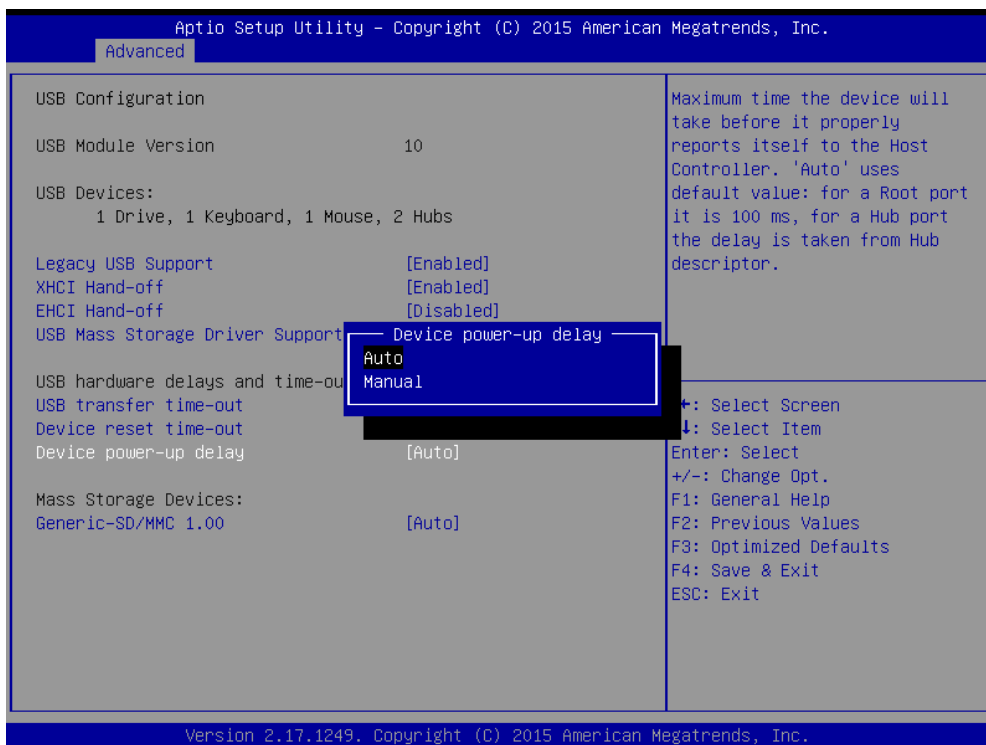
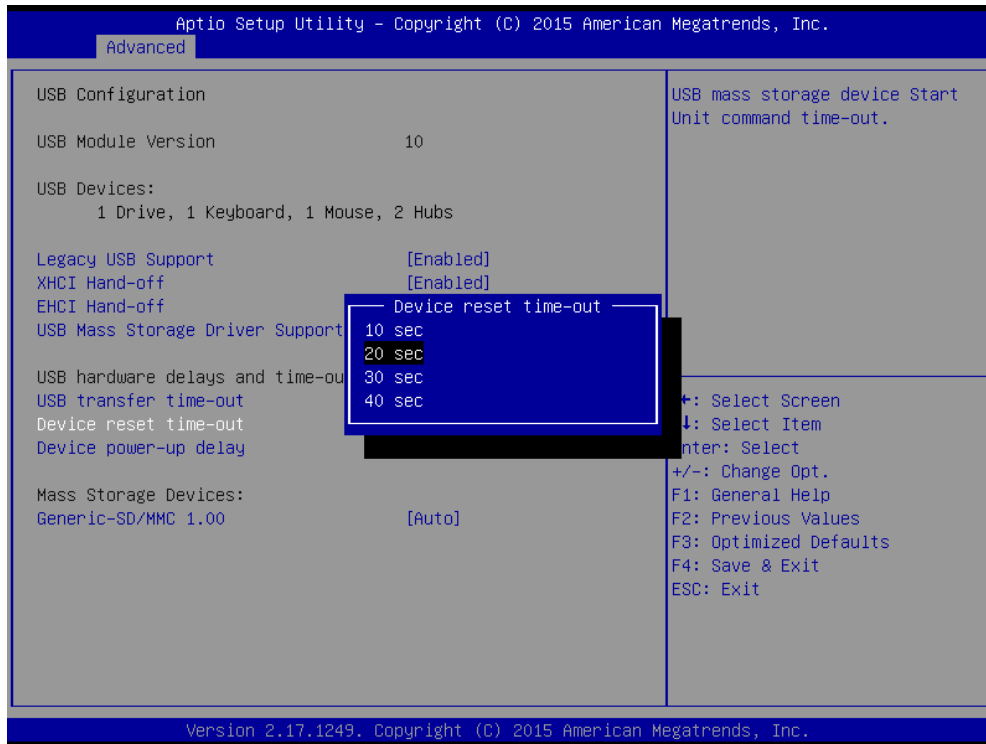
3.6.2.14 USB Configuration

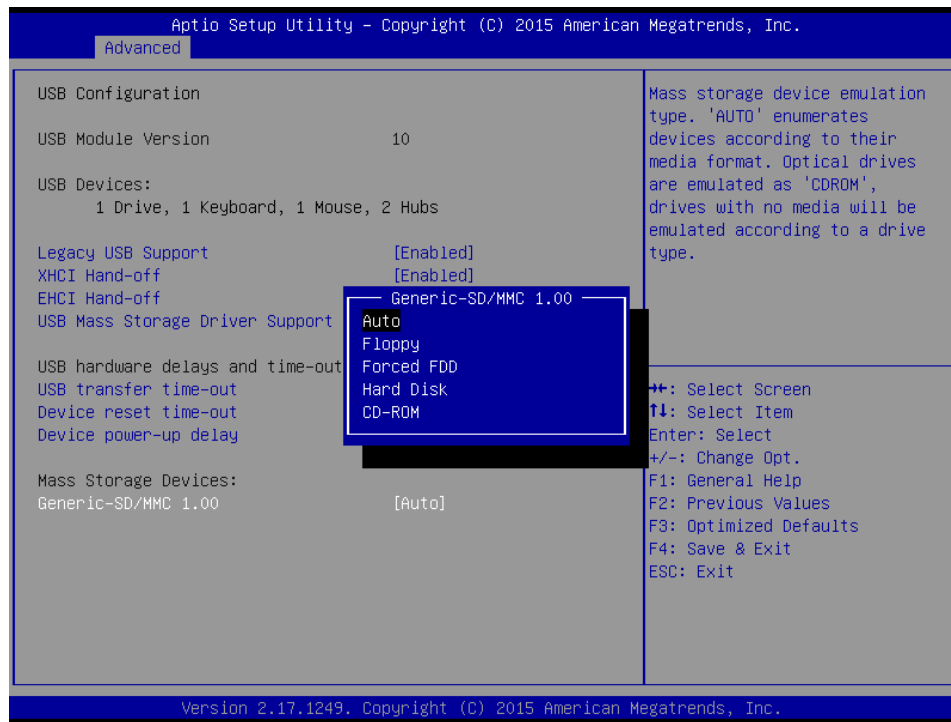
The USB Configuration menu helps read USB information and configures USB settings.





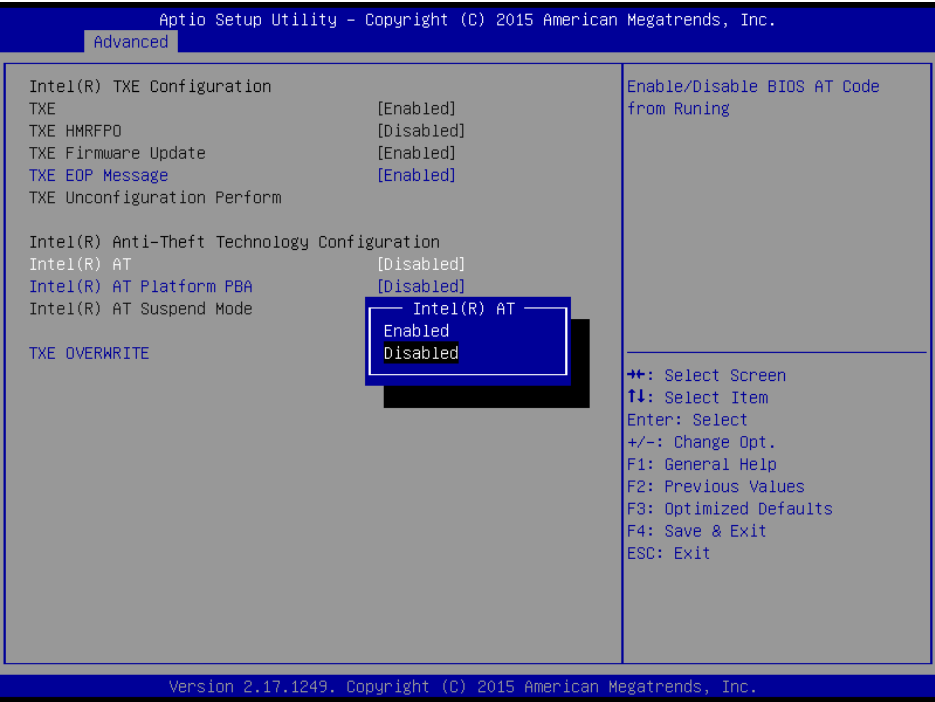
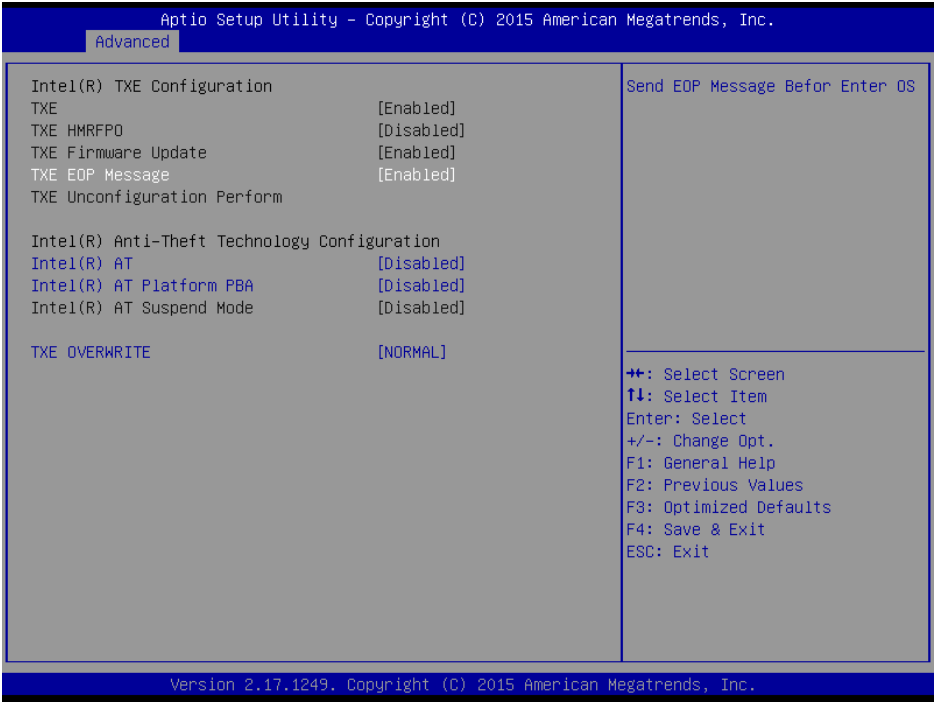




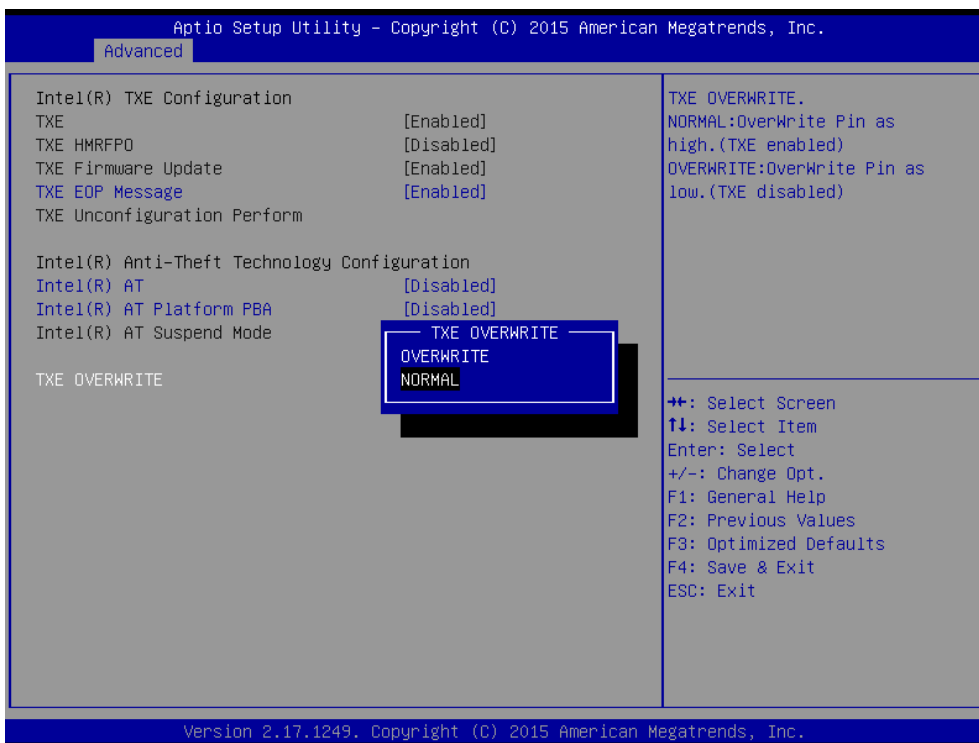
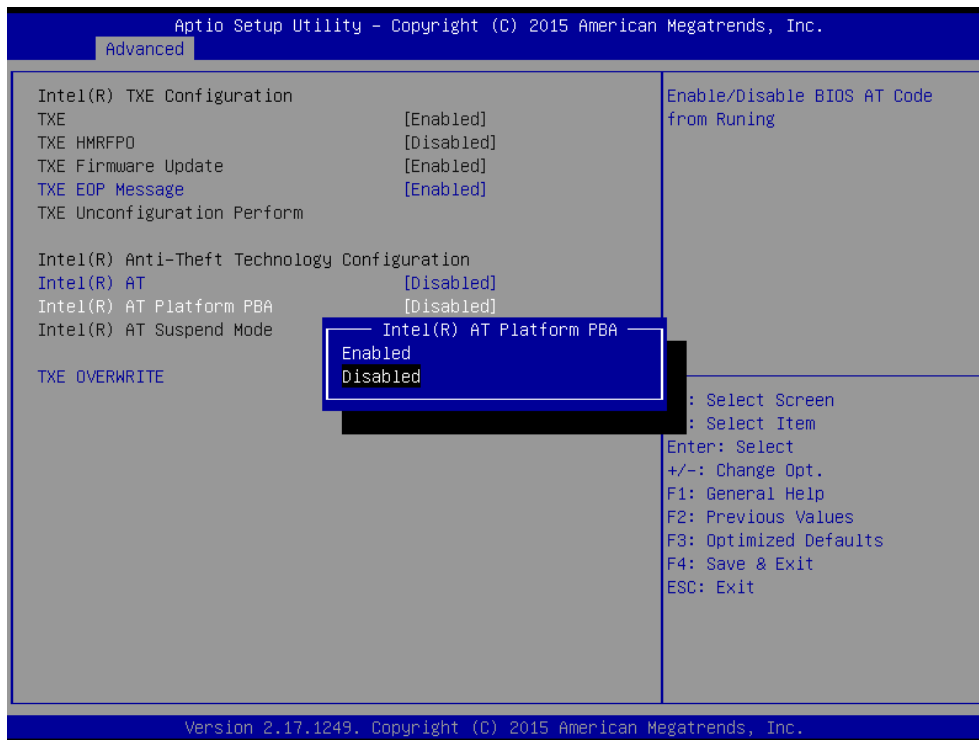


Item	Options	Description
Legacy USB Support	Enabled[Default] Disabled Auto	Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications.
XHCI Hand-off	Enabled[Default] Disabled	This is a workaround for OSeW without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
EHCI Hand-off	Enabled Disabled[Default]	This is a workaround for OSeS without EHCI hand-off support. The EHCI ownership change should be claimed by EHCI driver.
USB Mass Storage Driver Support	Enabled[Default] Disabled	Enable/Disable USB Mass Storage Driver Support.
USB transfer time-out	1 sec 5 sec 10 sec 20 sec[Default]	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	10 sec 20 sec[Default] 30 sec 40 sec	USB mass storage device Start Unit command time-out.
Device power-up delay	Auto[Default] Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100ms, for a Hub port the delay is taken form Hub descriptor.
Generic-SD/MMC 1.00	Auto[Default] Floppy Forced FDD Hard Disk CD-ROM	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type.

3.6.2.15 Security Configuration

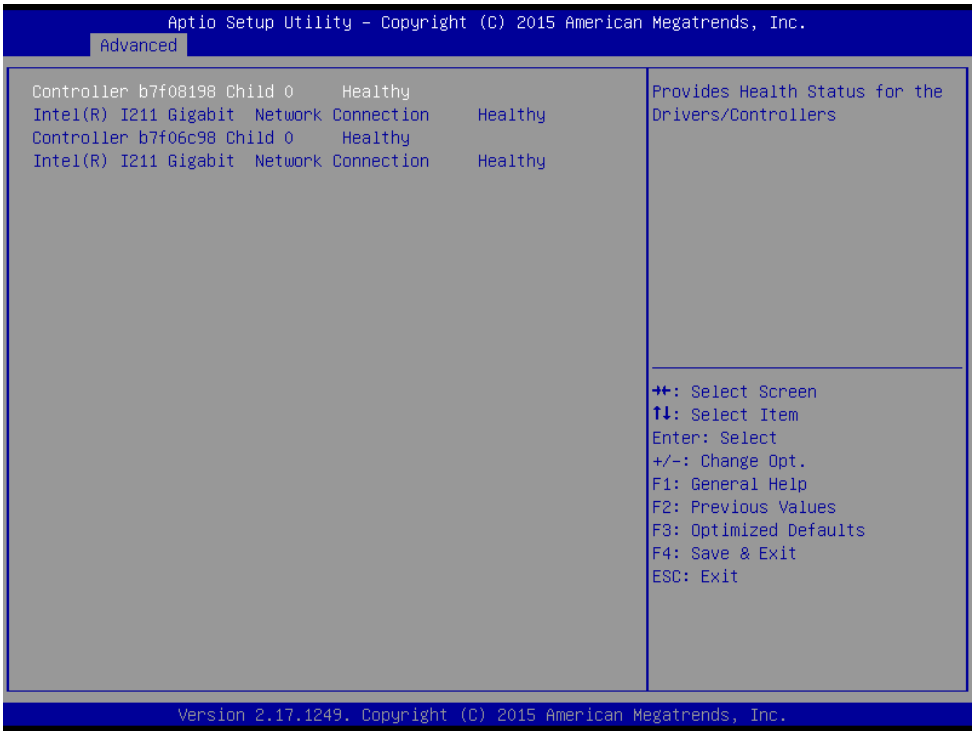
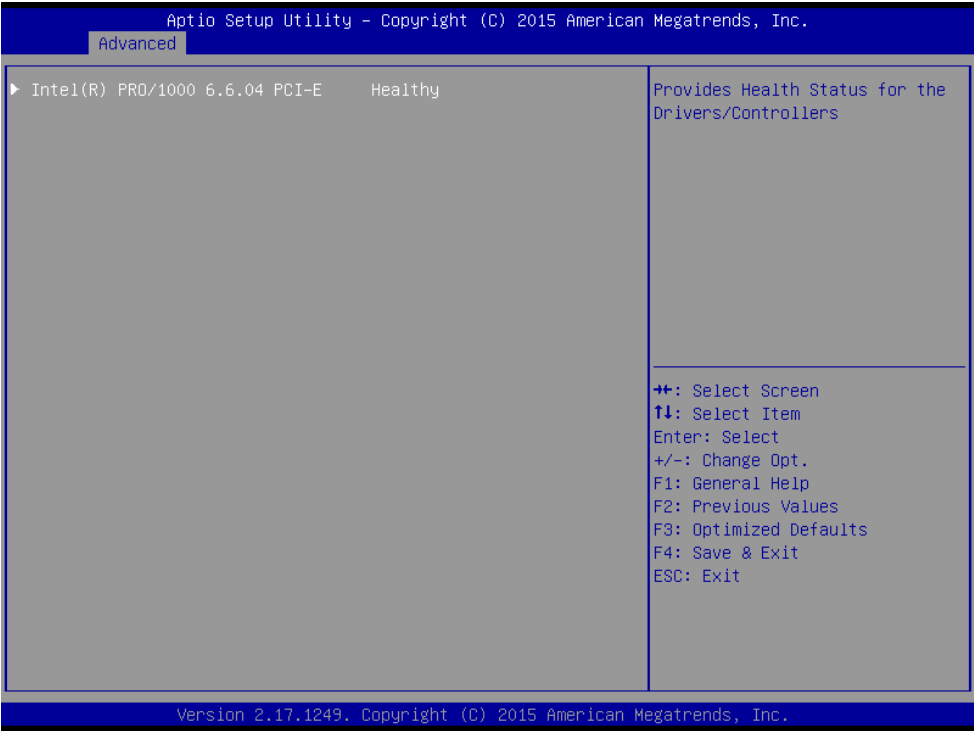


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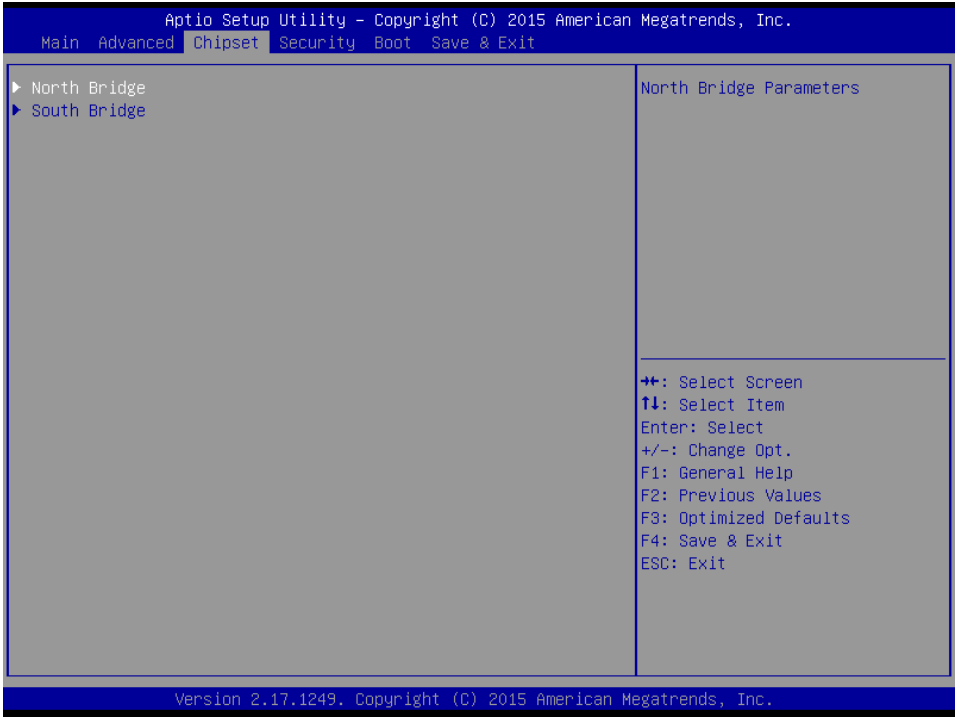


Item	Options	Description
TXE EOP Message	Disabled Enabled[Default],	Send EOP Message Before Enter OS.
Intel® AT	Disabled[Default] Enabled,	Enable/Disable BIOS AT Code from Running.
Inter® AT Platform PBA	Disabled[Default] Enabled	Enable/Disable BIOS AT Code from Running.
TXE OVERWRITE	OVERWRITE NORMAL[Default]	TXE OVERWRITE. NORMAL: OverWrite Pin as high. (TXE enabled) OVERWRITE:OverWrite Pin as low. (TXE disabled).

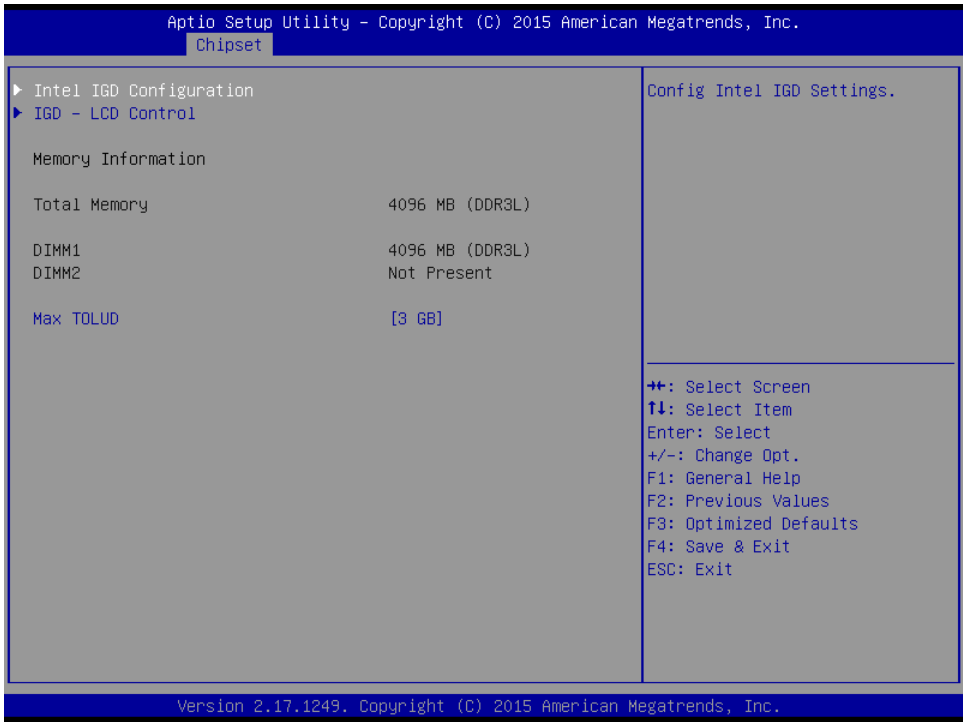
3.6.2.16 Lan driver report status

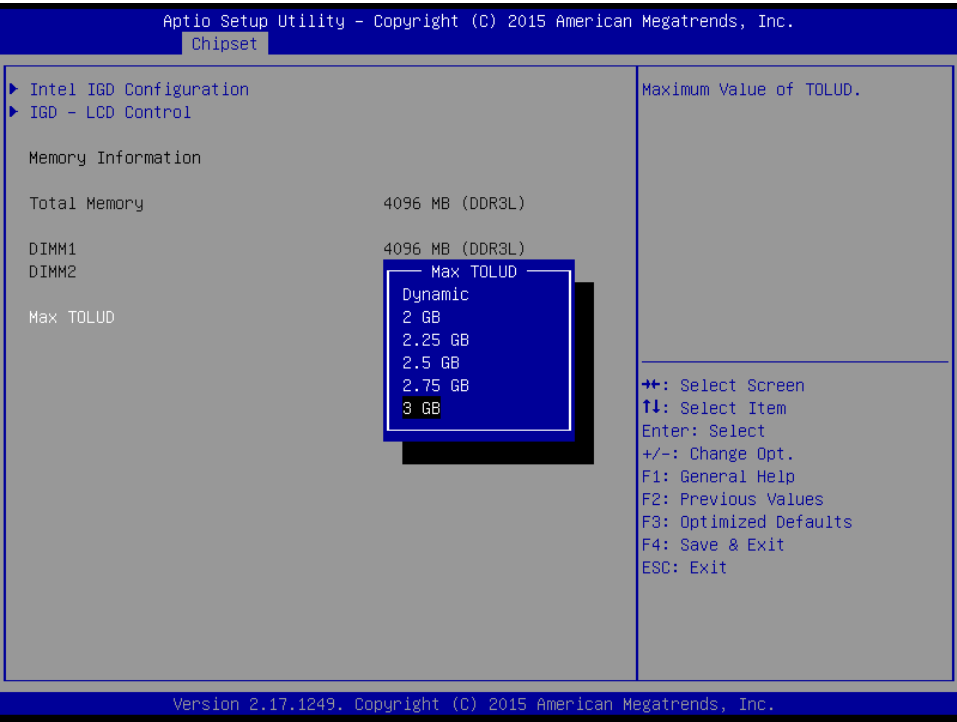


3.6.3 Chipset



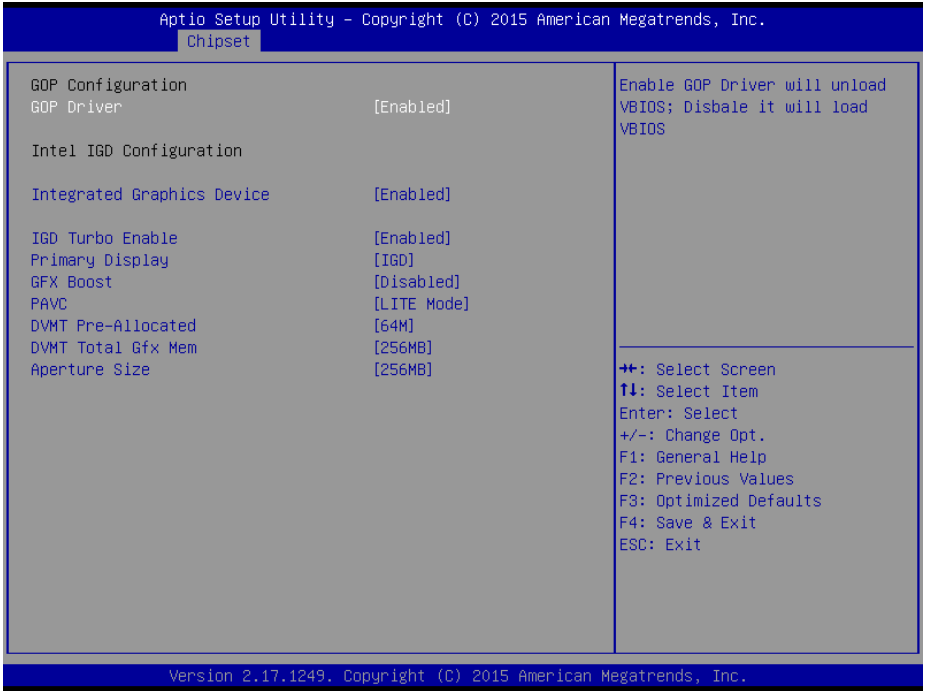
3.6.3.1 North Bridge

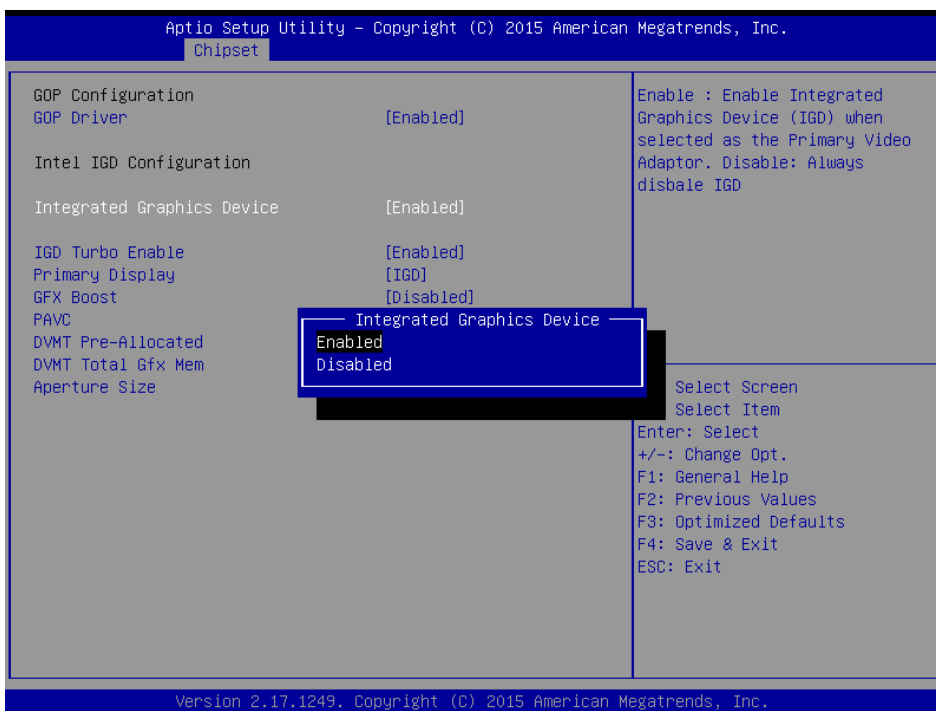
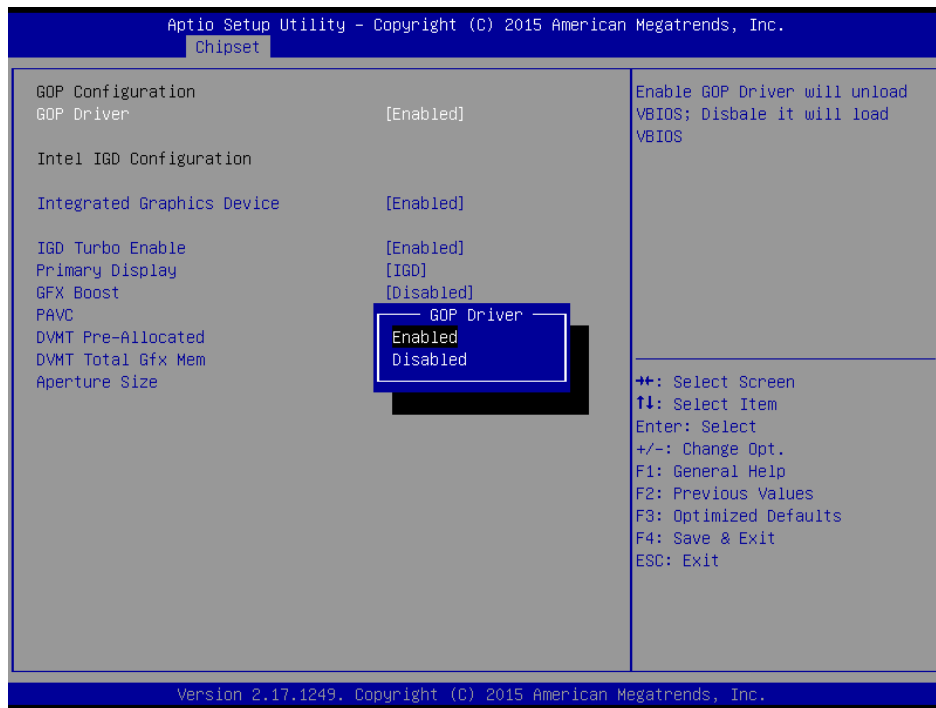


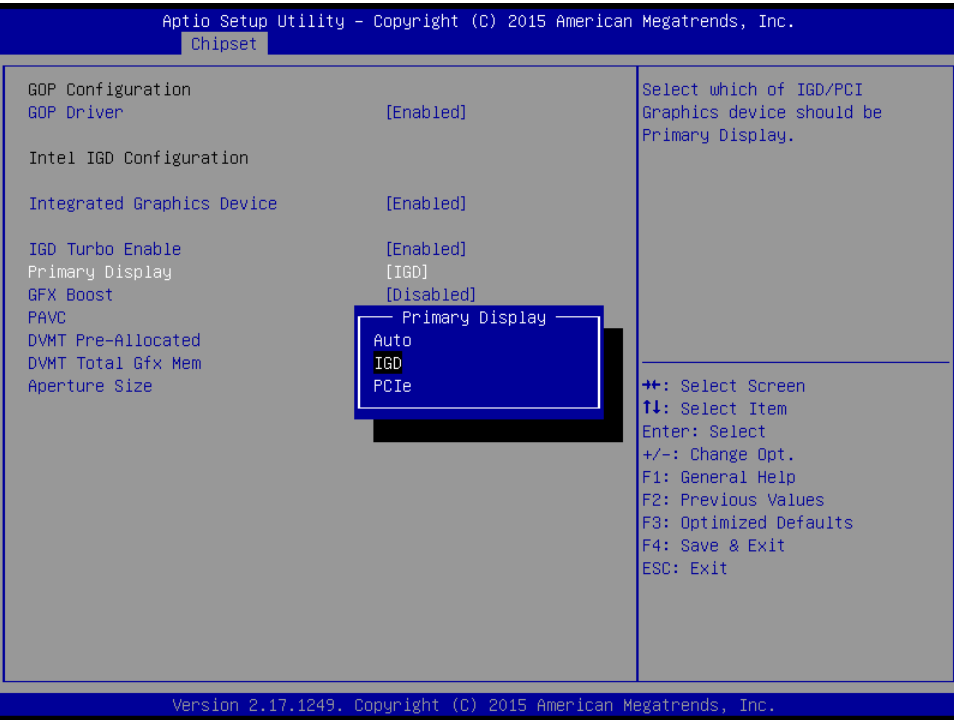
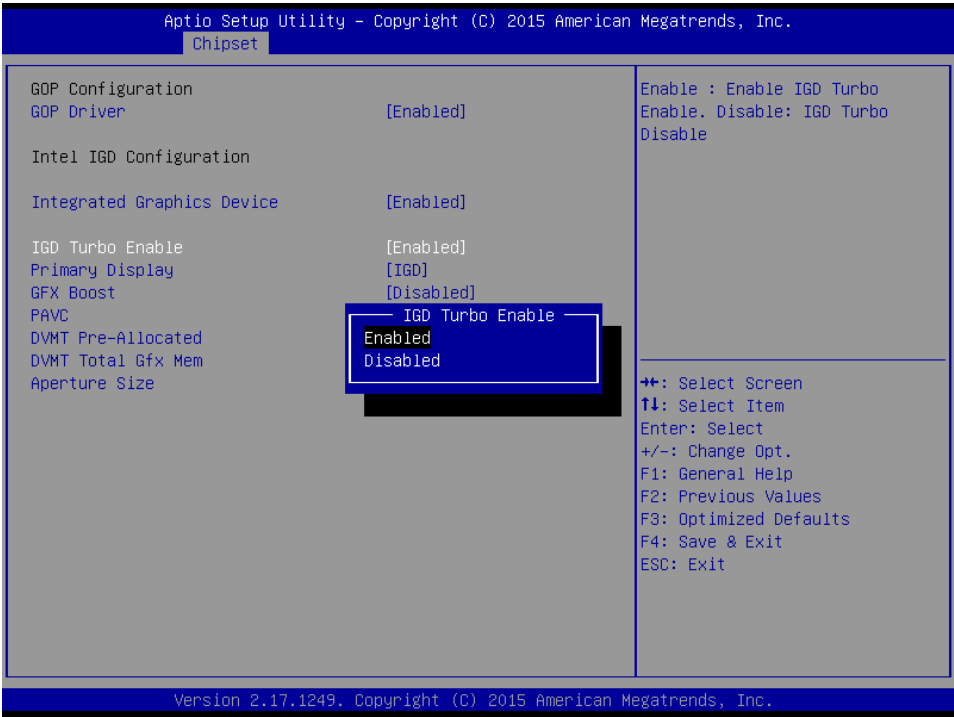


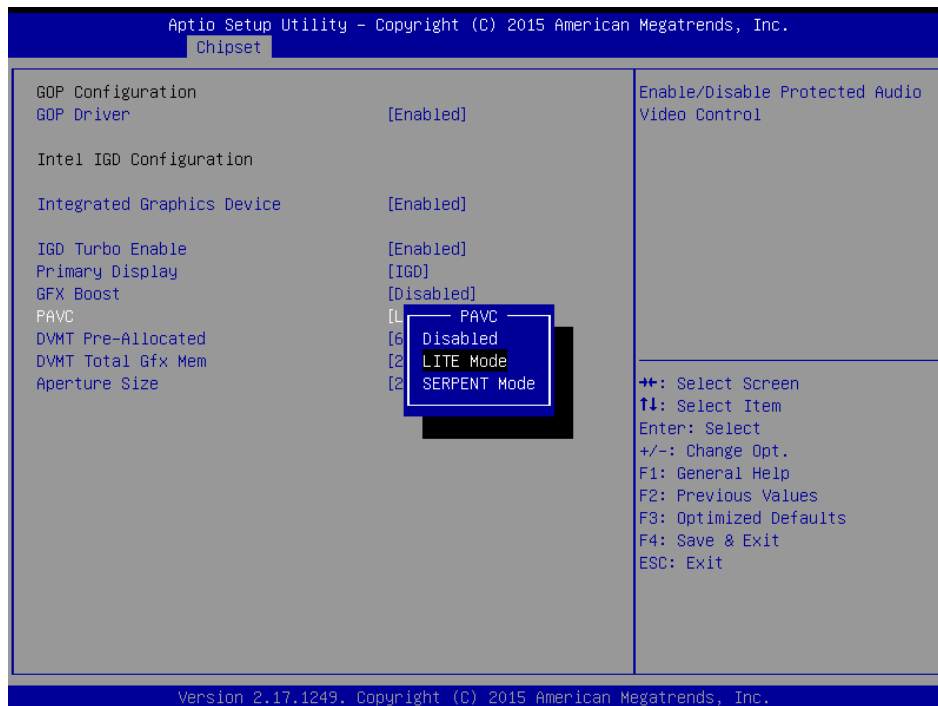
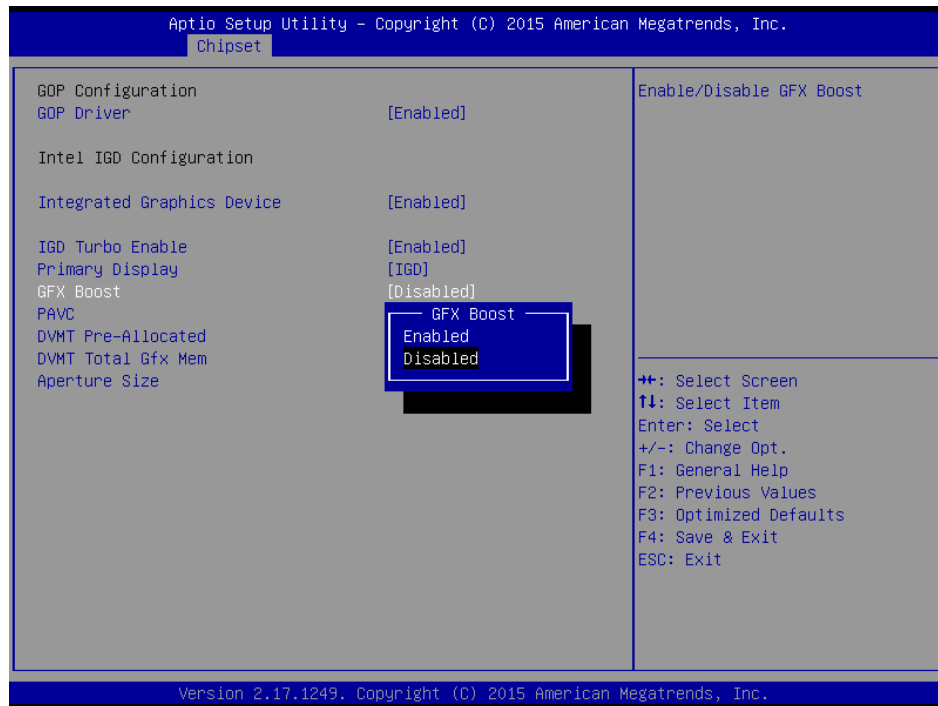
Item	Option	Description
Max TOLUD	Dynamic 2 GB 2.25 GB 2.5 GB 2.75 GB 3 GB[Default]	Maximum Value of TOLUD.

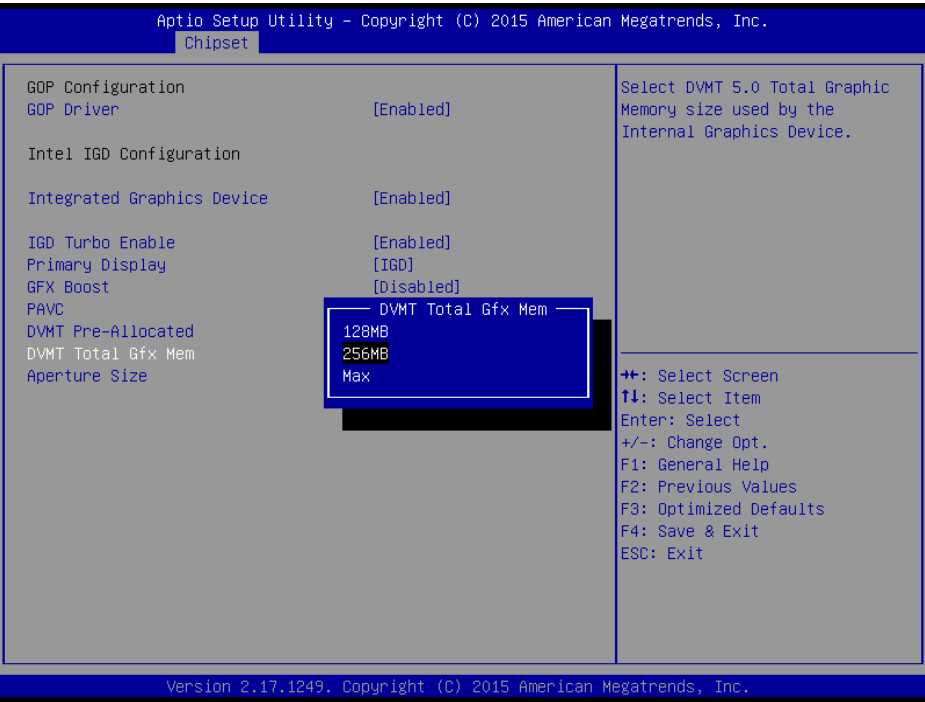
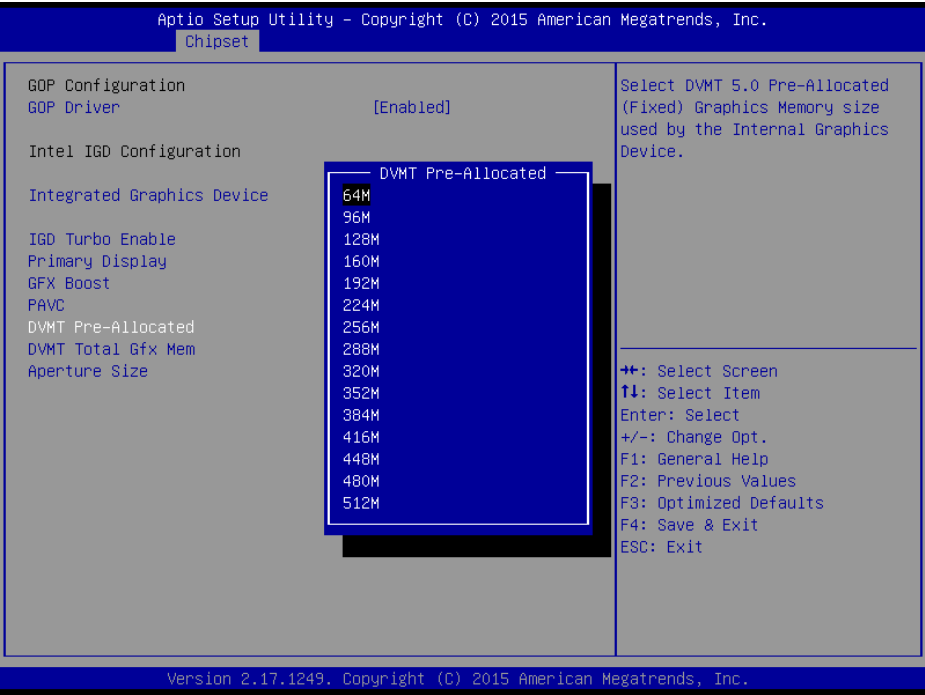
3.6.3.1.1 Intel IGD Configuration

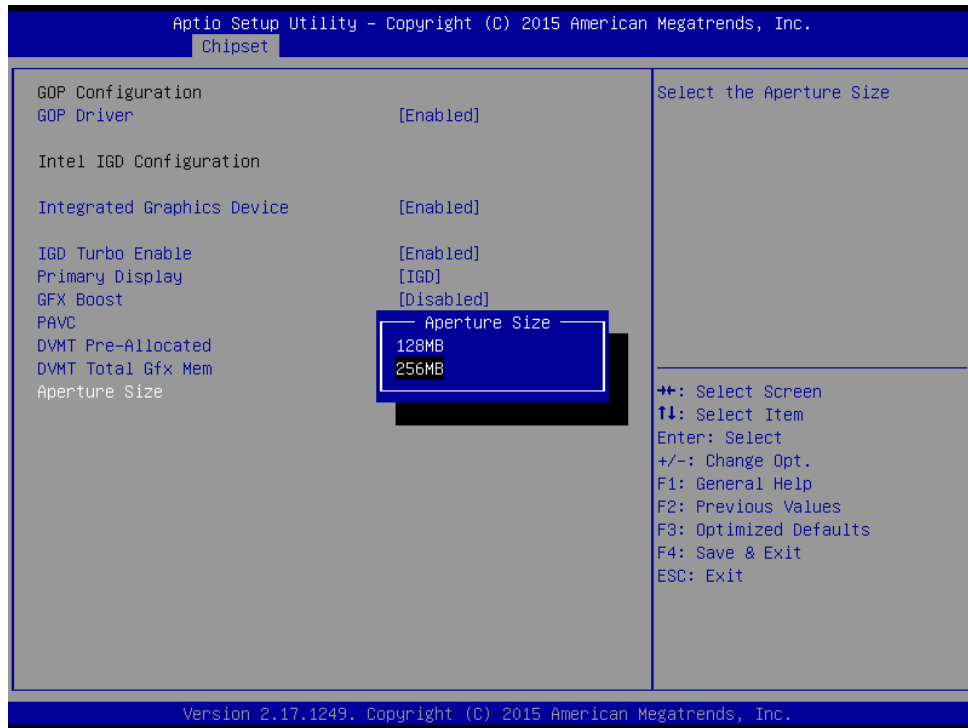






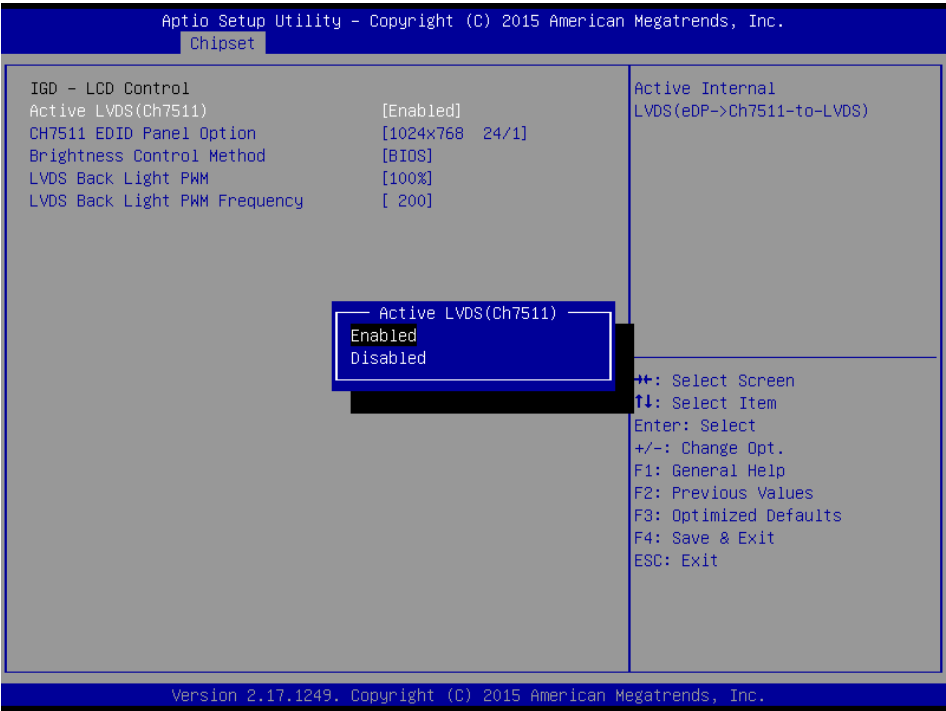
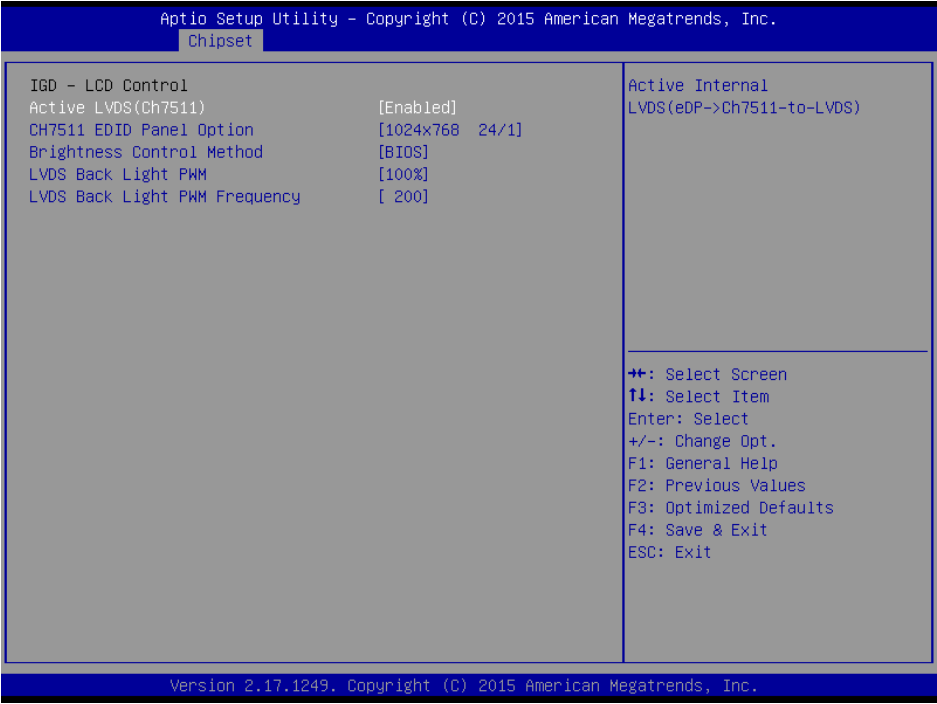


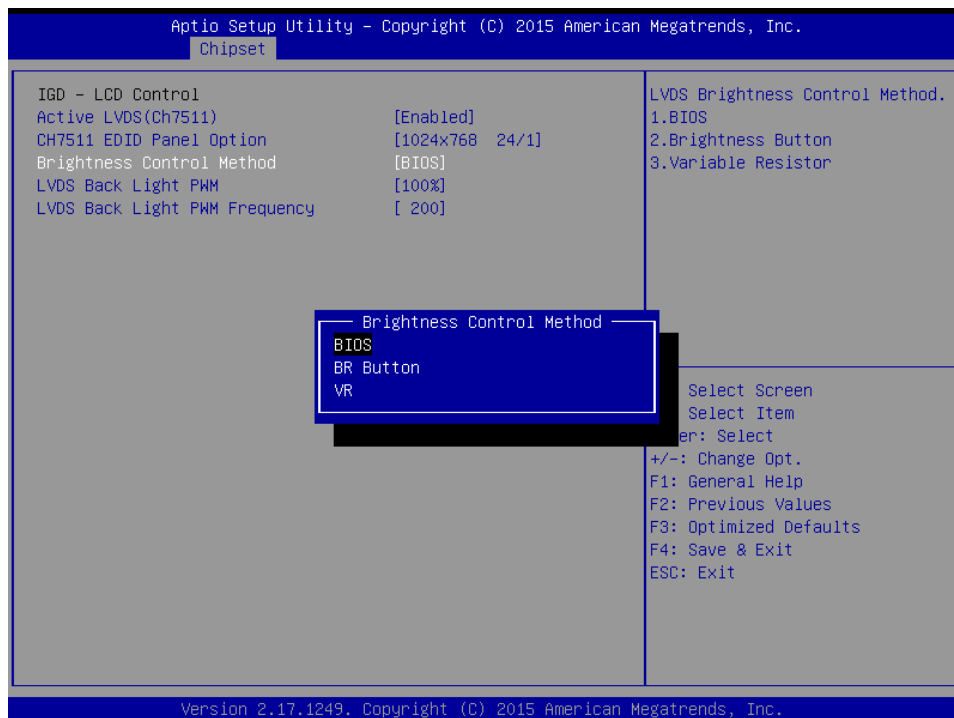
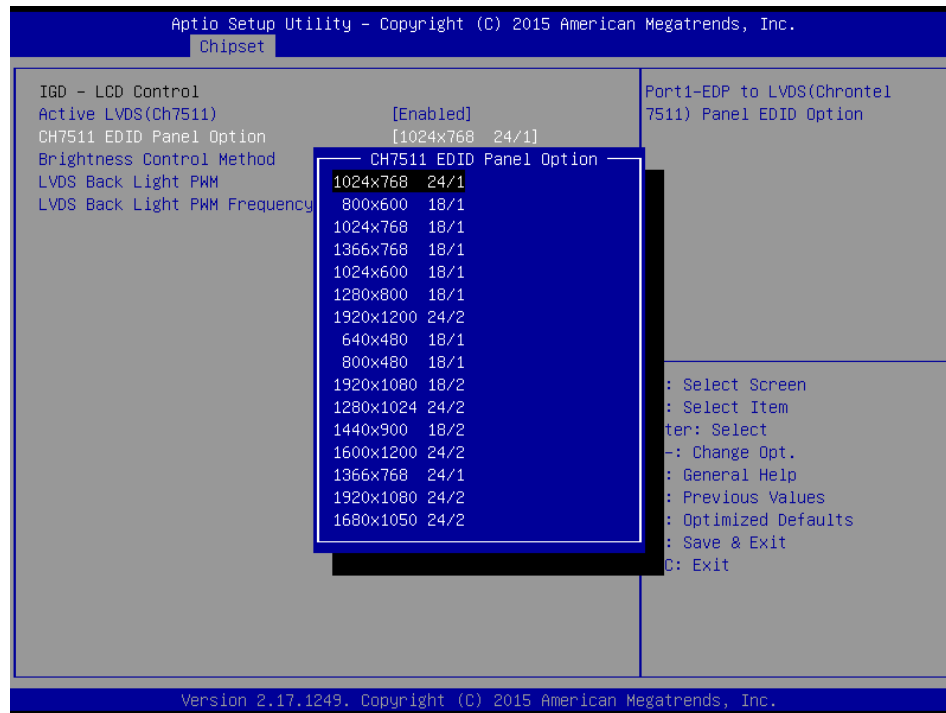


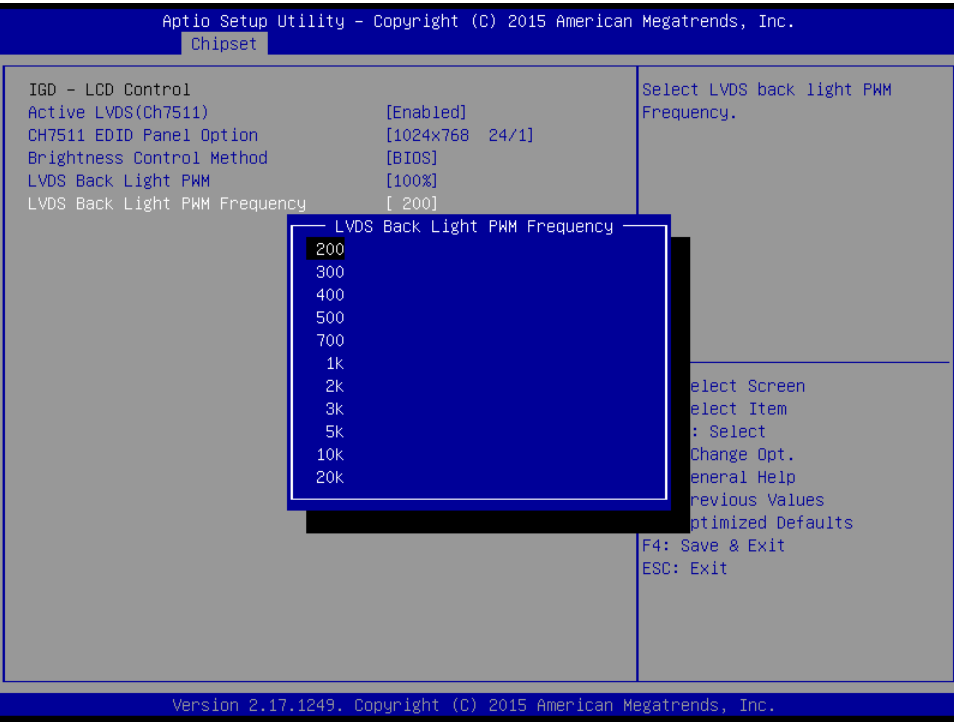
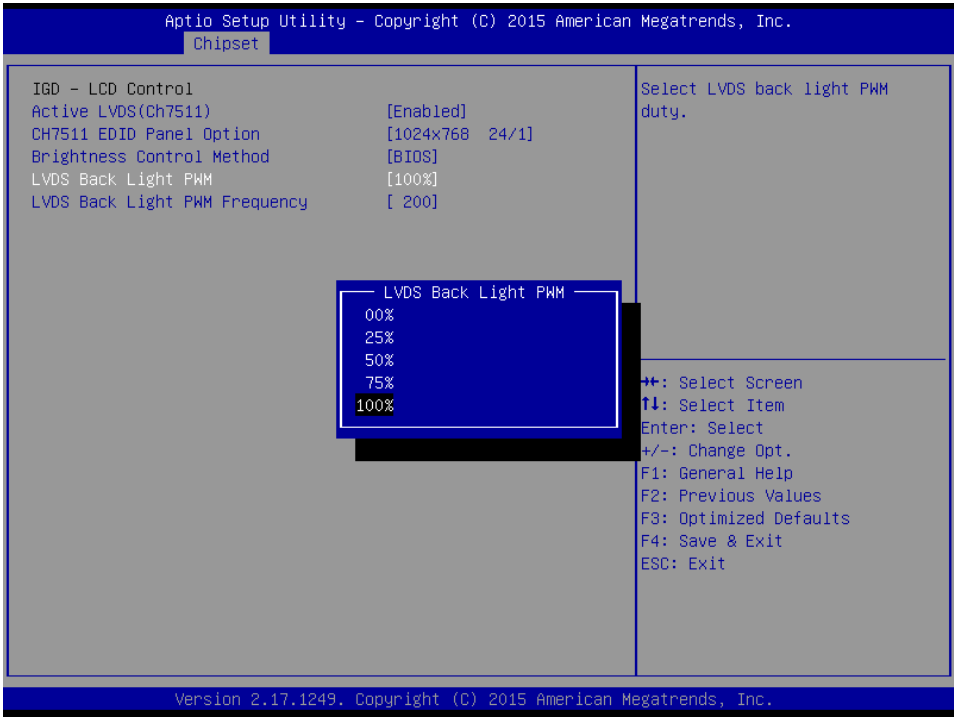


Item	Option	Description
GOP Driver	Enabled[Default], Disabled	Enable GOP Driver will unload VBIOS; Disable it will load VBIOS.
Integrated Graphics Device	Enabled[Default], Disabled	Enable: Enable Integrated Graphics Device (IGD) when selected as the Primary Video Adaptor. Disable: Always disable IGD.
IGD Turbo Enable	Enabled[Default], Disabled	Enable: Enable IGD Turbo Enable. Disable: IGD Turbo Disable.
Primary Display	Auto IGD[Default] PCIe	Select which of IGD/PCI Graphics device should be Primary Display.
GFX Boost	Enabled, Disabled[Default]	Enable/Disable GFX Boost.
PAVC	Disabled LITE Mode[Default] SERPENT Mode	Enable/Disable Protected Audio Video Control.
DVMT Pre-Allocated	64M[Default]/96M/128M/160M/192M/ 224M/256M/288M/320M/352M/ 384M/416M/448M/ 480M/512M	Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.
DVMT Total Gfx Mem	128MB 256MB[Default] Max	Select DVMT 5.0 Total Graphics Memory size used by the Internal Graphics Device.
Aperture Size	128MB 256MB[Default]	Select the Aperture Size.

3.6.3.1.2 IGD - LCD Control





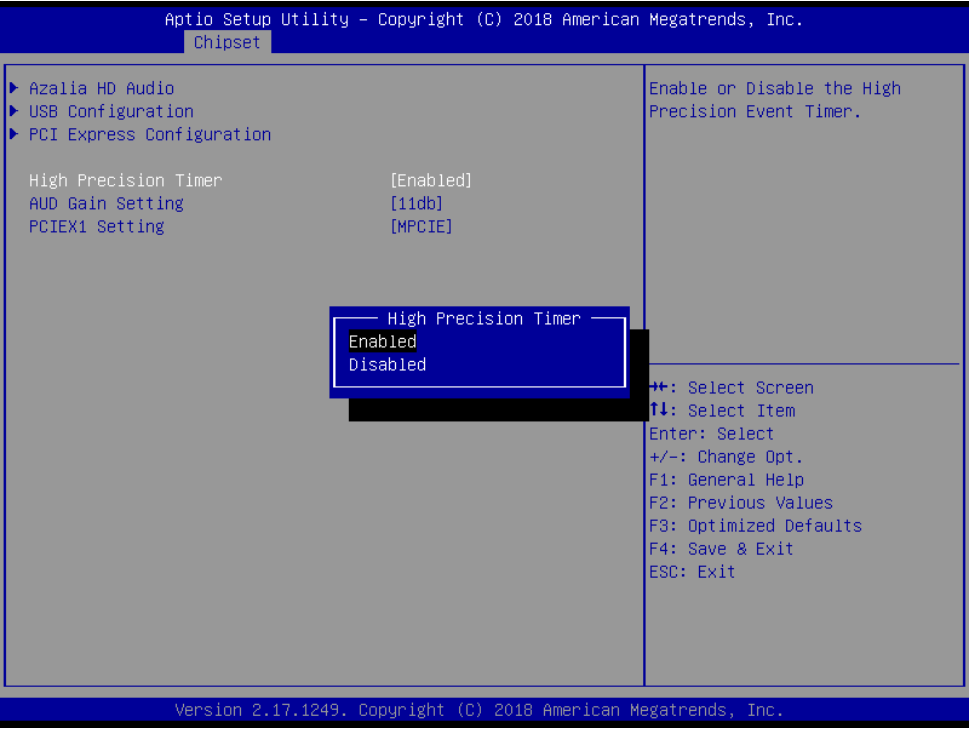


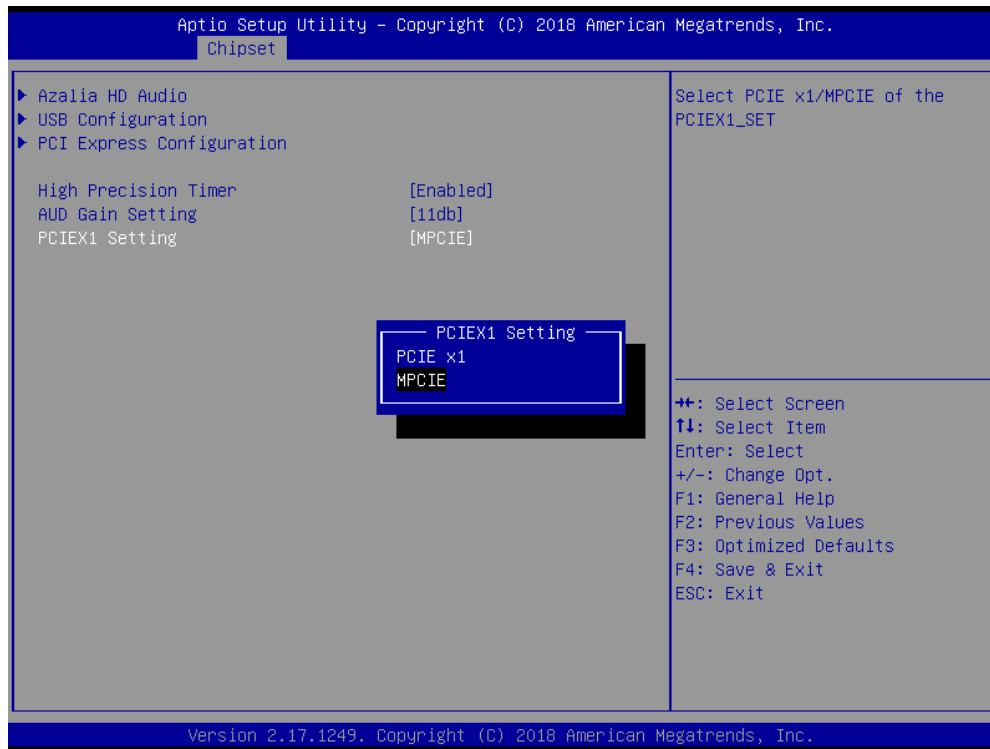
Item	Option	Description
Active LVDS (Ch7511)	Enabled[Default] Disabled	Active Internal LVDS(eDP->Ch7511-to -LVDS).
CH7511 EDID Panel Option	1024x768 24/1 800x600 18/1 1024x768 18/1 1366x768 18/1 1024x600 18/1 1280x800 18/1	Port1-EDP to LVDS (Chrontel 7511) Panel EDID Option.

	1920x1200 24/2 640x480 18/1 800x480 18/1 1920x1080 18/2 1280x1024 24/2[Default] 1440x900 18/2 1600x1200 24/2 1366x768 24/1 1920x1080 24/2 1680x1050 24/2	
Brightness Control Method	BIOS[Default] BR Button VR	LVDS Brightness Control Method. 1.BIOS 2.Brightness Button 3.Variable Resistor.
LVDS Back Light PWM	00% 25% 50% 75% 100%[Default]	Select LVDS back light PWM duty.
LVDS Back Light PWM Frequency	200[Default] 300 400 500 700 1k 2k 3k 5k	Select LVDS back light PWM Frequency.

3.6.3.2 South Bridge

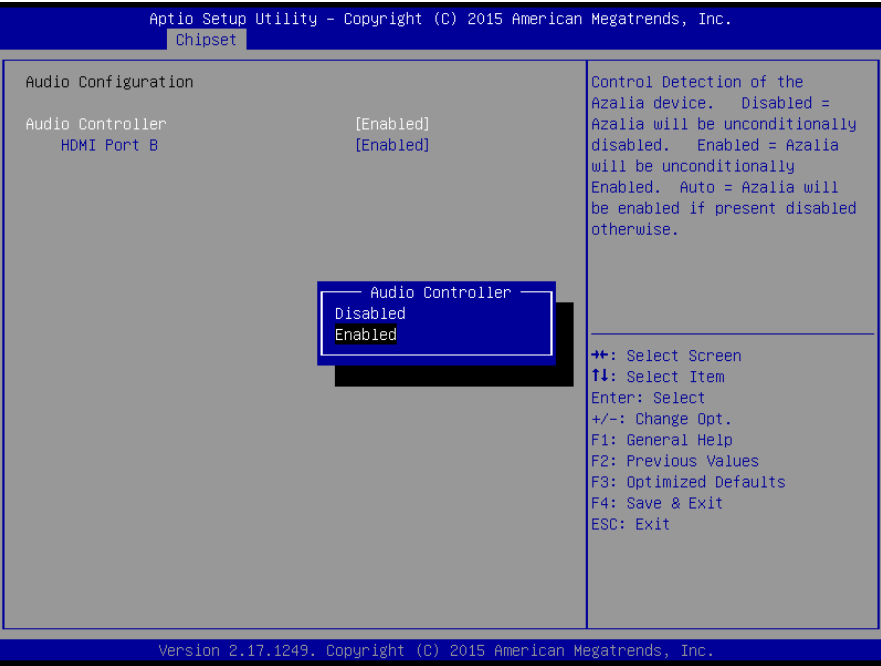
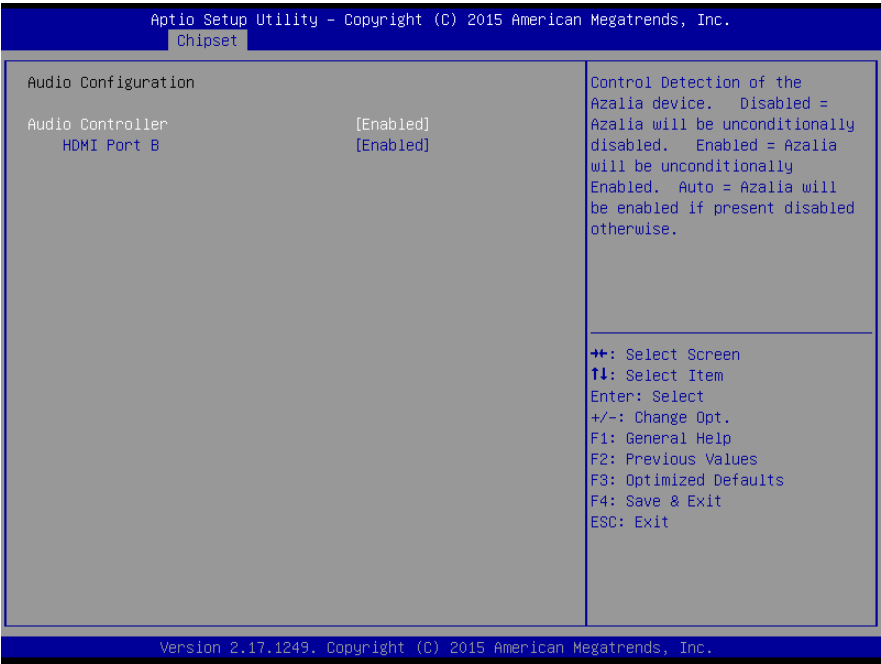


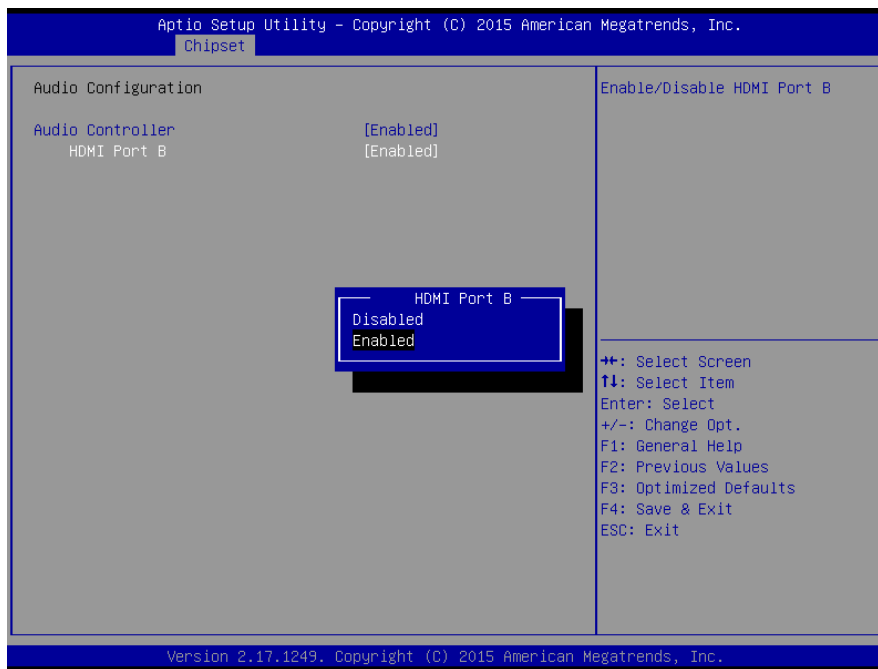




Item	Option	Description
High Precision Timer	Disabled Enabled[Default]	Enable or Disable the High Precision Event Timer.
AUD Gain Setting	11db[Default] 14db 19db 25db	Select db Value of the AUD Gain.
PCIEX1 Setting	PCIE x1 MPCIE[Default]	Select PCIE x1/ MPCIE of the PCIEEX1_SET

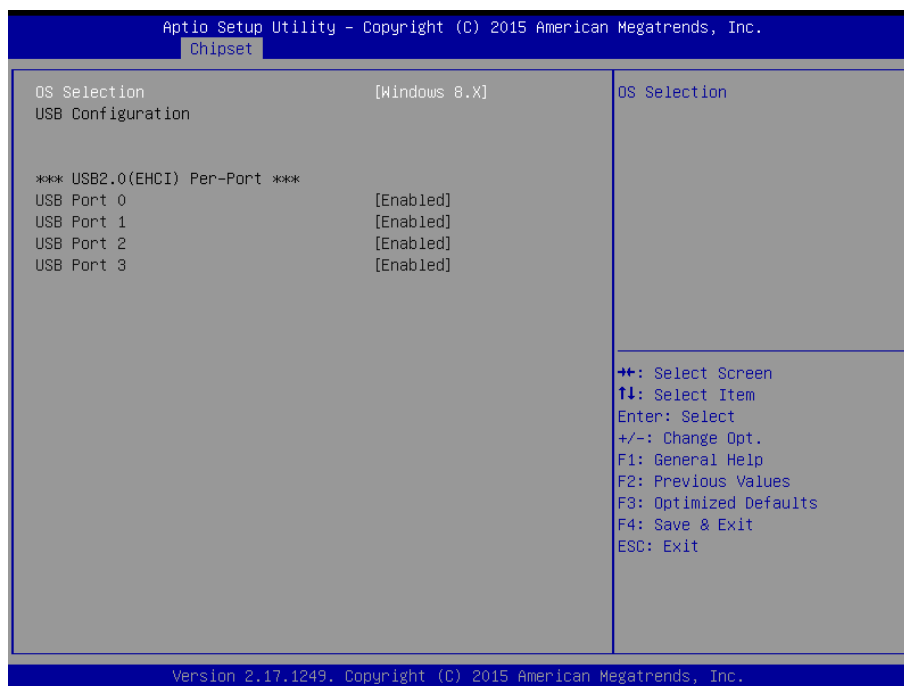
3.6.3.2.1 Azalia HD Audio

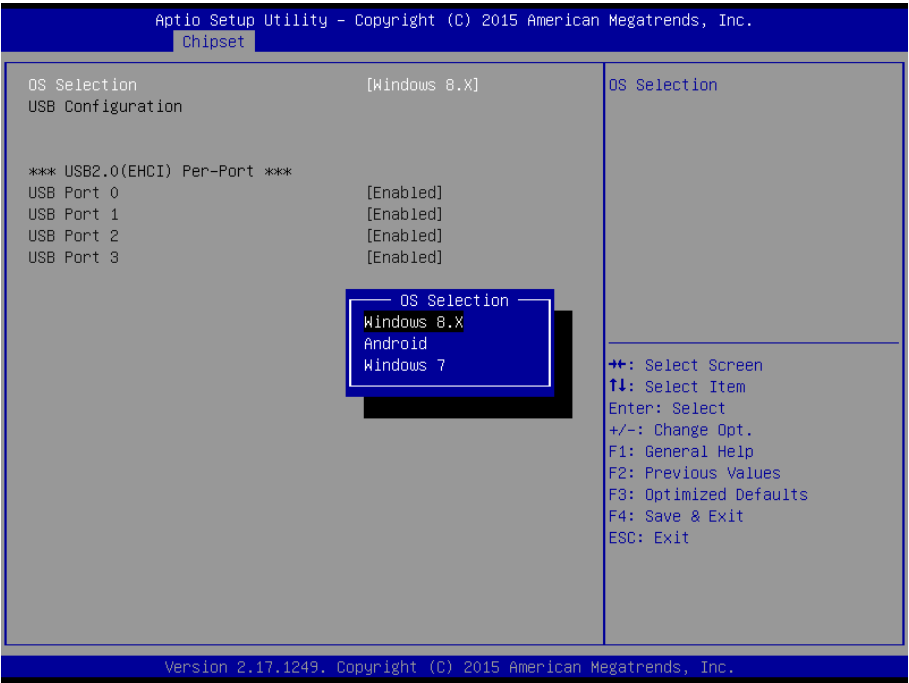




Item	Option	Description
Audio Controller	Enabled[Default], Disabled	Control Detection of the Azalia device. Disabled = Azalia will be unconditionally disabled. Enabled = Azalia will be unconditionally Enabled. Auto = Azalia will be enabled if present disabled otherwise.
HDMI Port B	Enabled[Default], Disabled	Enable/Disable HDMI Port B.

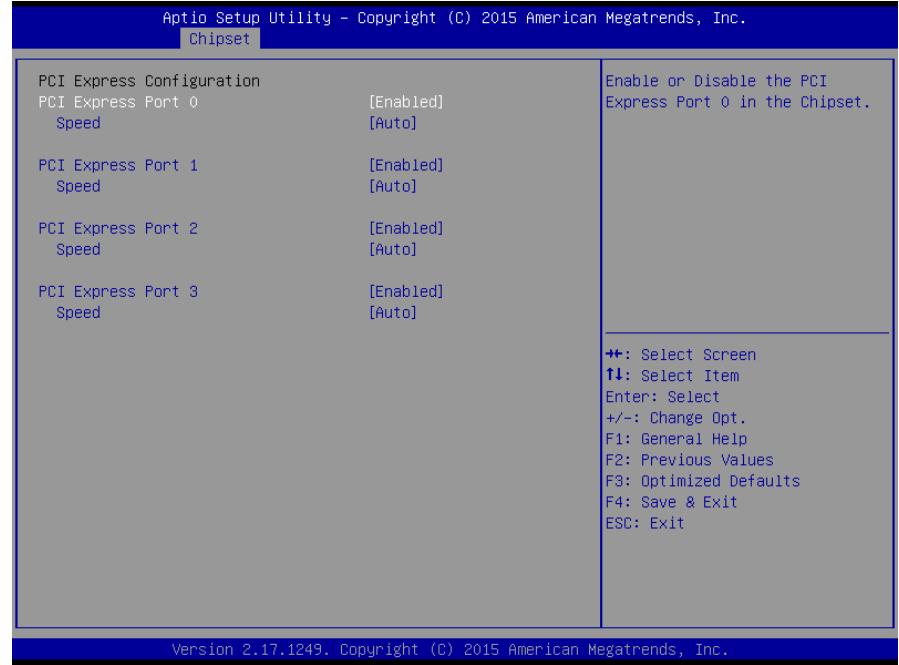
3.6.3.2.2 USB Configuration



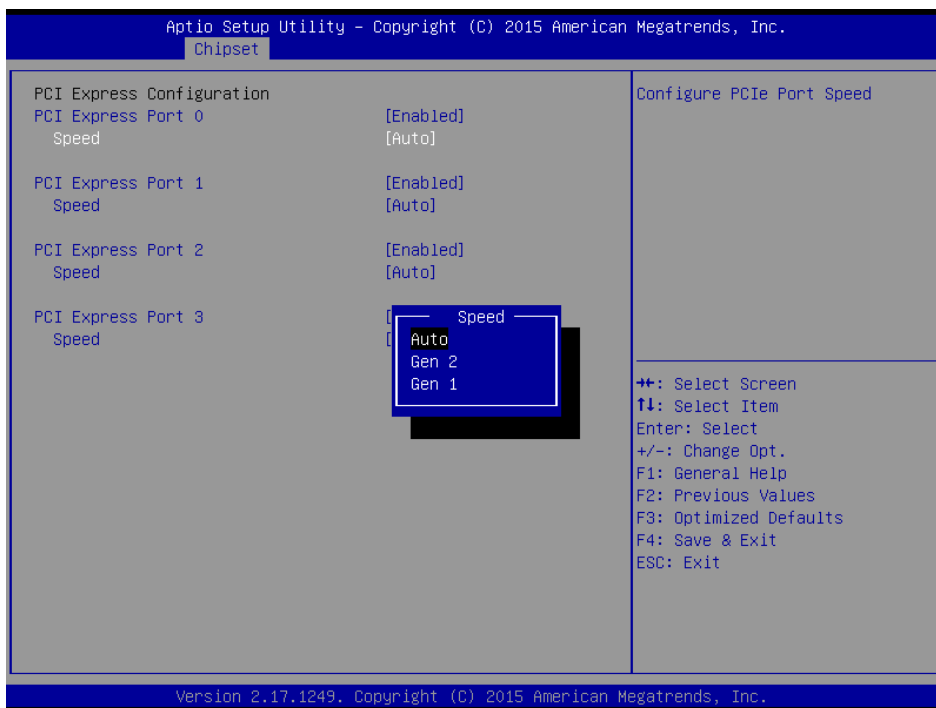
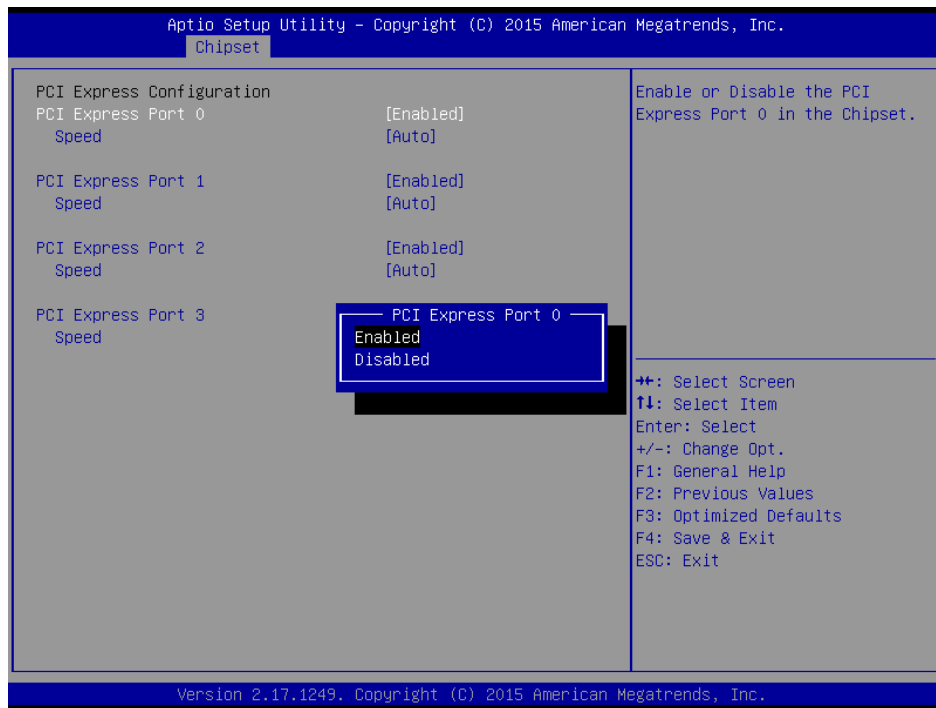


Item	Option	Description
OS Selection	Windows 8.X[Default] Android Windows 7	OS Selection.

3.6.3.2.3 PCI Express Configuration

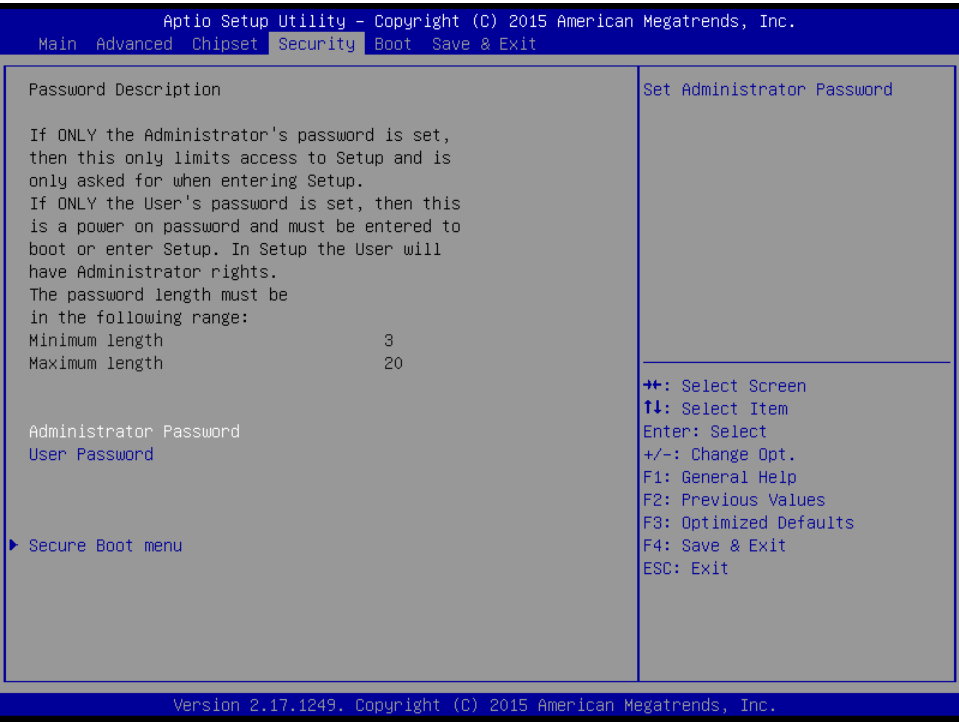


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Item	Option	Description
PCI Express Port 0/1/2/3	Enabled[Default], Disabled	Enable or Disable the PCI Express Port 0/1/2/3 in the Chipset.
Speed	Auto[Default] Gen 2 Gen 1	Configure PCIe Port Speed.

3.6.4 Security



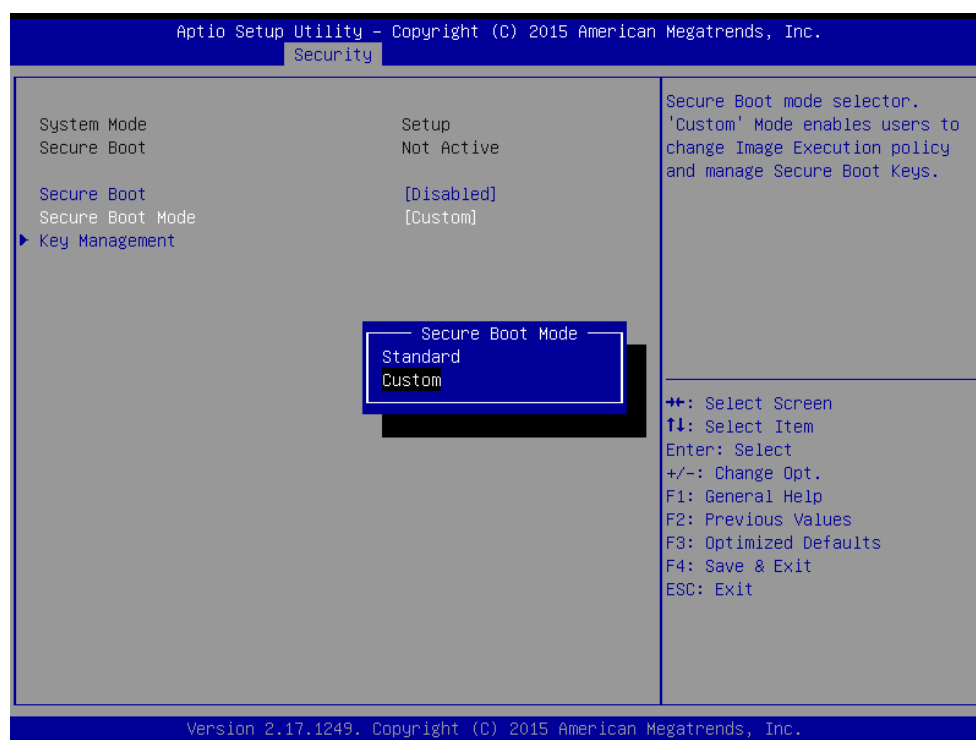
- Administrator Password

Set setup Administrator Password

- User Password

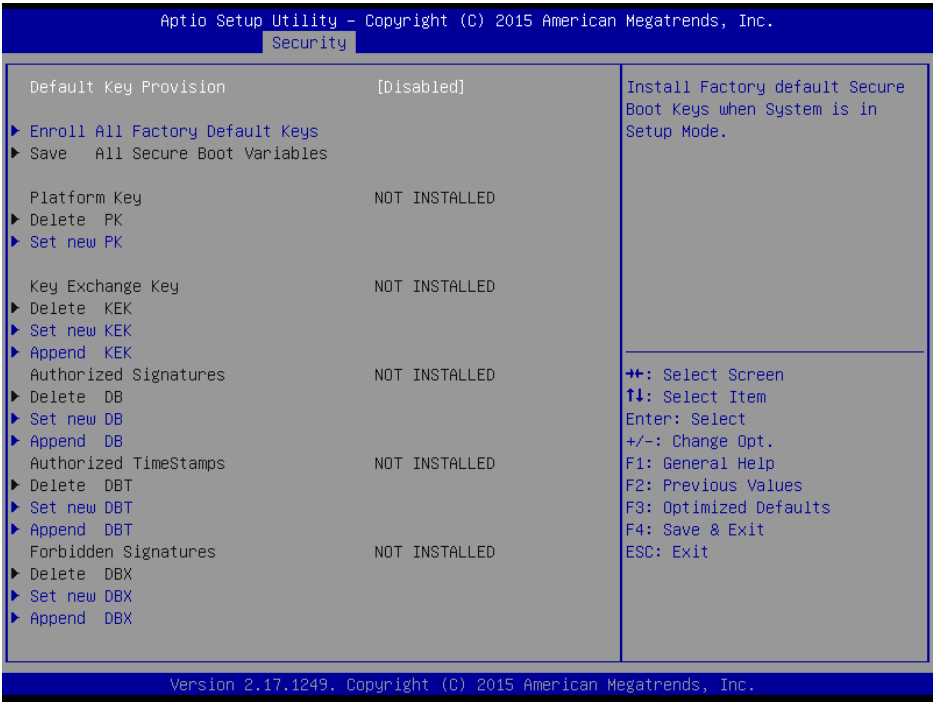
Set User Password

3.6.4.1 Secure Boot menu



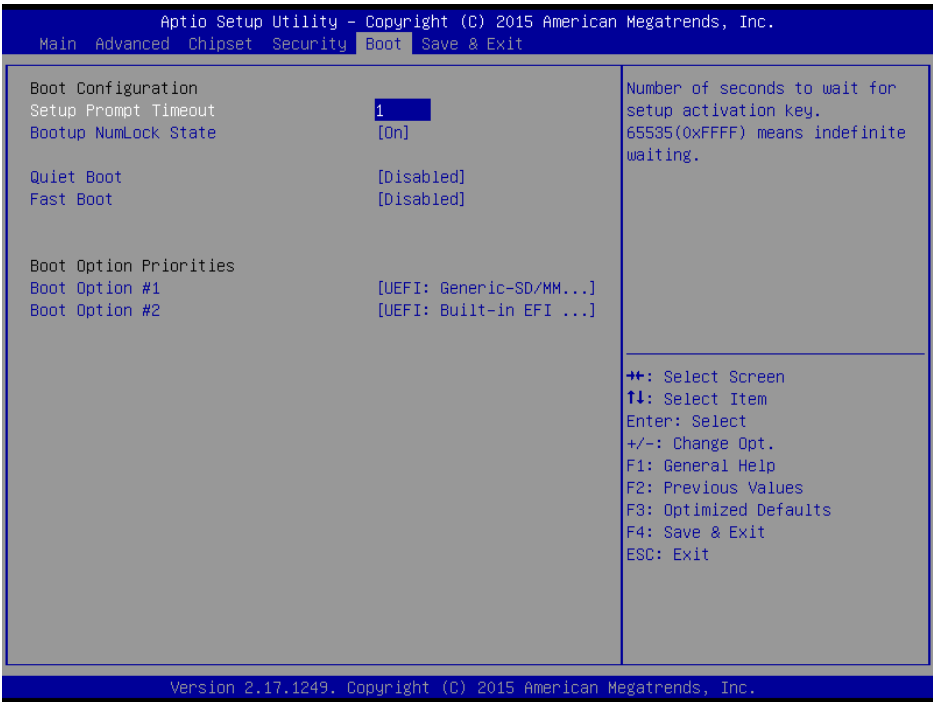
Item	Option	Description
Secure Boot	Disabled[Default] Enabled	Secure Boot can be enabled if 1.System running in User mode with enrolled Platform Key(PK) 2.CSM function is disabled.
Secure Boot Mode	Standard Custom[Default]	Secure Boot mode selector. 'Custom' Mode enables users to change Image Execution policy and manage Secure Boot Keys.

3.6.4.1.1 Key Management



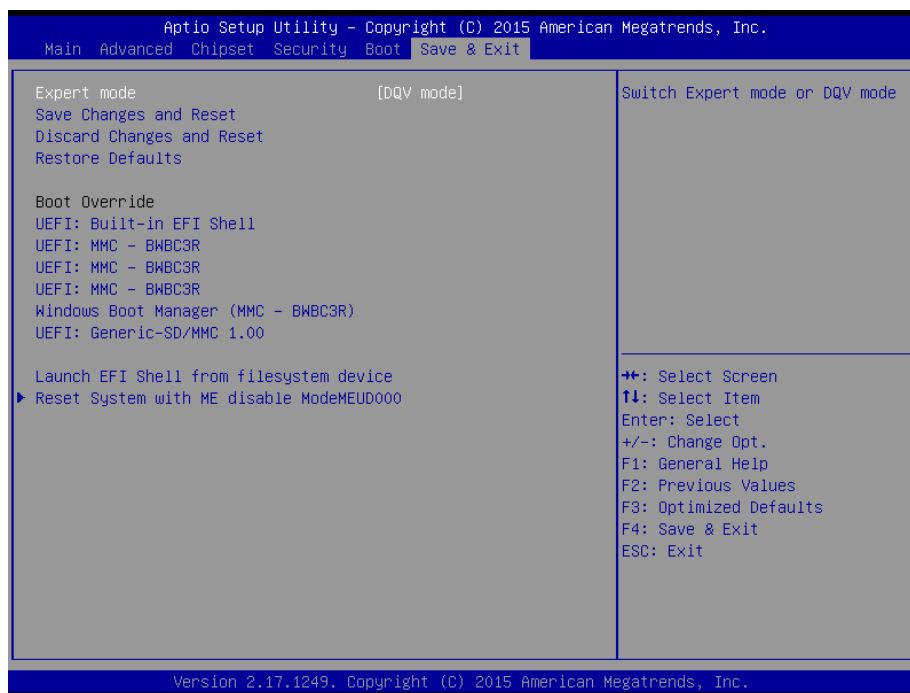
Item	Option	Description
Default Key Provision	Enabled, Disabled[Default]	Install Factory default Secure Boot Keys when System is in Setup Mode.

3.6.5 Boot



Item	Option	Description
Setup Prompt Timeout	1~ 65535	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	On[Default] Off	Select the Keyboard NumLock state
Quiet Boot	Disabled[Default] Enabled	Enables or disables Quiet Boot option
Fast Boot	Disabled[Default] Enabled	Enables or disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect for BBS boot options.
Boot Option #1/2	Set the system boot order.	

3.6.6 Save and exit



Item	Description
Save Changes and Reset	Reset the system after saving the changes.
Discard Changes and Reset	Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.
Restore Defaults	This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.
Launch EFI Shell from filesystem device	Attempts to Launch EFI Shell application (Shellx64.efi) from one of the available filesystem devices.