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PCB:E1907EX0301RT

EAP-EX03 Rev.A1

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Avalue Technology Inc.

Title

01-Cover Sheet

Size
A

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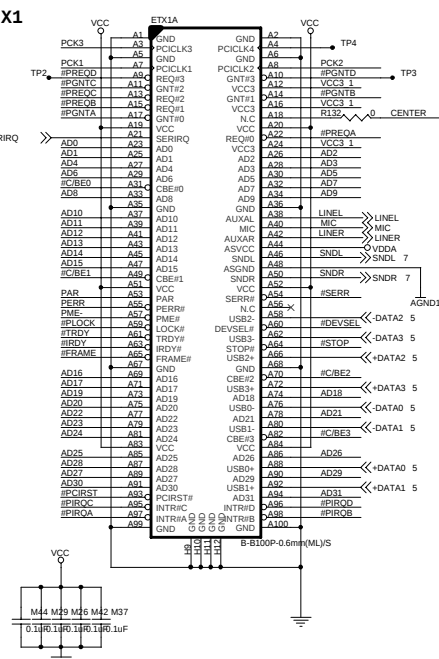
EEV-EX03

Rev
A1

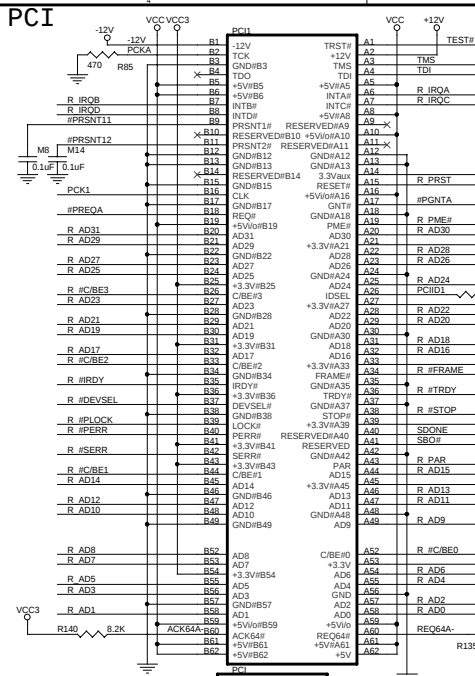
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X1



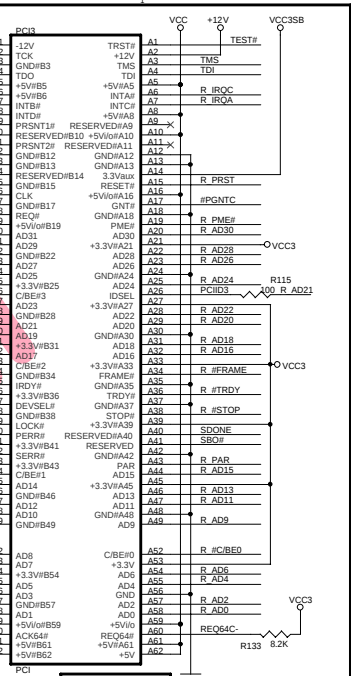
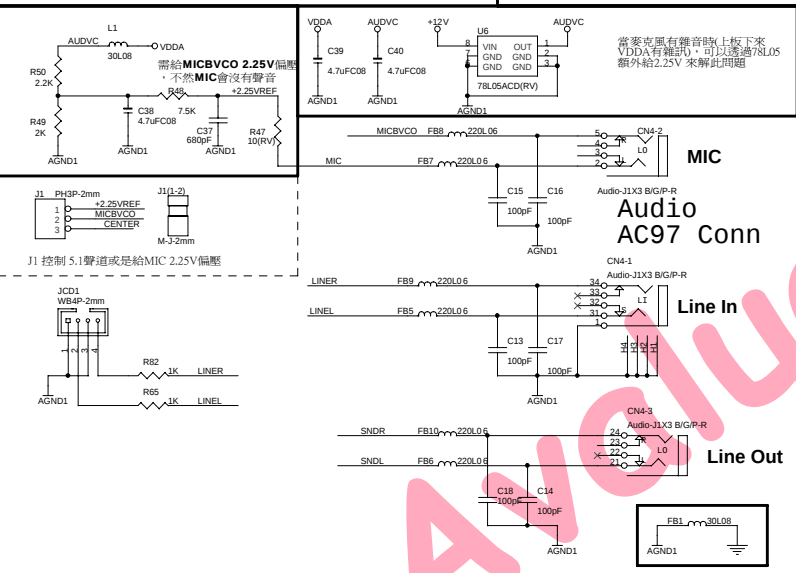
PCI

PCI SLOT1
ID=AD19, INTA

PCI

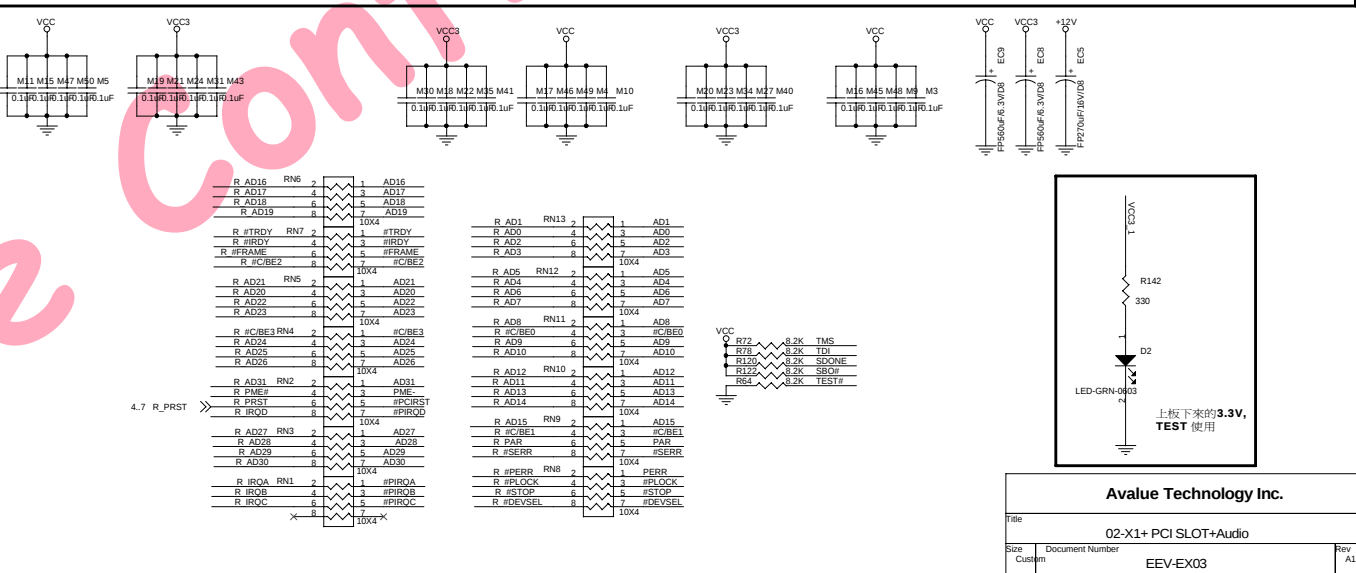
PCI SLOT2
ID=AD20, INTB

PCI

PCI SLOT3
ID=AD21, INTCAudio
AC97 Conn

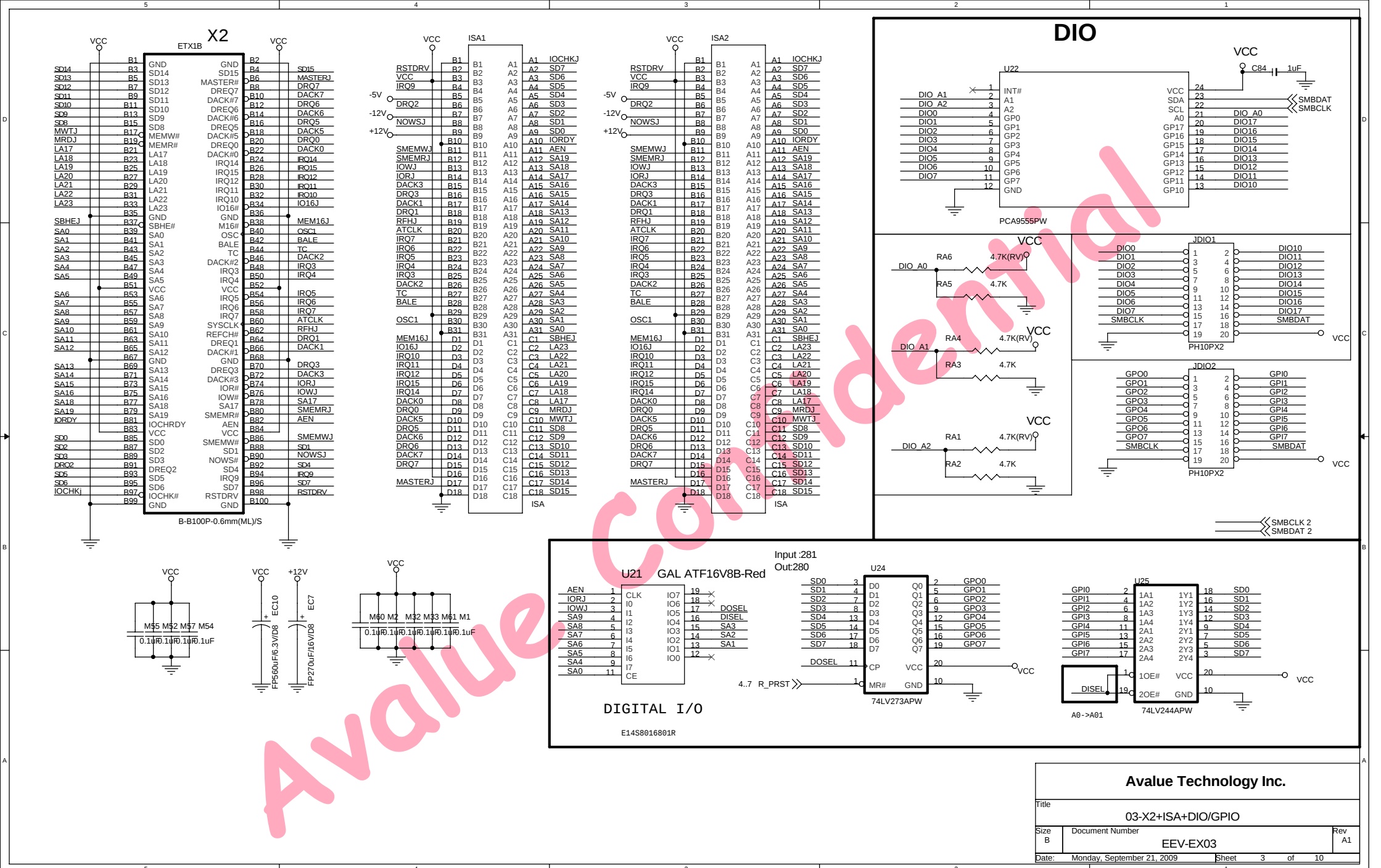
Line In

Line Out

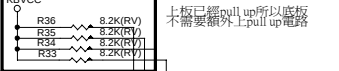
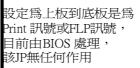


Audio Technology Inc.

File: 02-X1+PCI SLOT+Audio
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C45 控制面板對比



The schematic diagram illustrates the SP213ECA footprint, showing connections for three main components: CHIP, B-SUB9, and COM2.

CHIP: The CHIP component is connected to the SP213ECA footprint. The connections are as follows:

- CHIP Pin 12 to SP213ECA Pin 12 (C59, 0.1uF)
- CHIP Pin 14 to SP213ECA Pin 14 (C1+, 0.1uF)
- CHIP Pin 15 to SP213ECA Pin 15 (C2+, 0.1uF)
- CHIP Pin 16 to SP213ECA Pin 16 (C1-, 0.1uF)
- CHIP Pin 17 to SP213ECA Pin 17 (C2-, 0.1uF)

B-SUB9: The B-SUB9 component is connected to the SP213ECA footprint. The connections are as follows:

- B-SUB9 Pin 1 to SP213ECA Pin 1 (VCC)
- B-SUB9 Pin 2 to SP213ECA Pin 2 (VCC)
- B-SUB9 Pin 3 to SP213ECA Pin 3 (VCC)
- B-SUB9 Pin 4 to SP213ECA Pin 4 (VCC)
- B-SUB9 Pin 5 to SP213ECA Pin 5 (VCC)
- B-SUB9 Pin 6 to SP213ECA Pin 6 (VCC)
- B-SUB9 Pin 7 to SP213ECA Pin 7 (VCC)
- B-SUB9 Pin 8 to SP213ECA Pin 8 (VCC)
- B-SUB9 Pin 9 to SP213ECA Pin 9 (VCC)

COM2: The COM2 component is connected to the SP213ECA footprint. The connections are as follows:

- COM2 Pin 1 to SP213ECA Pin 1 (VCC)
- COM2 Pin 2 to SP213ECA Pin 2 (VCC)
- COM2 Pin 3 to SP213ECA Pin 3 (VCC)
- COM2 Pin 4 to SP213ECA Pin 4 (VCC)
- COM2 Pin 5 to SP213ECA Pin 5 (VCC)
- COM2 Pin 6 to SP213ECA Pin 6 (VCC)
- COM2 Pin 7 to SP213ECA Pin 7 (VCC)
- COM2 Pin 8 to SP213ECA Pin 8 (VCC)
- COM2 Pin 9 to SP213ECA Pin 9 (VCC)
- COM2 Pin 10 to SP213ECA Pin 10 (VCC)

The diagram also shows various other components and their connections, including capacitors (C59, C61, C58, C48), resistors (R12, R14, R16, R18, R20, R22, R24, R26, R28, R30, R32, R34, R36, R38, R40, R42, R44, R46, R48, R50, R52, R54, R56, R58, R60, R62, R64, R66, R68, R70, R72, R74, R76, R78, R80, R82, R84, R86, R88, R90, R92, R94, R96, R98, R100), and a footprint change to BHSX2DV.

Signal	Pin	Signal	Pin
GND	25	GND	1
GND	24	GND	2
GND	23	GND	3
GND	22	GND	4
GND	21	GND	5
GND	20	GND	6
GND	19	GND	7
GND	18	GND	8
SLIN-	17	SLINH	9
INIT-	16	INITH	10
ERR-	15	ERRH	11
AFD-	14	AFDH	12
		SLCT	13
		PE	14
		BUSY	15
		ACK#	16
		PP07	17
		PTD7	18
		PP06	19
		PTD6	20
		PP05	21
		PTD5	22
		PP04	23
		PTD4	24
		PP03	25
		PTD3	
		PP02	
		PTD2	
		PP01	
		PTD1	
		PP00	
		PTD0	
		STB#	
		STB-	

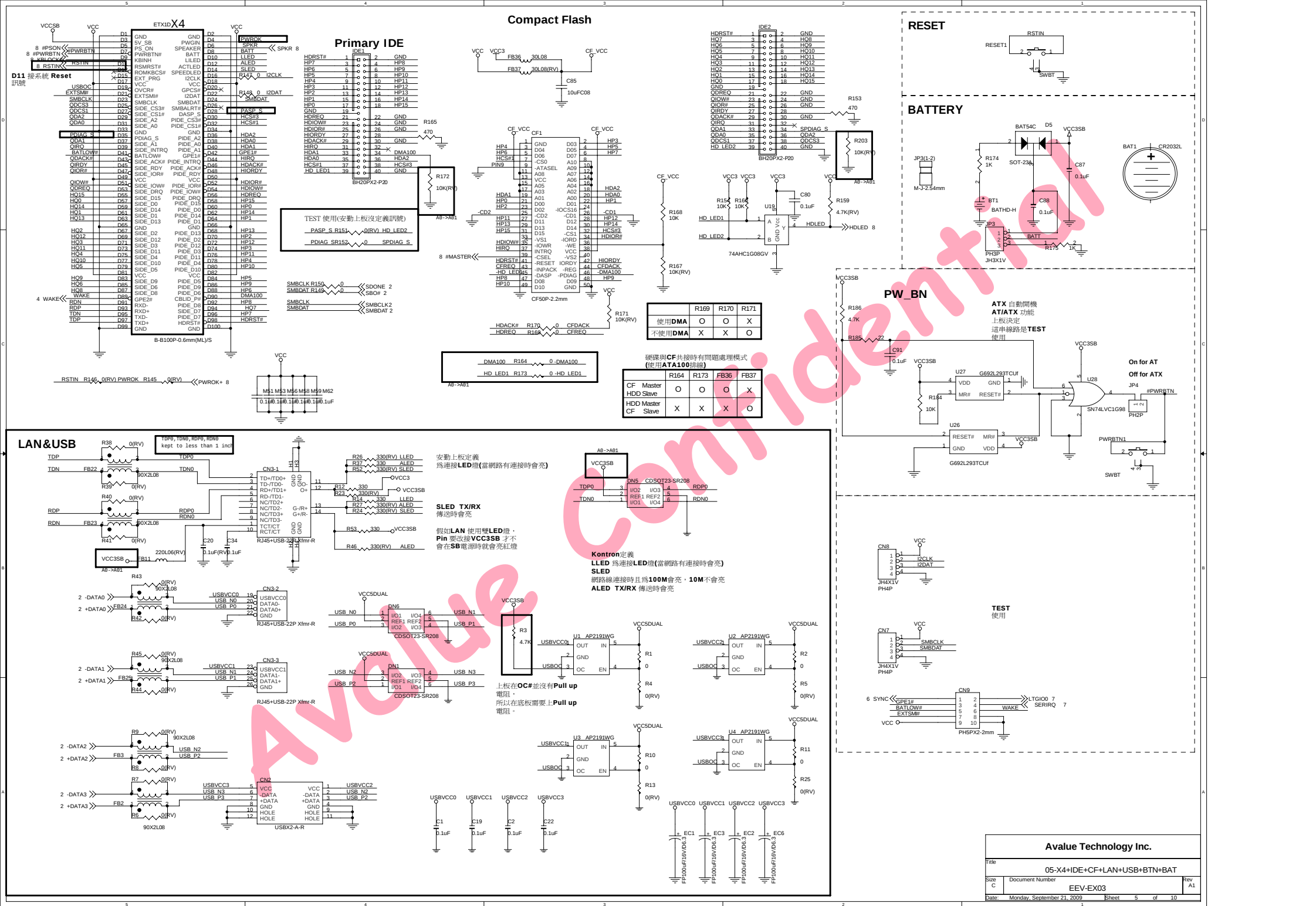
[illegible]

Title	04-X3+IR+COM+KB/MS+FLP+LPC
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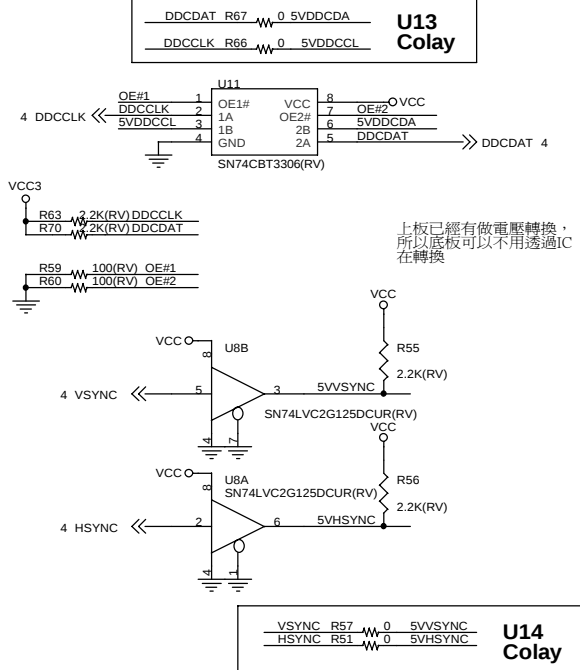
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Custom	EEV-EX03
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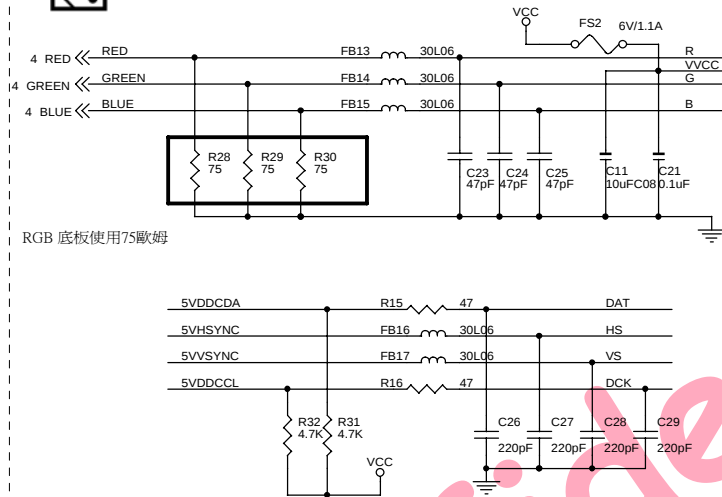


VGA Port

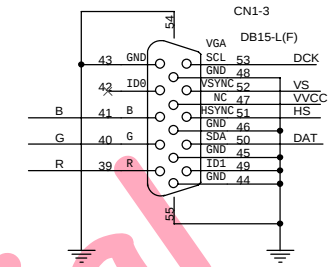


上板已經有做電壓轉換，
所以底板可以不用透過IC
在轉換

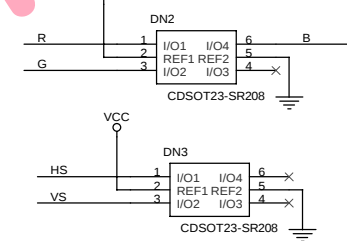
NEED CLOSES TO VGA Con.



RGB 底板使用75歐姆

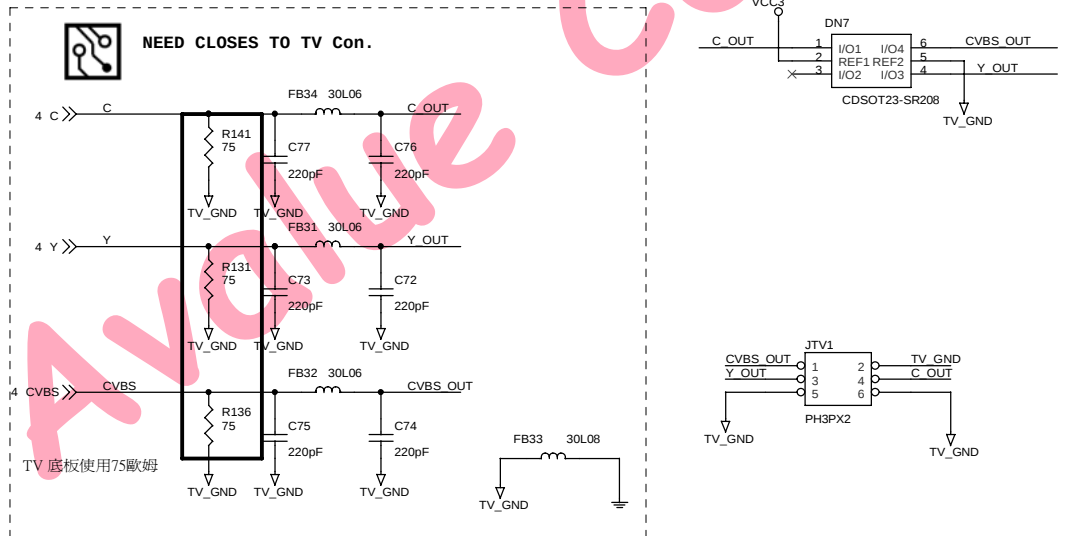


Close to the CN1-3



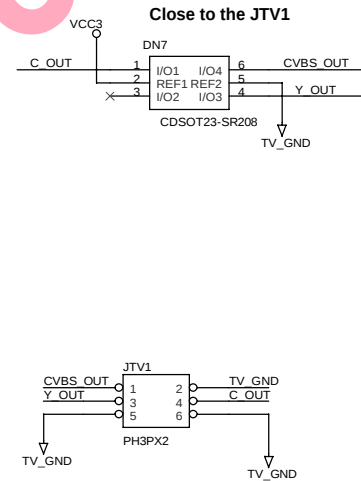
TV Port

NEED CLOSES TO TV Con.



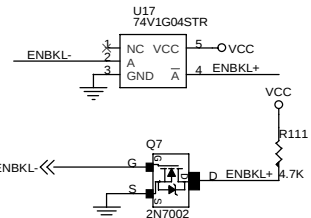
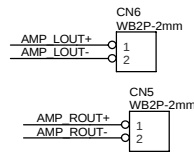
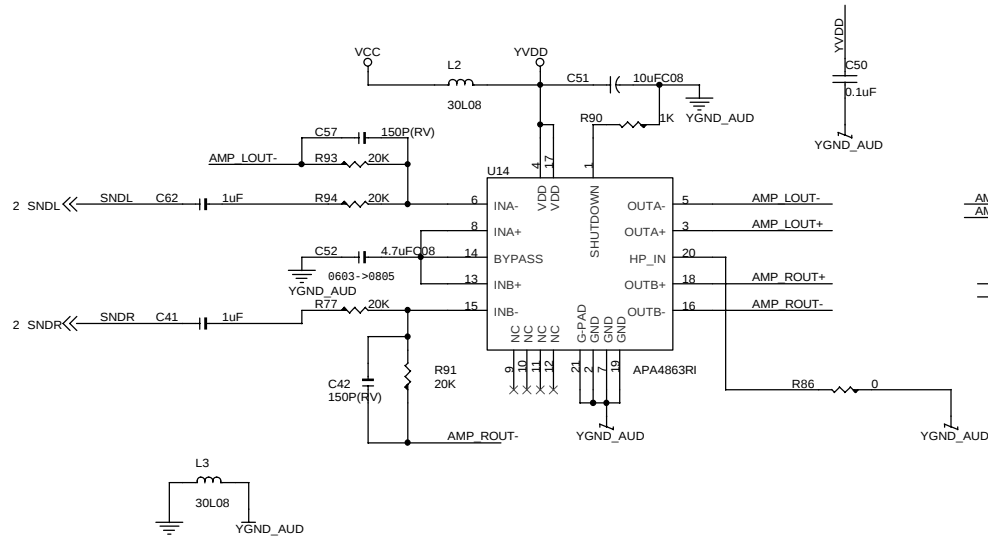
TV 底板使用75歐姆

Close to the JTV1

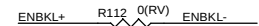


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Title			
06-CRT&TV Port			
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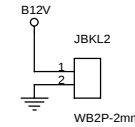
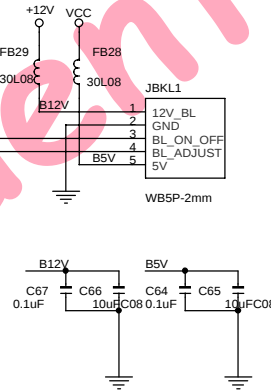
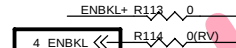


U17與Q7
Colay

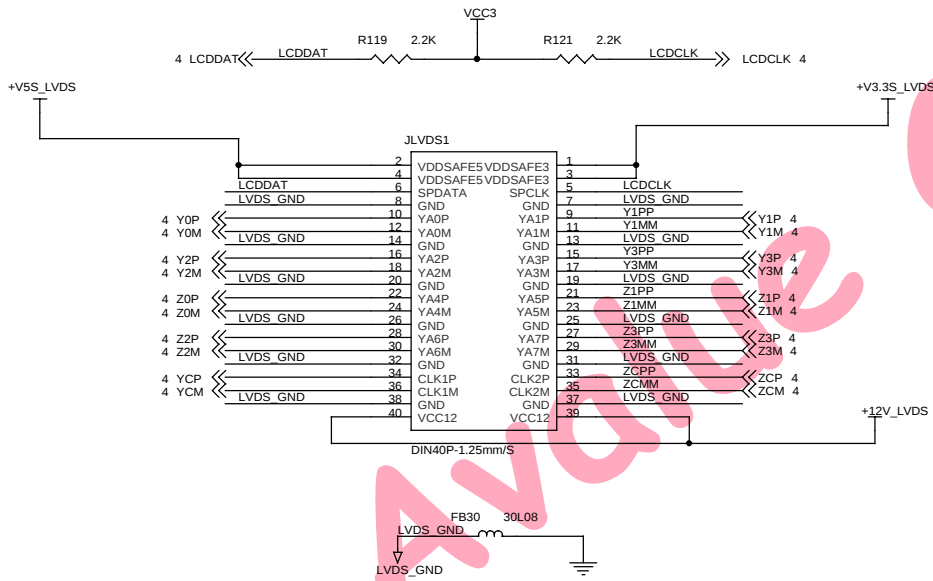


當上板已經有用顆MOSFET 使用

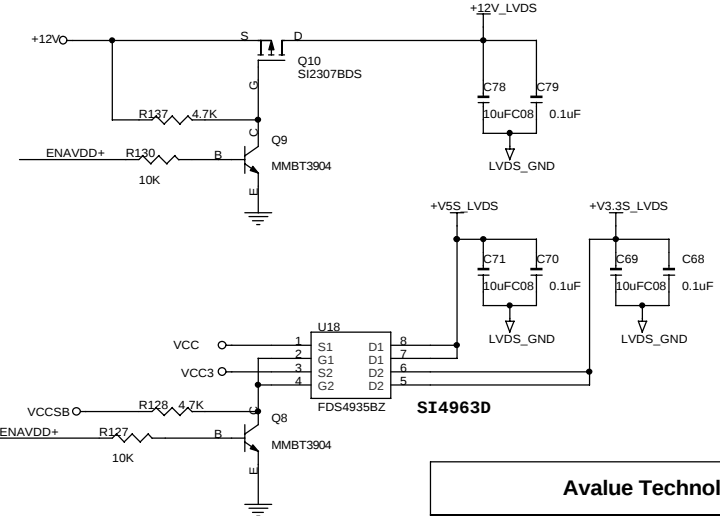
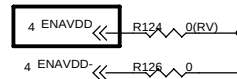
ENBKL 是給安勤早期舊板
(2643,2740等)控制 interver 使用



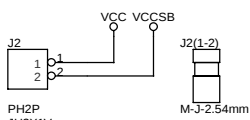
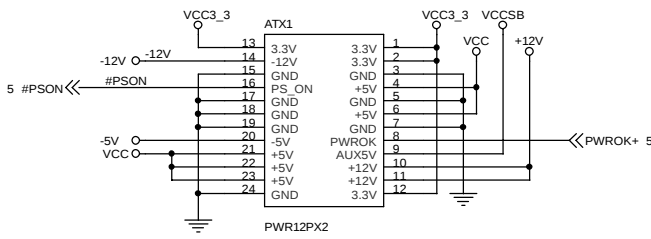
add connector



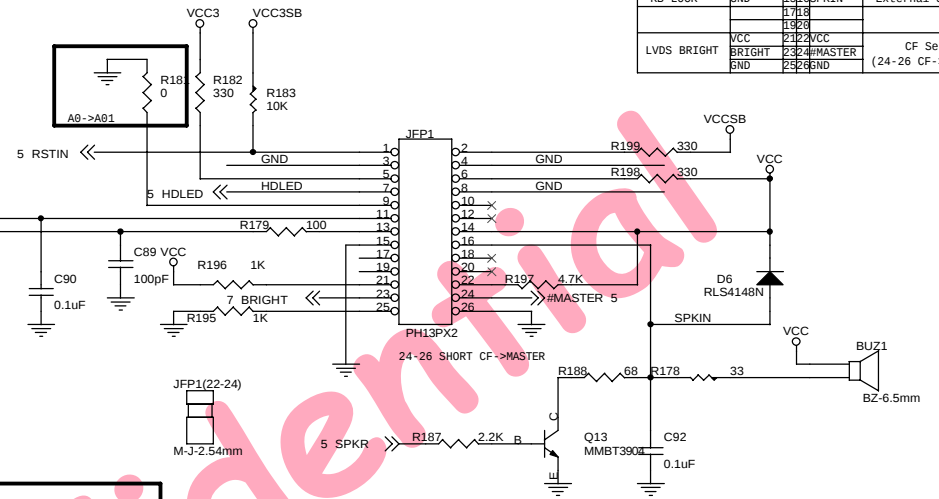
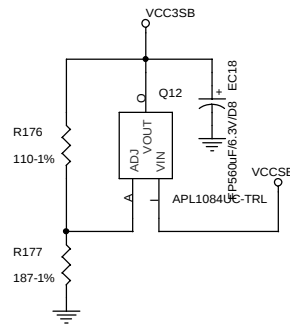
ENAVDD
是給安勤早期舊板
(2643,2740等)控制
面板數位電壓使用



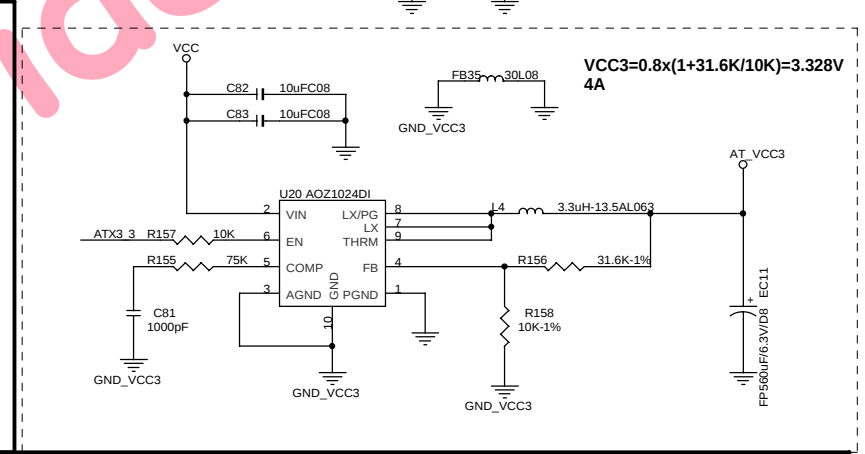
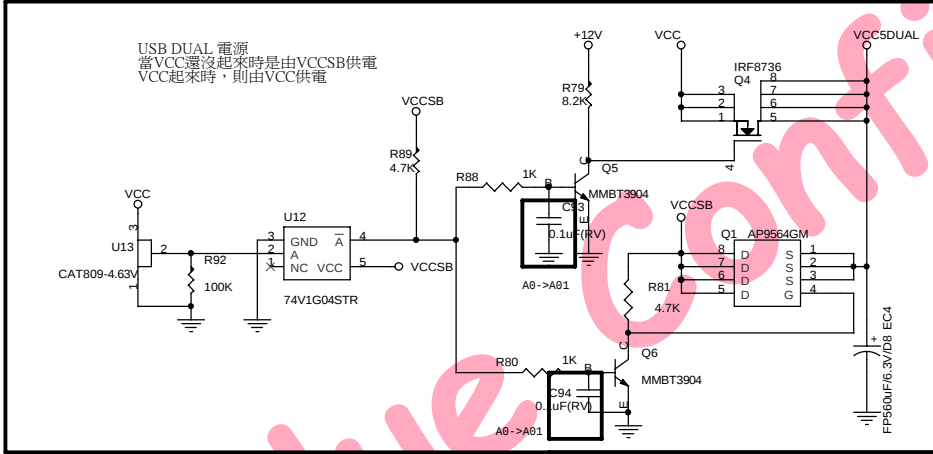
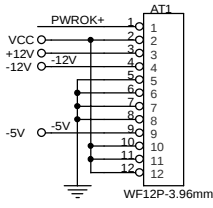
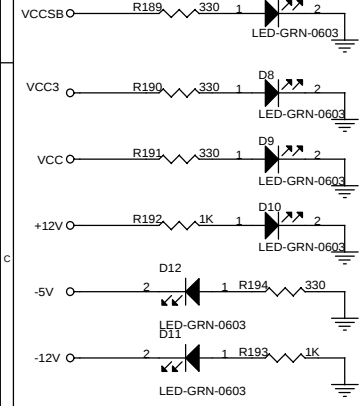
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Title			
07-LVDS+AMP			
Size	Document Number	Rev	
A3	EEV-EX03	A1	
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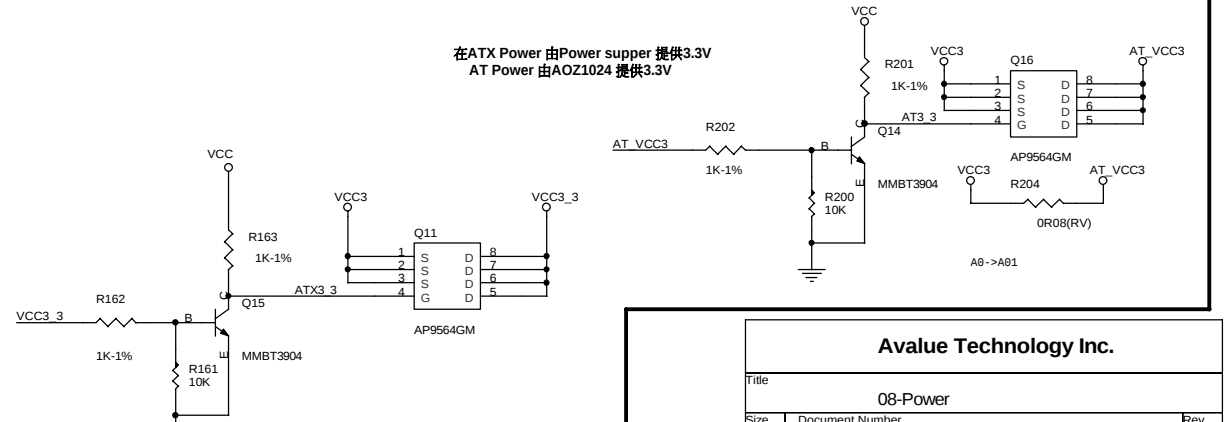
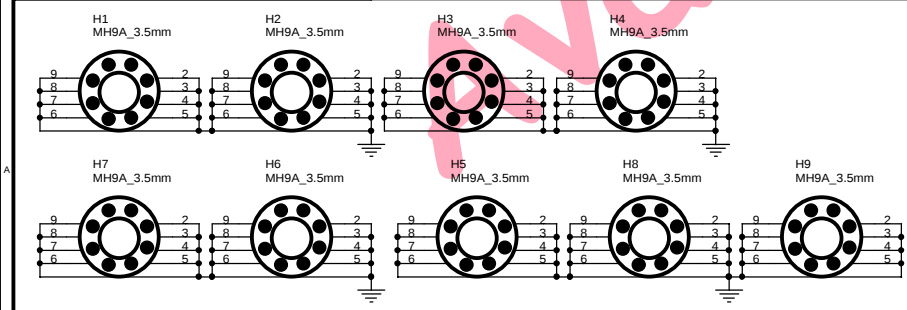
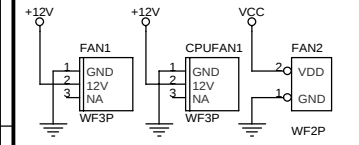
AT/ATX JP
AT :Shore
ATX:Open



RESET	1	2	VCCSB	STANDBY LED
IDE LED	3	4	VCC	POWER ON LED
POWER BUTTON	5	6	GND	
KB LOCK	7	8	GND	
KB LOCK	9	10	VCCSB	External SPEAK
KB LOCK	11	12	GND	
KB LOCK	13	14	VCCSB	
KB LOCK	15	16	GND	
KB LOCK	17	18	VCCSB	
KB LOCK	19	20	GND	
KB LOCK	21	22	VCCSB	
KB LOCK	23	24	GND	
KB LOCK	25	26	VCCSB	
KB LOCK	27	28	GND	
KB LOCK	29	30	VCCSB	
KB LOCK	31	32	GND	
KB LOCK	33	34	VCCSB	
KB LOCK	35	36	GND	
KB LOCK	37	38	VCCSB	
KB LOCK	39	40	GND	
KB LOCK	41	42	VCCSB	
KB LOCK	43	44	GND	
KB LOCK	45	46	VCCSB	
KB LOCK	47	48	GND	
KB LOCK	49	50	VCCSB	
KB LOCK	51	52	GND	
KB LOCK	53	54	VCCSB	
KB LOCK	55	56	GND	
KB LOCK	57	58	VCCSB	
KB LOCK	59	60	GND	
KB LOCK	61	62	VCCSB	
KB LOCK	63	64	GND	
KB LOCK	65	66	VCCSB	
KB LOCK	67	68	GND	
KB LOCK	69	70	VCCSB	
KB LOCK	71	72	GND	
KB LOCK	73	74	VCCSB	
KB LOCK	75	76	GND	
KB LOCK	77	78	VCCSB	
KB LOCK	79	80	GND	
KB LOCK	81	82	VCCSB	
KB LOCK	83	84	GND	
KB LOCK	85	86	VCCSB	
KB LOCK	87	88	GND	
KB LOCK	89	90	VCCSB	
KB LOCK	91	92	GND	
KB LOCK	93	94	VCCSB	
KB LOCK	95	96	GND	
KB LOCK	97	98	VCCSB	
KB LOCK	99	100	GND	



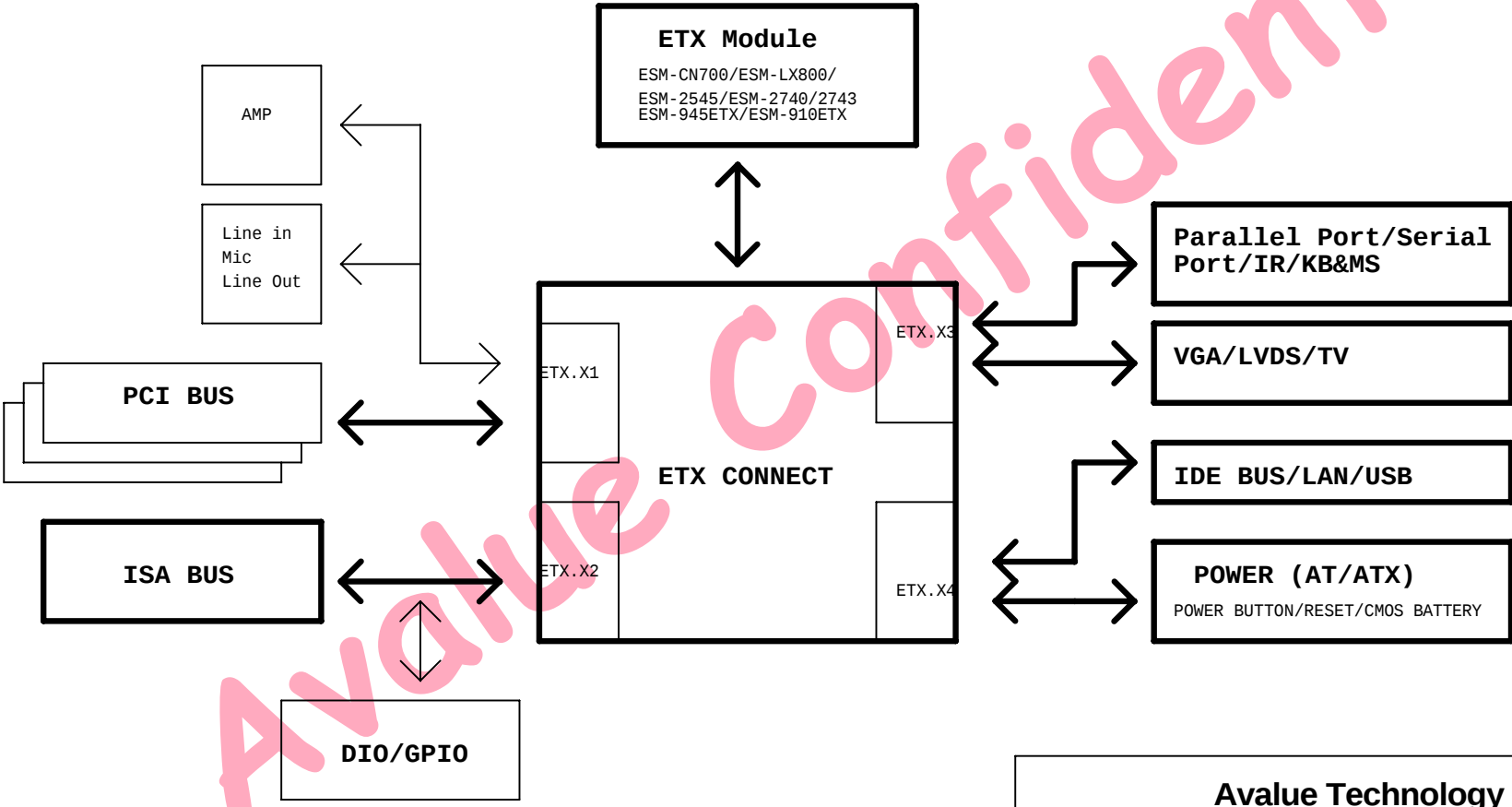
FAN Con.



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Title	08-Power		
Size	A3	Document Number	EEV-EX03
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BLOCK DIAGRAM



Avalue Confidential

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Title			
10-HISTORY			
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