# **EQM-CDV**

**Intel Cedarview Qseven Module** 

# **User's Manual**

1<sup>st</sup> Ed – 20 February 2012

Part No. E2047221100R

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THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

- (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.
- (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRED OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

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#### **EQM-CDV**

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# **Headquarters and Branch**

# Avalue Technology Inc.

7F, 228, Lian-cheng Road, Chung Ho City, Taipei,

Taiwan

Tel:+886-2-8226-2345 Fax: +886-2-8226-2777

Information:sales@avalue.com.tw Service: service@avalue.com.tw

# **Avalue Europe**

Tel: (732) 414-6500

Fax: (732) 414-6501

**Avalue USA** 

Avalue Technology Inc.

9 Timber Lane, Marlboro, NJ 07746-1443

Information: sales@avalue-usa.com

Service: support@avalue-usa.com

# **Avalue Europe A/S**

Moelledalen 22C. 3140 Aalsgaarde, Denmark Tel: +45-7025-0310

Fax:+45-4975-5026

Information: sales.europe@avalue.com.tw Service: service.europe@avalue.com.tw

#### **BCM Advanced Research**

# **BCM Advanced Research** an Avalue Company

7 Marconi, Irvine, CA92618

Tel: +1-949-470-1888 Fax: +1-949-470-0971

Information: BCMSales@bcmcom.com

Web: www.bcmcom.com

# **Avalue China**

#### Avalue Technology Inc.

Room 805, Building 9, No. 99 Tianzhou Rd.,

Caohejing Development Area,

Xuhui District, Shanghai Tel: +86-21-5169-3609 Fax:+86-21-5445-3266

Information: sales.china@avalue.com.cn

Service: service@avalue.com.tw

# **Avalue Japan**

### Avalue Technology Inc.

2F keduka-Bldg, 2-27-3 Taito,

Taito-Ku, Tokyo 110-0016 Japan

Tel: +81-3-5807-2321

Fax: +81-3-5807-2322

Information: sales.japan@avalue.com.tw

Service: service@avalue.com.tw

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- 5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

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# 1. Getting Started

# **1.1 Safety Precautions**

# Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

#### Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

# 1.2 Document Amendment History

Revision	Date	Ву	Comment	
1 <sup>st</sup>	Feb 2012	Avalue	Initial Release	

# 1.3 Manual Objectives

This manual describes in details Avalue Technology EQM-CDV QSeven Module.

We have tried to include as much information as possible but we have not duplicated information that is provided in the standard IBM Technical References, unless it proved to be necessary to aid in the understanding of this board.

We strongly recommend that you study this manual carefully before attempting to set up EQM-CDV QSeven Module or change the standard configurations. Whilst all the necessary information is available in this manual we would recommend that unless you are confident, you contact your supplier for guidance.

Please be aware that it is possible to create configurations within the CMOS RAM that make booting impossible. If this should happen, clear the CMOS settings, (see the description of the Jumper Settings for details).

If you have any suggestions or find any errors regarding this manual and want to inform us of these, please contact our Customer Service department with the relevant details.

# 1.4 System Specifications

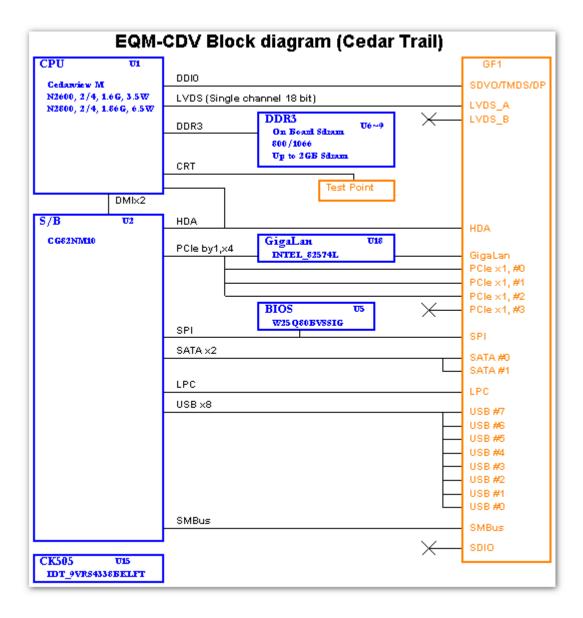
Cuetem					
System	Out and total ODY MANOROO A COURT OF NICES A COURT OF NICES				
CPU	Onboard Intel CDV-M N2600 1.6GHz or N2800 1.86GHz CPU				
BIOS	AMI 16M-bit SPI BIOS				
System Chipset	Intel NM10 Express Chipset				
I/O Chip	N/A				
System Memory	Onboard DDR3 800/1066, up to 2GB DDR3 800/1066 SDRAM				
SSD	N/A				
Watchdog Timer	Reset: 1 sec.~65535 sec./min. and 1 sec. or 1 min./step				
H/W Status	Monitoring system temperature, voltage. Auto trotting control when CPU				
Monitor	overheats				
Expansion	LPC, SMBus, HD audio I/F				
1/0					
MIO	2 x SATA ports to baseboard				
USB	8 x USB 2.0 ports to baseboard				
IrDA	N/A				
External I/O Connector	Qseven spec 1.2 connector for expansions				
Display					
Chipset	Intel Cedarview integrated graphics				
Resolution	HDMI mode: 1920 x 1200 @ 60Hz				
Resolution	LCD/Simultaneous mode : 1366 x 768 @ 60 Hz (CDV-M)				
Multiple	LIDML - LV/DC to be a beauty				
Display	HDMI + LVDS to baseboard				
LCD	Cinale about al 40 kit LVDC				
Interface	Single channel 18 bit LVDS				
TV-out	N/A				
Built-in Touch Screen	(Optional)				
Audio					
AC97 Codec	N/A				
Audio Interface	N/A				
Ethernet					
140101	1 x Intel 82574L Gigabit Ethernet				
LAN Chip					
	10/100/1000 Base-Tx Gigabit Ethernet Compatible				
Ethernet Interface					

# EQM-CDV

Mechanical &	
Environmental	
Power Requirement	+5V
ACPI	Single power ATX Support S0, S3, S4, S5
ACPI	ACPI 3.0 Compliant
Power Type	Qseven power spec
Operating Temp.	0 to 60°C
Storage Temp.	-20~-80°C
Operating Humidity	0%~90% relative humidity, non-condensing
Size (L x W)	70mm x 70mm
Weight	TBD

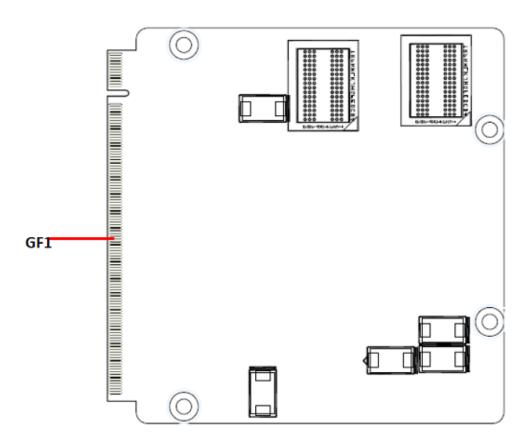
# 1.5 Architecture Overview—Block Diagram

The following block diagram shows the architecture and main components of EQM-CDV QSeven Module.



# 2 Hardware Configuration

# 2.1 Product Overview



# 2.2 Installation Procedure

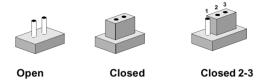
This chapter explains you the instructions of how to setup your system.

- 1. Turn off the power supply.
- 2. Insert the Q7 module into the carrier board (be careful with the orientation).
- Insert all external cables for hard disk, floppy, keyboard, mouse, USB etc. except for flat panel. A CRT monitor must be connected in order to change CMOS settings to support flat panel.
- 4. Connect power supply to the board via the ATXPWR.
- 5. Turn on the power.
- 6. Enter the BIOS setup by pressing the delete key during boot up. Use the "LOAD BIOS DEFAULTS" feature. The *Integrated Peripheral Setup* and the *Standard CMOS Setup* Window must be entered and configured correctly to match the particular system configuration.
- 7. If TFT panel display is to be utilized, make sure the panel voltage is correctly set before connecting the display cable and turning on the power.

# 2.3 Jumper and Connector List

You can configure your board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch.

It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To "close" a jumper you connect the pins with the clip. To "open" a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either two pins.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers.

Connectors on the board are linked to external devices such as hard disk drives, a keyboard, or floppy drives. In addition, the board has a number of jumpers that allow you to configure your system to suit your application.

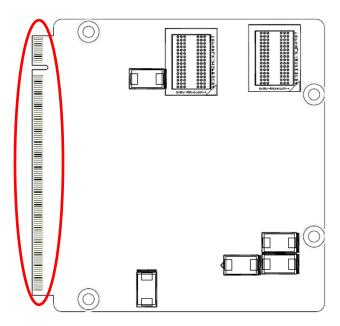
If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

The following tables list the function of each of the board's jumpers and connectors.

Connectors					
Label	Function	Note			
GF1	QSeven connector				

# 2.4 Setting Jumpers & Connectors

# 2.4.1 QSeven connector (GF1)



\*Default

Signal	PIN	PIN	Signal
GND1	1	2	GND2
GBE_MDI3-	3	4	GBE_MDI2-
GBE_MDI3+	5	6	GBE_MDI2+
GBE_LINK100#	7	8	GBE_LINK1000#
GBE_MDI1-	9	10	GBE_MDI0-
GBE_MDI1+	11	12	GBE_MDI0+
NC	13	14	GBE_ACT#
GBE_CTREF	15	16	SUS_S5#
WAKE#	17	18	SUS_S3#
SUS_STAT#	19	20	PWRBTN#
SLP_BTN#	21	22	LID_BTN#
GND3	23	24	GND4
GND5	25	26	PWGIN
BATLOW#	27	28	RSTBTN#
SATA0_TX+	29	30	SATA1_TX+
SATA0_TX-	31	32	SATA1_TX-
SATA_ACT#	33	34	GND6
SATA0_RX+	35	36	SATA1_RX+
SATA0_RX-	37	38	SATA1_RX-

Signal	PIN	PIN	Signal
GND7	39	40	GND8
BIOS_DISABLE#	41	42	NC
NC	43	44	NC
NC	45	46	NC
NC	47	48	NC
NC	49	50	NC
NC	51	52	NC
NC	53	54	NC
NC	55	56	NC
GND9	57	58	GND10
HDA_SYNC	59	60	SMB_CLK
HDA_RST#	61	62	SMB_DAT
HDA_BCLK	63	64	SMB_ALERT#
HDA_SDI	65	66	NC
HDA_SDO	67	68	NC
THRM#	69	70	NC
NC	71	72	NC
GND11	73	74	GND12
USB_P7-	75	76	USB_P6
USB_P7+	77	78	USB_P6
USB_6_7_OC#	79	80	USB_4_5_OC#
USB_P5-	81	82	USB_P4-
USB_P5+	83	84	USB_P4+
USB_2_3_OC#	85	86	USB_0_1_OC#
USB_P3-	87	88	USB_P2-
USB_P3+	89	90	USB_P2+
NC	91	92	NC
USB_P1-	93	94	USB_P0-
USB_P1+	95	96	USB_P0+
GND13	97	98	GND14
LVDS_A0+	99	100	NC
LVDS_A0-	101	102	NC
LVDS_A1+	103	104	NC

Signal	PIN	PIN	Signal
LVDS_A1-	105	106	NC
LVDS_A2+	107	108	NC
LVDS_A2-	109	110	NC
LVDS_PPEN	111	112	LVDS_BLEN
LVDS_A3+	113	114	NC
LVDS_A3-	115	116	NC
GND15	117	118	GND16
LVDS_A_CLK+	119	120	NC
LVDS_A_CLK-	121	122	NC
LVDS_BLT_CTRL	123	124	NC
LVDS_DID_DAT	125	126	LVDS_BLC_DAT
LVDS_DID_CLK	127	128	LVDS_BLC_CLK
NC	129	130	NC
SDVO_BCLK+	131	132	NC
SDVO_BCLK-	133	134	NC
GND17	135	136	GND18
SDVO_GREEN+	137	138	NC
SDVO_GREEN-	139	140	NC
GND19	141	142	GND20
SDVO_BLUE+	143	144	NC
SDVO_BLUE-	145	146	NC
GND21	147	148	GND22
SDVO_RED+	149	150	SDVO_CTRL_DAT
SDVO_RED-	151	152	SDVO_CTRL_CLK
HDMI_HPD#	153	154	NC
PCIE_CLK_REF+	155	156	PCIE_WAKE#
PCIE_CLK_REF-	157	158	PCIE_RST#
GND23	159	160	GND24
NC	161	162	NC
NC	163	164	NC
GND25	165	166	GND26
PCIE2_TX+	167	168	PCIE2_RX+
PCIE2_TX-	169	170	PCIE2_RX-

Signal	PIN	PIN	Signal
EXCD0_PERST#	171	172	EXCD1_PERST#
PCIE1_TX+	173	174	PCIE1_RX+
PCIE1_TX-	175	176	PCIE1_RX-
NC	177	178	NC
PCIE0_TX+	179	180	PCIE0_RX+
PCIE0_TX-	181	182	PCIE0_RX-
GND27	183	184	GND28
LPC_AD0	185	186	LPC_AD1
LPC_AD2	187	188	LPC_AD3
LPC_CLK	189	190	LPC_FRAME#
SERIRQ	191	192	LPC_LDRQ#
VCC_RTC	193	194	SPKR
FAN_TACHOIN	195	196	FAN_PWMOUT
GND29	197	198	GND30
RSVD199	199	200	RSVD200
RSVD201	201	202	NC
RSVD203	203	204	NC
VCC_5V_SB1	205	206	VCC_5V_SB2
NC	207	208	NC
NC	209	210	NC
VCC1	211	212	VCC2
VCC3	213	214	VCC4
VCC5	215	216	VCC6
VCC7	217	218	VCC8
VCC9	219	220	VCC10
VCC11	221	222	VCC12
VCC13	223	224	VCC14
VCC15	225	226	VCC16
VCC17	227	228	VCC18
VCC19	229	230	VCC20

# 3.BIOS Setup

# 3.1 Introduction

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

# 3.2 Starting Setup

The AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the CMOS and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways: By pressing <Del> immediately after switching the system on, or By pressing the <Del> key when the following message appears briefly at the bottom of the screen during the POST (Power On Self Test).

#### **Press DEL to enter SETUP**

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to.

Press F1 to Continue, DEL to enter SETUP

# 3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
<b>↑</b>	Move to previous item
$\downarrow$	Move to next item
<b>←</b>	Move to the item in the left hand
$\rightarrow$	Move to the item in the right hand
Esc key	Main Menu Quit and not save changes into CMOS Status Page Setup Menu and Option Page Setup Menu Exit current page and return to Main Menu
PgUp key	Increase the numeric value or make changes
PgDn key	Decrease the numeric value or make changes
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
(Shift) F2 key	Change color from total 16 colors. F2 to select color forward, (Shift) F2 to select color backward
F3 key	Calendar, only for Status Page Setup Menu
F4 key	Reserved
F5 key	Restore the previous CMOS value from CMOS, only for Option Page Setup Menu
F6 key	Load the default CMOS value from BIOS default table, only for Option Page Setup Menu
F7 key	Load the default
F8 key	Reserved
F9 key	Reserved
F10 key	Save all the CMOS changes, only for Main Menu

# Navigating Through The Menu Bar

Use the left and right arrow keys to choose the menu you want to be in.



**Note:** Some of the navigation keys differ from one screen to another.

# To Display a Sub Menu

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A ">" pointer marks all sub menus.

# 3.4 Getting Help

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the F1 key again.

# 3.5 In Case of Problems

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the AMI BIOS supports an override to the CMOS settings which resets your system to its defaults.

The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

# 3.6 BIOS setup

Once you enter the AMI BIOS CMOS Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

#### 3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



### 3.6.1.1 System Language

Use this option to select system language

#### **3.6.1.2** System Date

Use the system time option to set the system time. Manually enter the hours, minutes and seconds.

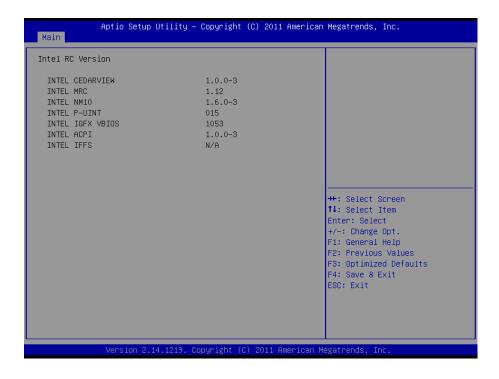
#### **3.6.1.3 System Time**

Use the system Date option to set the system date. Manually enter the day, month and year.



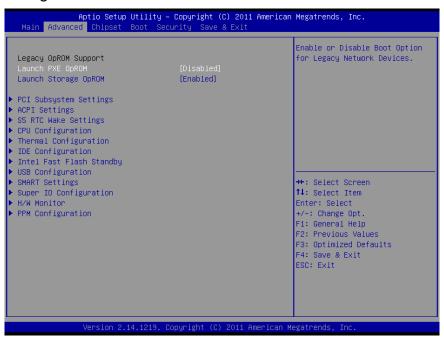
**Note:** BIOS setup screens shown in this chapter are for reference only, and may not exactly match what you see on your screen. Visit the Avalue website (<a href="www.avalue.com.tw">www.avalue.com.tw</a>) to download the latest product and BIOS information.

### 3.6.1.4 Intel RC Version



# 3.6.2 Advanced BIOS settings

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



Item	Options	Description
Launch PXE OpROM	Disabled,	Enable or disable Boot Option for Legacy
Laurich FAE Opkowi	Enabled	Network Devices
Loungh Storage OnDOM	Disabled,	Enable or disable Boot Option for Legacy
Launch Storage OpROM	Enabled	Mass storage devices With Option ROM.

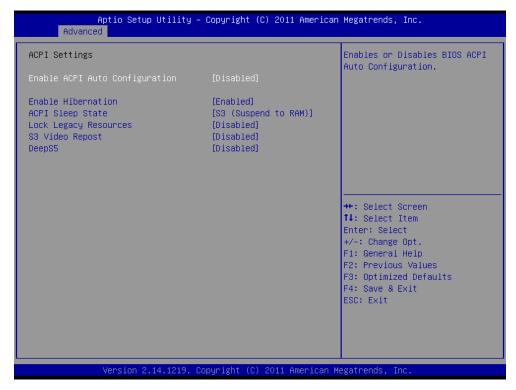
# 3.6.2.1 PCI Subsystem Settings



Item	Options	Description
	32 PCI Bus Clocks	
	64 PCI Bus Clocks	
	96 PCI Bus Clocks	
DCI Lotonov Timor	128 PCI Bus Clocks	Value to be programmed into PCI Latency
PCI Latency Timer	160 PCI Bus Clocks	Timer Register.
	192 PCI Bus Clocks	
	224 PCI Bus Clocks	
	248 PCI Bus Clocks	
VCA Polette Speen	Enabled	Enables or Disables VGA Palette registers
VGA Palette Snoop	Disabled	Snooping.
PERR# Generation	Enabled	Enables or Disables PCI Device to Generate
PERR# Generation	Disabled	PERR#
CERR# Comparation	Enabled	Enables or Disables PCI Device to Generate
SERR# Generation	Disabled	SFRR#

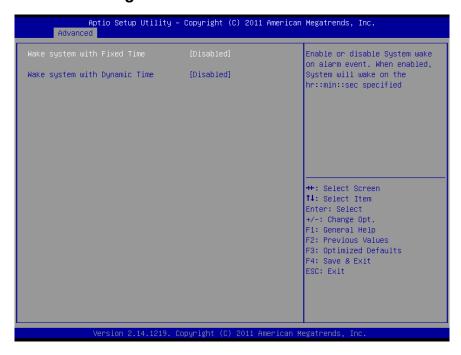
# 3.6.2.2 ACPI Settings

You can use this item to set up ACPI Configuration.



Item	Options	Description
Enable ACPI Auto Configuration	Disabled, Enabled	Enables or Disables BIOS ACPI Auto Configuration.
Enable Hibernation	Disabled, Enabled	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	Suspend Disabled, S3 (Suspend to RAM)	Select the highest ACPI sleep state the system will enter, when the SUSPEND button is pressed.
Lock Legacy Resources	Disabled, Enabled	Enables or Disables Lock of Legacy Resources.
S3 video Repost	Disabled, Enabled	Enable or Disable S3 video repost
DeepS5	Disabled, Enabled	Enter DeepS5

# 3.6.2.3 S5 RTC Wake settings



Item	Options	Description
Wake system with Fixed Time	Disabled, Enabled	Enables or Disables wake on alarm event. When enabled, System will wake on the hr::min::sec specified.
Wake system with Dynamic Time	Disabled, Enabled	Enables or Disables wake on alarm event. When enabled, System will wake on the current time + Increase minutes (s)

# 3.6.2.4 CPU Configuration

Use the CPU configuration menu to view detailed CPU specification and configure the CPU.



Item	Options	Description
Hyper-Threading	Disabled, Enabled	This item allows you to enable or disable Intel® Hyper Threading technology.
Core-Multi Processing	Disabled, Enabled	Enable or Disable Core-Multi Processing mode
Execute Disable Bit	Disabled, Enabled	This item allows you to enable or disable the No-Execution page protection technology.
Limit CPUID Maximum	Disabled, Enabled	This item allows you to limit CPUID maximum Value.

# 3.6.2.5 Thermal Configuration



# 3.6.2.5.1 CPU Thermal Configuration



Item	Options	Description
DTS SMM	Enabled Disabled Critical Temp reporting (Out of Spec)	Disabled: ACPI thermal management uses EC reported temperature values.  Enabled: ACPI thermal management uses DTS SMM mechanism to obtain CPU temperature values.  Out of spec: ACPI thermal management uses EC reported temperature values and DTS SMM is used to handle Out of spec condition.

# 3.6.2.5.2 Platform Thermal Configuration



Item	Options	Description
	POR 15C 23C 31C 39C	
Critical Trip Point	47C 55C 63C 71C 79C 87C 95C	This value controls the temperature of the ACPI Critical Trip Point – the point in which the OS will shut the system off. NOTE: 100C is the Plan Of Record (POR) for all Intel mobile
	111C 119C 127C	
Active Trip Point Lo Fan Speed	Disabled 15C 23C	This value controls the temperature of the ACPI Active Trip Point – the point in which the OS will turn the processor fan on low
Active Trip Point Hi Fan Speed	31C 39C 47C	This value controls the temperature of the ACPI Active Trip Point – the point in which the OS will turn the processor fan on high
Passive Trip Point	55C 63C 71C 79C 87C 95C 103C 111C	This value controls the temperature of the ACPI Passive Trip Point - the point in which the OS will begin throttling the processor.

# **EQM-CDV**

Passive TC1 Value	1 – 16	This value sets the TC1 -2 value for the ACPI passive cooling Formula. Range 1 - 16
Passive TC2 Value	1 – 10	
Passive TSP Value	2 - 32	This item sets the TSP value for the ACPI Passive Cooling Formula. It represents in tenths of a second how often the OS will read the temperature when passive cooling is enabled Range 2- 32

# 3.6.2.6 IDE Configuration



Item	Options	Description
SATA Controller(s)	Enabled	SATA Ports (0-3) Device Names if Present
SATA Controller(s)	Disabled	and Enabled.
Configure SATA as	IDE	Select a configuration for SATA Controller
Configure SATA as	AHCI	Select a configuration for SATA Controller

# 3.6.2.6 Intel Fast Flash Standby



### **EQM-CDV**

Item	Options	Description
iFFS Support	Enabled Disabled	Enable or Disable iFFS

# 3.6.2.7 USB Configuration

The USB configuration menu is used to read USB configuration information and configure USB.



Item	Options	Description
Legacy USB support	Enabled Disabled Auto	Enables Legacy USB support. AUTO disables legacy support if no USB devices are connected. DISABLE will keep USB devices available only for EFI applications.
ECHI hand-off	Enabled Disabled	This is a workaround for OSes without EHCl hand-off support. The EHCl ownership change should be claimed by EHCl driver.
USB transfer time-out	1sec / 5sec 10sec / 20sec	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	10sec / 20sec 30sec / 40sec	USB mass storage device Start Unit command time-out.
Device power-up delay	Auto Manual	Maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses default value: for a Root port it is 100ms, for a Hub port the delay is taken from Hub descriptor.
Device power-up delay in seconds	1~40	Delay range is 1~40 seconds, in one second increments.
USB 2.0 Flash Disk 1100	Auto Floppy Forced FDD Hard-disk CD-ROM	Mass storage device emulation type. "AUTO" enumerates devices according to their media format. Optical drives are emulated as "CDROM", drives with no media will be emulated according to a drive type.

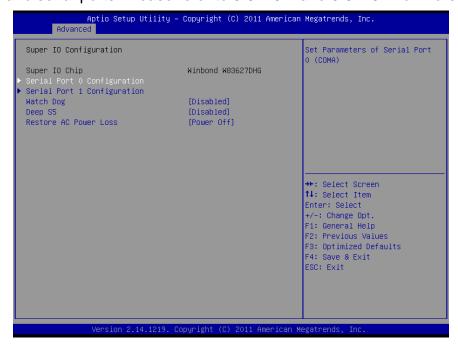
# 3.6.2.8 Smart settings



Item	Options	Description
SMART Self Test	Enabled Disabled	Run SMART Self test on all HDDs during POST

# 3.6.2.9 Super IO Configuration

You can use this item to set up or change the Super IO configuration for FDD controllers, parallel ports and serial ports. Please refer to 3.6.2.9.1 and 3.6.2.9.2 for more information.



### **EQM-CDV**

Item	Option	Description
	Disabled	
	30sec	
	40sec	
Watch Dog	50sec	Sat SIO watch dog times
Watch Dog	60sec	Set SIO watch dog timer.
	2min	
	10min	
	30min	
Doon SE	Enabled	Doon CE for nower coving
Deep S5	Disabled	Deep S5 for power saving
Bootone on AC Bower	Power Off	Specify what state to go when
Restore on AC Power	Power On	power is re-applied after a power
Loss	Last State	failure (G3 state).

# 3.6.2.9.1 Serial Port 0 Configuration



Item	Option	Description
Serial Port	Enabled, Disabled	Use the Serial port option to enable or disable the serial port.
Change Settings	Auto IO=3F8h; IRQ=4, IO=3F8h; IRQ=3,4,5,6,7,10,11,12 IO=2F8h; IRQ=3,4,5,6,7,10,11,12 IO=3E8h; IRQ=3,4,5,6,7,10,11,12 IO=2E8h; IRQ=3,4,5,6,7,10,11,12	Use the change Settings option to change the serial port IO port address and interrupt address.

# 3.6.2.9.2 Serial Port 1 Configuration



Item	Option	Description
Serial Port	Enabled,	Use the Serial port option to
Serial Fort	Disabled	enable or disable the serial port.
Change Settings	Auto IO=2F8h; IRQ=3, IO=3F8h; IRQ=3,4,5,6,7,10,11,12 IO=2F8h; IRQ=3,4,5,6,7,10,11,12 IO=3E8h; IRQ=3,4,5,6,7,10,11,12 IO=2E8h; IRQ=3,4,5,6,7,10,11,12	Use the change Settings option to change the serial port IO port address and interrupt address.
Device Mode	Standard serial port mode IrDA 1.0 (HP SIR) Mode ASKIR Mode	Change the Serial Port mode. Select <high speed=""> or <normal mode=""> mode</normal></high>

#### 3.6.2.10 H/W Monitor

The H/W Monitor shows the operating temperature, fan speeds and system voltages.



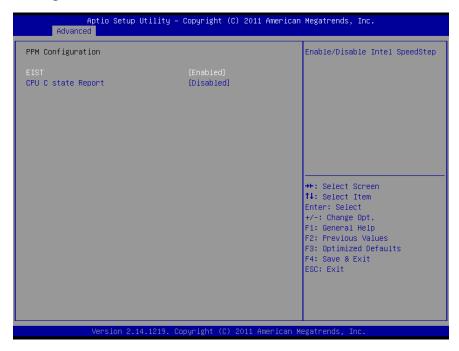
## Fan speed

- System Fan speed
- CPU Fan Speed

## Voltage

- +12V
- 3VCC
- +5V
- +1.5V

# 3.6.2.11 PPM configuration



Item	Option	Description
EIST	Enabled,	Enable/Disable Intel SpeedStep.
	Disabled	, ,
CPU C state Report	Enabled,	Enable/Disable CPU C State
CFO C State Report	Disabled	report to OS.

#### **Advanced Chipset Features** 3.6.3



## 3.6.3.1 Host bridge



## 3.6.3.1.1 Memory Frequency and Timing



Item	Option	Description
MRC Fast Boot	Enabled, Disabled	Enable or Disable MRC fast boot
Max TOLUD	Dynamic	Maximum Value of TOLUD.  Dynamic assignment would adjust TOLUD automatically based on largest MMIO length

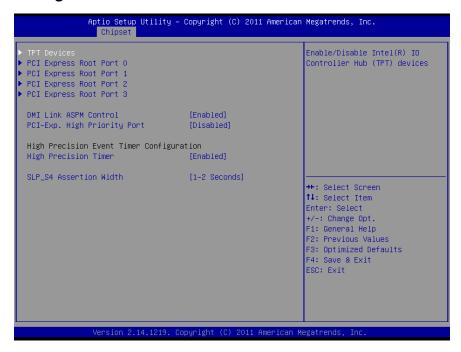
## 3.6.3.1.2 Intel IGD Configuration



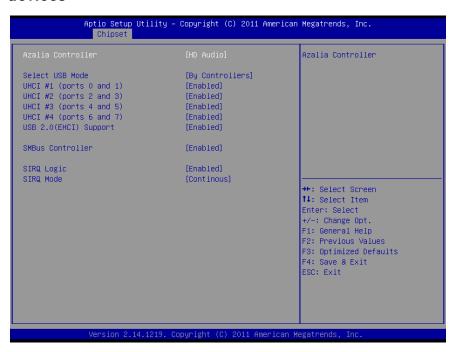
Item	Option	Description
VBIOS Version	1053 1059	Select the VBIOS version
IGFX - Boot Type	VBIOS Default CRT LVDS (eDP) HDMI/DVI CRT + LVDS (eDP) CRT + HDMI/DVI LVDS (eDP) + HDMI/DVI	Select the Video Device which will be activated during POST. This has no effect if external graphics present.
Panel Scaling	Auto Force Scaling Off Maintain Aspect Ratio	Select the LCD panel scaling option used by the Internal Graphics Device.

Active LFP	No LVDS Int-LVDS (eDP-7511)	Select the Active LFP Configuration. No LVDS: VBIOS does not enable LVDS. Int-LVDS: VBIOS enables LVDS driver by integrated encoder. SDVO LVDS: VBIOS enables LVDS driver by SDVO encoder. eDP Port-A: LFP Driven by Int-DisplayPort encoder from Port-A. eDP Port-D: LFP Driven by Int-DisplayPort encoder from Port-D (through PCH).
CH7511 EDID Panel Option	1024x768 24/1 800x600 24/1 1024x768 18/1 1024x576 18/1 1024x600 18/1 1280x800 18/1 1920x1200 18/2 640x480 24/1 800x480 24/1 1280x768 24/1 1280x1024 24/2 1440x900 24/2 1366x768 24/1 1920x1080 24/2 1680x1050 24/2	Port1-EDP to LVDS (Chrotel 7511) Panel EDID Option.
LVDS Back Light PWM	00% 25% 50% 75% 100%	Select LVDS backlight PWM duty
LVDS Back Light PWM Frequency	175 230 350 700 1k / 2k / 3k / 5k 10k / 20k / 30k / 50k / 100k	Select LVDS backlight PWM frequency
IGD Clock Source	External clock Internal clock	IGD clock selection
Fixed Graphics Memory Size	128MB 256MB	Configure Fixed Graphics memory Size
ALS Support	Enabled Disabled	Valid only for ACPI. Legacy=ALS Support through the IGD INT10 function. ACPI=ALS support through an ACPI ALS driver
BIA	Auto Disabled Level1/2/3/4/5	Auto: GMCH Use VBT Default; Level n: Enabled with Selected Aggressiveness Level.
EAP-OPT Delay	Disabled 1/2/3/4/5sec	EAP-OPT Delay for HDMI

### 3.6.3.2 South bridge



#### 3.6.3.2.1 TCP devices



Item	Option	Description
Azalia Controller	Disabled HD Audio	Azalia controller
Select USB Mode	By Ports By controllers	Select USB mode to connect USB ports

#### **EQM-CDV**

UHCI #1 (ports 0 and 1)	Enabled Disabled	
UHCI #2 (ports 2 and 3)	Enabled Disabled	Control the USB UHCI (USB1.1)
UHCI #3 (ports 4 and 5)	Enabled Disabled	functions. Disable from highest to lowest controller.
UHCI #4 (ports 6 and 7)	Enabled Disabled	
USB 2.0(EHCI) Support	Enabled Disabled	Enable or Disable USB 2.0 (EHCI) Support.
SMBus Controller	Enabled Disabled	Enable or Disable OnChip SMBus Controller.
SIRQ Logic	Enabled Disabled	Enable or Disable SIRQ logic
SIRQ Mode	Quiet Continuous	Set SIRQ mode.

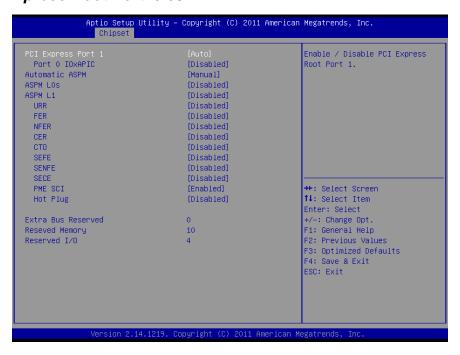
#### 3.6.3.2.2 PCI Express Root Port 0



Item	Option	Description
PCI Express Port 0	Enabled	Enable / Disable PCI Express
PGI Express Port 0	Disabled	Root Port 0.
Port 0 IOxAPIC	Enabled	Enable / Disable PCI Express
POIL O IOXAPIC	Disabled	Root Port 0 I/O APIC
	Manual	Automatically enable ASPM
Automatic ASPM	Auto	based on reported capabilities
		and known issues
	Disabled	
ASPM L0s	Root Port Only	Enable PCIe ASPM L0s
AOI W EUS	End point Port Only	Lilable i Cie Adi W Los
	Both Root And Endports	
ASPM L1	Enabled	Enable PCIe ASPM L1s
ASFIVILI	Disabled	Eliable Fole ASFIVILIS

	Enabled	PCI Express Unsupported
URR	Disabled	Request Reporting
		Enable/Disable.
EED	Enabled	PCI Express Device Fatal Error
FER	Disabled	Reporting Enable/Disable
NFER	Enabled	PCI Express Device Non-Fatal
NFER	Disabled	Error Reporting Enable/Disable.
CER	Enabled	PCI Express Device correctable
CER	Disabled	Error Reporting Enable/Disable
сто	Enabled	PCI Express Completion Timer
CIO	Disabled	TO Enable/Disable
SEFE	Enabled	Root PCI Express System Error
SEFE	Disabled	on Fatal Error Enable/Disable
	Enabled	Root PCI Express System Error
SENFE	Disabled	on Non-Fatal Error
		Enable/Disable
SECE	Enabled	Root PCI Express Error on
SECE	Disabled	correctable Error Enable/Disable
PME SCI	Enabled	PCI Express PME SCI
FIVE 3CI	Disabled	Enable/Disable.
Hot Plug	Enabled	PCI Express Hot Plug
Hot Flug	Disabled	Enable/Disable
Extra Bus Reserved		Extra Bus Reserved (0 -7)for
LXII a dus Reserved	0 - 7	bridges behind this Root Bridge.
		Reserved memory and
Reserved Memory	1 – 20MB	Prefetchable Memory (1-20MB)
		Range for this Root Bridge.
		Reserved I/O
Reserved I/O	4K/8K/12K/16K/20K	(4K/8K/12K/16K/20K) Range for
		this Root Bridge.

## 3.6.3.2.3 PCI Express Root Port 1/2/3



PCI Express Port 2	[Auto]	Enable / Disable PCI Express
Port O IOXAPIC	[Disabled]	Root Port 2.
Automatic ASPM	[Manual]	
ASPM LOS	[Disabled]	
ASPM L1	[Disabled]	
URR	[Disabled]	
FER	[Disabled]	
NFER	[Disabled]	
CER	[Disabled]	
сто	[Disabled]	
SEFE	[Disabled]	
SENFE	[Disabled]	
SECE	[Disabled]	
PME SCI	[Enabled]	→+: Select Screen
Hot Plug	[Disabled]	↑↓: Select Item
		Enter: Select
Extra Bus Reserved	0	+/-: Change Opt.
Reseved Memory	10	F1: General Help
Reserved I/O	4	F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit

PCI Express Port 3	[Auto]	Enable / Disable PCI Express
Port O IOXAPIC	[Disabled]	Root Port 3.
Automatic ASPM	[Manual]	
ASPM LOS	[Disabled]	
ASPM L1	[Disabled]	
URR	[Disabled]	
FER	[Disabled]	
NFER	[Disabled]	
CER	[Disabled]	
сто	[Disabled]	
SEFE	[Disabled]	
SENFE	[Disabled]	
SECE	[Disabled]	
PME SCI	[Enabled]	→+: Select Screen
Hot Plug	[Disabled]	↑↓: Select Item
		Enter: Select
Extra Bus Reserved	0	+/-: Change Opt.
Reseved Memory	10	F1: General Help
Reserved I/O	4	F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit

Item	Option	Description
PCI Express Port 0	Auto Enabled Disabled	Enable / Disable PCI Express Root Port 0.
Port 0 IOxAPIC	Enabled Disabled	Enable / Disable PCI Express Root Port 0 I/O APIC
Automatic ASPM	Manual Auto	Automatically enable ASPM based on reported capabilities and known issues
ASPM L0s	Disabled Root Port Only End point Port Only Both Root And Endports	Enable PCIe ASPM L0s
ASPM L1	Enabled Disabled	Enable PCIe ASPM L1s

URR	Enabled	PCI Express Unsupported
	Disabled	Request Reporting
		Enable/Disable.
FER	Enabled	PCI Express Device Fatal Error
	Disabled	Reporting Enable/Disable
NFER	Enabled	PCI Express Device Non-Fatal
	Disabled	Error Reporting Enable/Disable.
CER	Enabled	PCI Express Device correctable
	Disabled	Error Reporting Enable/Disable
сто	Enabled	PCI Express Completion Timer
	Disabled	TO Enable/Disable
SEFE	Enabled	Root PCI Express System Error
	Disabled	on Fatal Error Enable/Disable
SENFE	Enabled	Root PCI Express System Error
	Disabled	on Non-Fatal Error
		Enable/Disable
SECE	Enabled	Root PCI Express Error on
	Disabled	correctable Error Enable/Disable
PME SCI	Enabled	PCI Express PME SCI
	Disabled	Enable/Disable.
Hot Plug	Enabled	PCI Express Hot Plug
	Disabled	Enable/Disable
Extra Bus Reserved		Extra Bus Reserved (0 -7)for
	0 - 7	bridges behind this Root Bridge.
Reserved Memory		Reserved memory and
	1 – 20MB	Prefetchable Memory (1-20MB)
		Range for this Root Bridge.
Reserved I/O		Reserved I/O
	4K/8K/12K/16K/20K	(4K/8K/12K/16K/20K) Range for
		this Root Bridge.

## 3.6.4 Boot settings



#### **EQM-CDV**

Item	Option	Description
Setup Prompt Timeout	1~65535	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Bootup NumLock State	On Off	Select the keyboard NumLock state
Quiet Boot	Enabled Disabled	Enables or Disables Quiet Boot Option
Fast Boot	Enabled Disabled	Enables or Disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect for BBS boot options
GateA20 Active	Upon request Always	UPON REQUEST –GA20 can be disabled using BIOS services.  ALWAYS- do not allow disabling GA20; this option is useful when any RT code is executed above 1MB
Option ROM Messages	Force BIOS Keep current	Set display mode for Option ROM
Interrupt 19 Capture	Enabled Disabled	Enabled: allows Option ROMs to trap Int 19
CSM Support	Disabled Enabled Auto	Enable/Disable CSM Support. If Auto is selected, based on OS, CSM will be enabled/disabled automatically.
Boot Option #1/2/3	Sets the system boot order	

# 3.6.5 Security

Use the Security menu to set system and user password.



#### 3.6.5.1 Administrator Password

This setting specifies a password that must be entered to access the BIOS Setup Utility. If only the Administrator's password is set, then this only limits access to the BIOS setup program and is only asked for when entering the BIOS setup program. By default, no password is specified.

#### 3.6.5.2 User Password

This setting specifies a password that must be entered to access the BIOS Setup Utility or to boot the system. If only the User's password is set, then this is a power on password and must be entered to boot or enter the BIOS setup program. In the BIOS setup program, the User will have Administrator rights. By default, no password is specified.

#### 3.6.6 Save & Exit



#### 3.6.6.1 Save Changes and Exit

Use the save changes and reset option to save the changes made to the BIOS options and to exit the BIOS configuration setup program.



#### 3.6.6.2 Discard Changes and Exit

Use the Discard changes and Exit option to exit the system without saving the changes made to the BIOS configuration setup program.

#### 3.6.6.3 Save Changes and Reset

Any changes made to BIOS settings are stored in NVRAM. The setup program then exits and reboots the controller.

#### 3.6.6.4 Discard Changes and Reset

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.

#### 3.6.6.5 Save Changes

Changes made to BIOS settings during this session are committed to NVRAM. The setup program remains active, allowing further changes.

### 3.6.6.6 Discard Changes

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The BIOS setup continues to be active.

#### 3.6.6.7 Restore Defaults

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

#### 3.6.6.8 Save as user defaults

This option saves a copy of the current BIOS settings as the User Defaults. This option is useful for preserving custom BIOS setup configurations.

#### 3.6.6.9 Restore as user defaults

This option restores all BIOS settings to the user defaults. This option is useful for restoring previously preserved custom BIOS setup configurations.

#### 3.6.6.10 Boot override

This option lists all possible bootable devices and allows the user to override the **Boot Option Priorities** list for the current boot. If no changes have been made to the BIOS setup options, the system will continue booting to the selected device without first rebooting. If BIOS setup options have been changed and saved, a reboot will be required and the boot override selection will not be valid.

# 4. Drivers Installation



**Note**: Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

# 4.1 Install Chipset Driver (Cedarview) W7

Insert the Supporting DVD-ROM to DVD-ROM drive, click on "start" icon and it should show the index page of Avalue's products automatically. If not, locate the folder HTML and choose the product from the targeted folder.



**Note:** The installation procedures and screen shots in this section are based on W7 operating system.

**Step 1.** Locate 「Driver\_Chipset Intel\Cedarview\W7\INF」



Step 2. Click Next to start setup program



**Step3.** Click **Yes** to accept license agreement.



Step 4. Click Next to continue



Step 5. Click Next to continue



Step 6. Click Yes to finish installation

# 4.2 Install VGA Driver (For Cedarview)

Insert the Supporting DVD-ROM to DVD-ROM drive, click on "start" icon and it should show the index page of Avalue's products automatically. If not, locate the folder HTML and choose the product from the targeted folder.

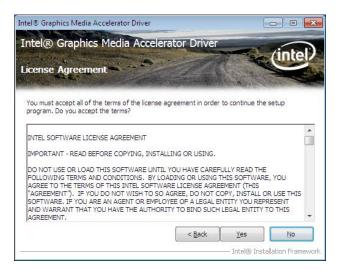


**Note:** The installation procedures and screen shots in this section are based on W7 operating system.

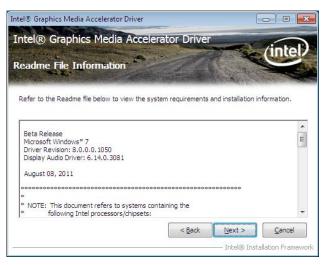
**Step 1.** Locate \( \text{VGA\Cedarview\W7\VGA} \)



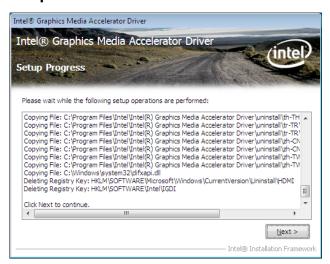
Step 2. Click Next to start setup program



**Step 3.** Click **Yes** to accept license agreement.



Step 4. Click Next to continue



Step 5. Click Next to continue



Step 6. Click Yes to finish installation

# 4.3 Install Audio Driver (For Realtek ALC892)

Insert the Supporting DVD-ROM to DVD-ROM drive, click on "start" icon and it should show the index page of Avalue's products automatically. If not, locate the folder HTML and choose the product from the targeted folder.



Note: The installation procedures and screen shots in this section are based on W7 operating system.





Step 2. Click Next to start setup program



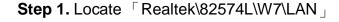
**Step 3.** Click **Finish** to complete installation

# 4.4 Install Ethernet Driver (For Realtek 82574L)

Insert the Supporting DVD-ROM to DVD-ROM drive, click on "start" icon and it should show the index page of Avalue's products automatically. If not, locate the folder HTML and choose the product from the targeted folder.

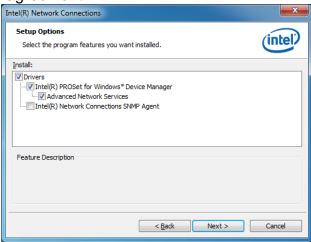


**Note:** The installation procedures and screen shots in this section are based on W7 operating system.





**Step 3.** Click **Next** to accept licence agreement.



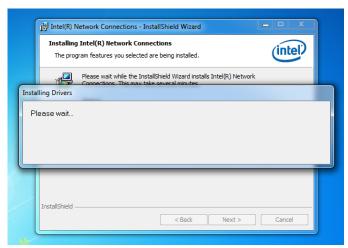
**Step 4.** Click **Next** after selecting programs to install.

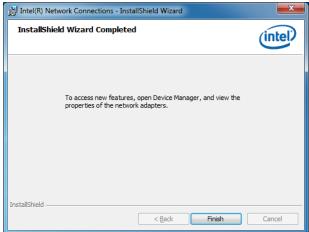


Step 2. Click Next.



Step 5. Click Install to begin installation

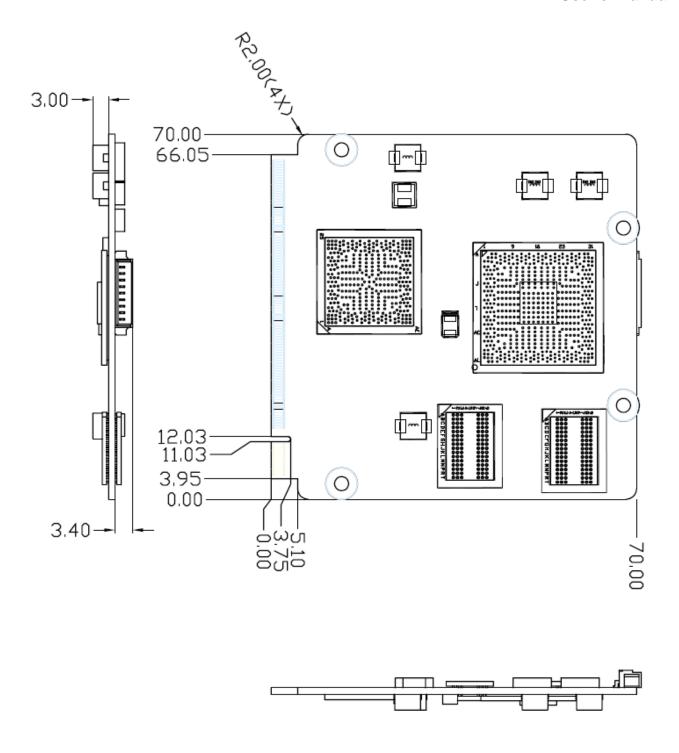




Step 6. Wait while installing.

**Step 7.** Click **Finish** to complete installation

# 4. Mechanical Drawing



# **EQM-CDV**

