

# **EPS-CFSE**

**Intel® Coffee Lake Processor w/QM87E Chassis fanless  
embedded system**

## **Quick Reference Guide**

**1<sup>st</sup> Ed –23 July 2021**

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## FCC Statement



THIS DEVICE COMPLIES WITH PART 15 FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS:

- (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE.
- (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRED OPERATION.

THIS EQUIPMENT HAS BEEN TESTED AND FOUND TO COMPLY WITH THE LIMITS FOR A CLASS "A" DIGITAL DEVICE, PURSUANT TO PART 15 OF THE FCC RULES.

THESE LIMITS ARE DESIGNED TO PROVIDE REASONABLE PROTECTION AGAINST HARMFUL INTERFERENCE WHEN THE EQUIPMENT IS OPERATED IN A COMMERCIAL ENVIRONMENT. THIS EQUIPMENT GENERATES, USES, AND CAN RADIATE RADIO FREQUENCY ENERGY AND, IF NOT INSTALLED AND USED IN ACCORDANCE WITH THE INSTRUCTION MANUAL, MAY CAUSE HARMFUL INTERFERENCE TO RADIO COMMUNICATIONS.

OPERATION OF THIS EQUIPMENT IN A RESIDENTIAL AREA IS LIKELY TO CAUSE HARMFUL INTERFERENCE IN WHICH CASE THE USER WILL BE REQUIRED TO CORRECT THE INTERFERENCE AT HIS OWN EXPENSE.

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We want you to get the maximum performance from your products. So if you run into technical difficulties, we are here to help. For the most frequently asked questions, you can easily find answers in your product documentation. These answers are normally a lot more detailed than the ones we can give over the phone. So please consult the user's manual first.

To receive the latest version of the user's manual; please visit our Web site at:

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# 1. Getting Started

## 1.1 Safety Precautions

### Warning!



Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.

### Caution!



Always ground yourself to remove any static charge before touching the CPU card. Modern electronic devices are very sensitive to static electric charges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components in a static-dissipative surface or static-shielded bag when they are not in the chassis.

## 1.2 Packing List

- 1 x EPS-CFSE Intel® Coffee Lake Processor w/QM87E Chassis fanless embedded system
- Other major components include the following:
  - Screw kit
  - Accessory box



If any of the above items is damaged or missing, contact your retailer.

## 1.3 System Specifications

<b>System</b>	
<b>CPU</b>	Intel® Core™ i7-9700TE Processor (12M Cache, up to 3.80 GHz) Intel® Core™ i5-9500TE Processor (9M Cache, up to 3.60 GHz) Intel® Core™ i3-9100TE Processor (6M Cache, up to 3.20 GHz)
<b>SBC</b>	ECM-CFS
<b>BIOS</b>	AMI UEFI BIOS, 256 Mbit SPI Flash ROM
<b>System Chipset</b>	Intel® H310 Express Chipset
<b>System Memory</b>	1 x 260-Pin SO-DIMM Socket, Max. Up to 32GB DDR4 2400/2666MHz
<b>Watchdog Timer</b>	H/W Reset, 1sec. ~ 65535sec. and 1sec./step
<b>H/W Status Monitor</b>	Monitoring System Temperature, Voltage and FAN Status with Auto Throttling Control
<b>Expansion</b>	
<b>Expansion</b>	1 x mPCIe Socket, (SATA/USB2.0) 1 x mPCIe Socket (USB, Factory Option)
<b>Storage</b>	
<b>Combination</b>	1 x mSATA (share with Expansion) 2 x 2.5" Drive Bay (Internal)
<b>Front I/O</b>	
<b>USB Port</b>	2 x USB 2.0
<b>Button</b>	1 x Push Button for Power on/off, w/ LED
<b>GPIO</b>	1 x 8-Bit GPIO (via cable, pre-break hole)
<b>Rear I/O</b>	
<b>Power connector</b>	1 x DC Jack
<b>LED</b>	1 x Power on/off LED at rear side 1 x Storage Access LED at front/rear side
<b>Serial Port</b>	1 x RS-232 (on board) 1 x RS-232 (via cable)
<b>Display</b>	2 x HDMI
<b>USB Port</b>	4 x USB 3.2 Gen. 1
<b>LAN</b>	2 x RJ45
<b>Audio</b>	1 x Mic-In, 1 x Line-Out
<b>Antenna</b>	2 x Antenna with dust cover (2 x LTE+GPS / 2 x Wifi)
<b>Display</b>	
<b>Chipset</b>	Intel® UHD Graphics 630
<b>Resolution</b>	2 x HDMI 1.4: Max. Resolution 4096x2304@30Hz
<b>Multiple Display</b>	Dual Display

## EPS-CFSE

<b>Ethernet</b>	
<b>Chipset</b>	1 x Intel i211-AT 1 x Intel i219-LM
<b>Ethernet Interface</b>	2 x 10/100/1000Base-Tx GbE compatible.
<b>Audio</b>	
<b>Chipset</b>	Realtek ALC888 HD codec
<b>Audio Interface</b>	Mic-In, Line-Out
<b>Mechanical &amp; Environmental</b>	
<b>Power Connector</b>	Lockable DC Jack
<b>Power Requirement</b>	+12Vdc ( $\pm 5\%$ )
<b>Power Type</b>	AT/ATX (ATX is default setting)
<b>ACPI</b>	Single power ATX Support S0,S3, S4, S5 ACPI 5.0 Compliant
<b>Dimension (W x L x H)</b>	9.84" x 8.66" x 2.36" (250mm x 220mm x 60mm)
<b>Weight</b>	3.5KG
<b>Mounting Kit</b>	Wall mount kit (standard)
<b>Reliability</b>	
<b>Vibration Test (operation)</b>	<p>Random Vibration Operation</p> <ol style="list-style-type: none"> <li>Test PSD: 0.00454G<sup>2</sup>/Hz, 1.5 Grms</li> <li>System condition: operation mode</li> <li>Test frequency: 5~500 Hz</li> <li>Test axis: X, Y and Z axis</li> <li>Test time: 30 minutes per each axis</li> <li>IEC60068-2-64 Test Fh</li> <li>Storage: SSD</li> </ol>
<b>Vibration Test (non-operation)</b>	<p>Sine Vibration test (non-operation)</p> <ol style="list-style-type: none"> <li>Test Acceleration: 2G</li> <li>Test frequency: 5~500 Hz</li> <li>Sweep : 1 Oct/ per one minute. (logarithmic)</li> <li>Test Axis: X, Y and Z axis</li> <li>Test time :30 min. each axis</li> <li>System condition: non-Operating mode</li> <li>Reference IEC 60068-2-6 Testing procedures</li> <li>Storage: SSD</li> </ol>
<b>Shock Test</b>	<ol style="list-style-type: none"> <li>Wave from: Half Sine wave</li> </ol>

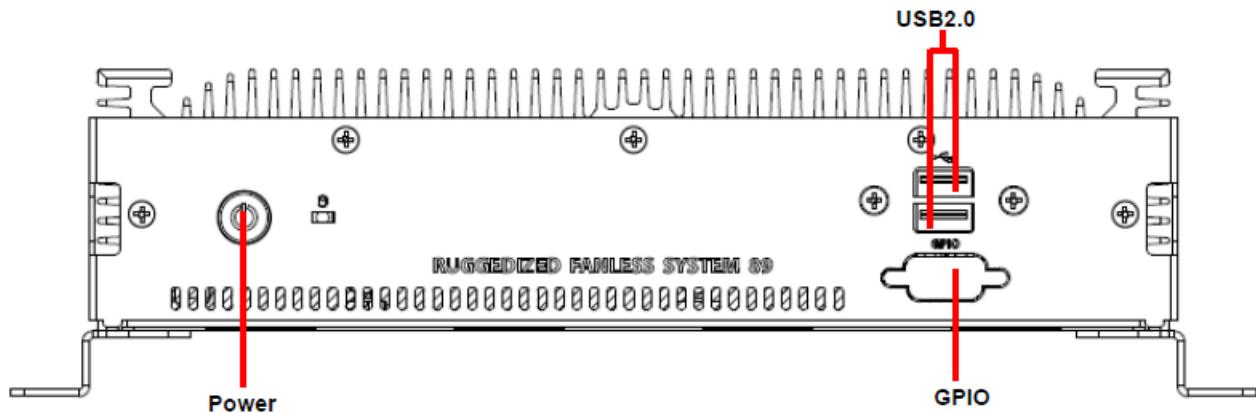
	<ul style="list-style-type: none"> <li>2. Acceleration Rate: 10g</li> <li>3. Duration Time: 11ms</li> <li>4. No. of shock: X.Y and Z axis 300 times</li> <li>5. Test Axis: X, Y and Z axis</li> <li>6. operation mode</li> <li>7. Reference IEC 60068-2-27 testing procedures</li> <li>8. Storage: SSD</li> </ul> <p>Test Eb: Shock Test</p>
<b>Package Drop Test</b>	<ul style="list-style-type: none"> <li>1. One corner, three edges, six faces</li> <li>2. ISTA 2A, IEC-60068-2-32 Test: Ed</li> </ul> <p>Test Ea: Drop Test</p> <ul style="list-style-type: none"> <li>1. Test phase: One corner, three edges, six faces</li> <li>2. Test high: 96.5cm</li> <li>3. Package weight: 5Kg</li> <li>4. 4 Test drawing</li> </ul>
<b>Package Vibration Test</b>	<p>Package Vibration Test:</p> <ul style="list-style-type: none"> <li>1. Test PSD: 0.026G<sup>2</sup>/Hz, 2.16 Grms</li> <li>2. Test frequency: 5~500 Hz</li> <li>3. Test axis: X, Y and Z axis</li> <li>4. Test time: 30 minutes per each axis</li> <li>5. IEC 60068-2-64 Test Fh</li> </ul>
<b>Operating Temperature</b>	-10°C ~ 50°C (14°F ~ 122°F) ambient w/ air flow (w/SSD)
<b>Operating Humidity</b>	40°C @ 95% Relative Humidity, Non-condensing
<b>Storage Temperature</b>	-40°C ~ 75°C (-40°F ~ 167°F)
<b>Certification</b>	CE, FCC Class A
<b>OS Supported</b>	Windows 10 (64-bit), Linux



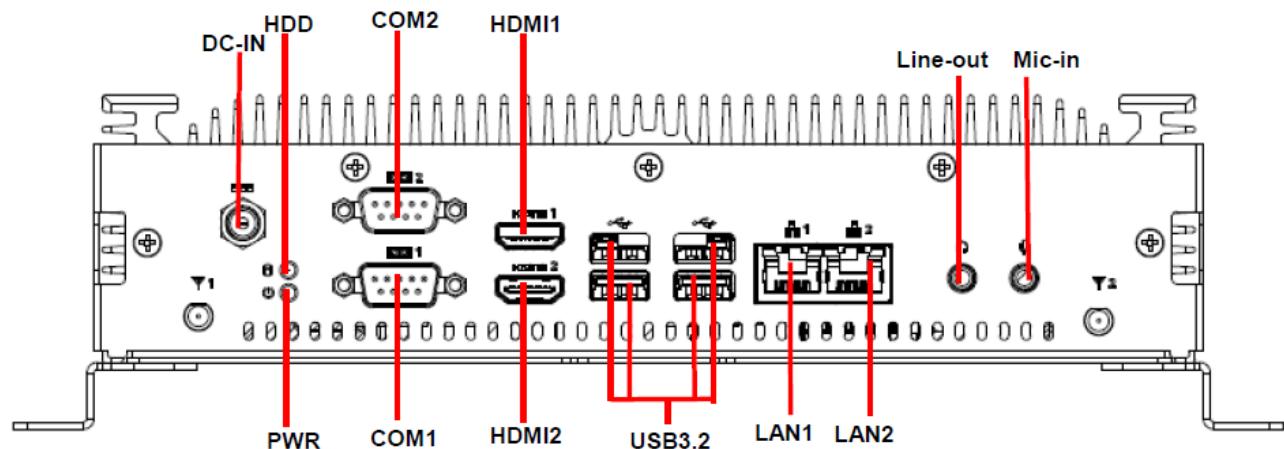
**Note:** Specifications are subject to change without notice.

## 1.4 System Overview

### 1.4.1 Front View



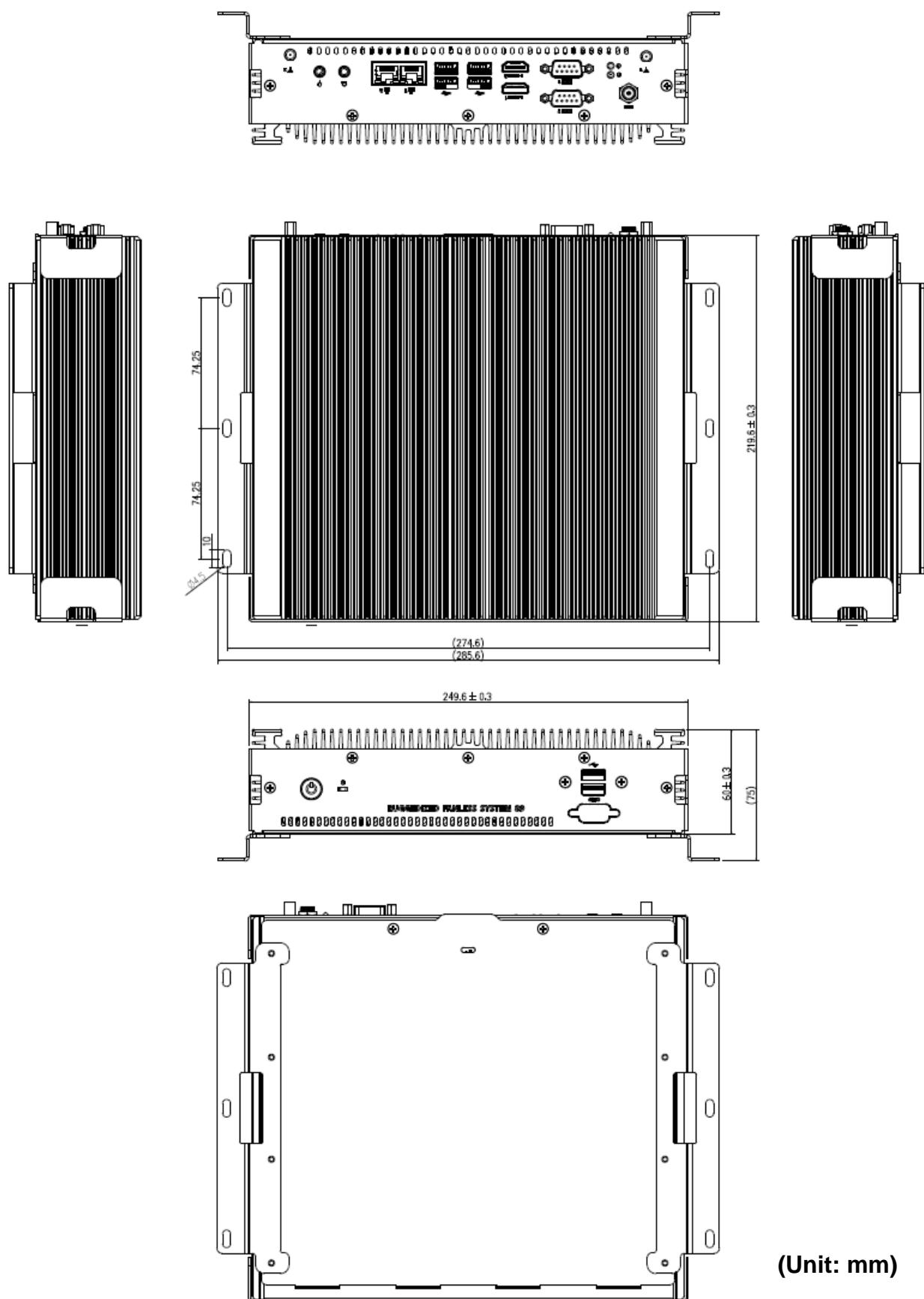
### 1.4.2 Rear View



### Connectors

Label	Function	Note
<b>Power</b>	Power on button	
<b>GPIO</b>	General purpose I/O connector	
<b>USB2.0</b>	USB 2.0 connector x 2	
<b>USB3.2</b>	USB 3.2 connector x 4	
<b>COM1/2</b>	Serial Port 1/2 connector	
<b>DC-IN</b>	DC power-in connector	
<b>LAN1/2</b>	RJ-45 Ethernet 1/2	
<b>HDMI1/2</b>	HDMI connector 1/2	
<b>HDD</b>	HDD indicator	
<b>PWR</b>	System power indicator	
<b>Mic-in</b>	Mic-in audio jack	
<b>Line-out</b>	Line-out audio jack	

## 1.5 System Dimensions



# 2. Hardware Configuration

For advanced information, please refer to:

- 1- ECM-CFS included in this manual.

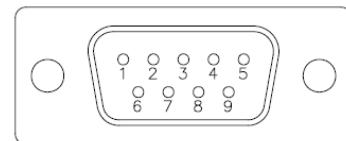
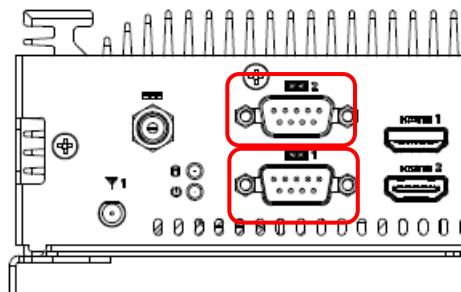


**Note:** If you need more information, please visit our website:

<http://www.alue.com.tw>

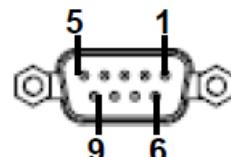
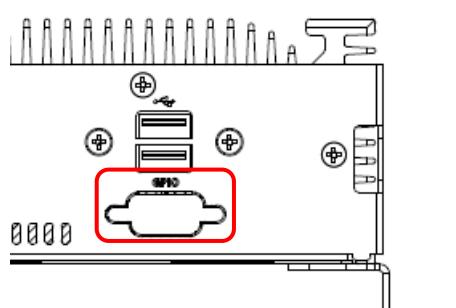
## 2.1 EPS-CFS connector mapping

### 2.1.1 Serial Port 1/2 connector (COM1/2)



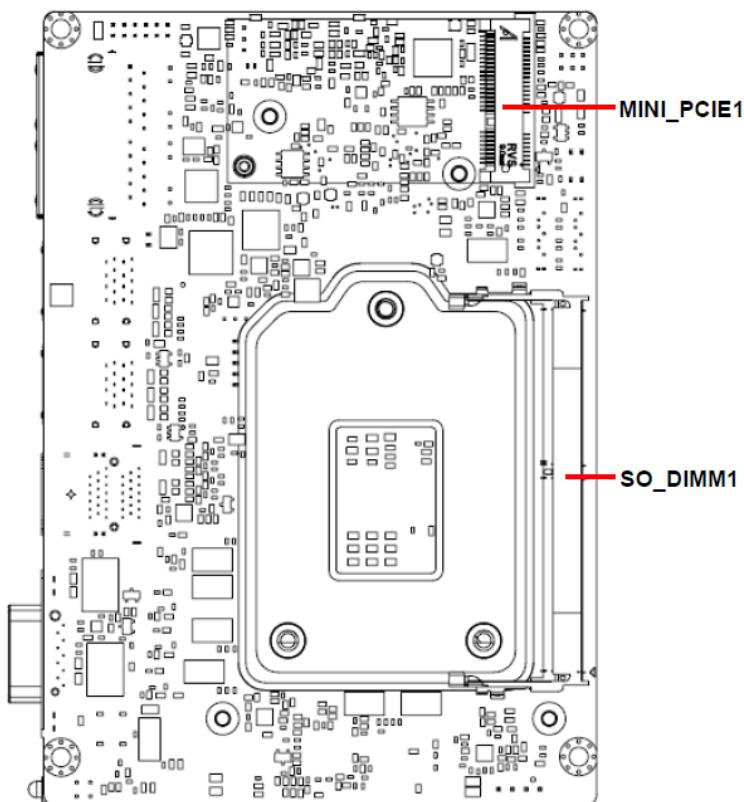
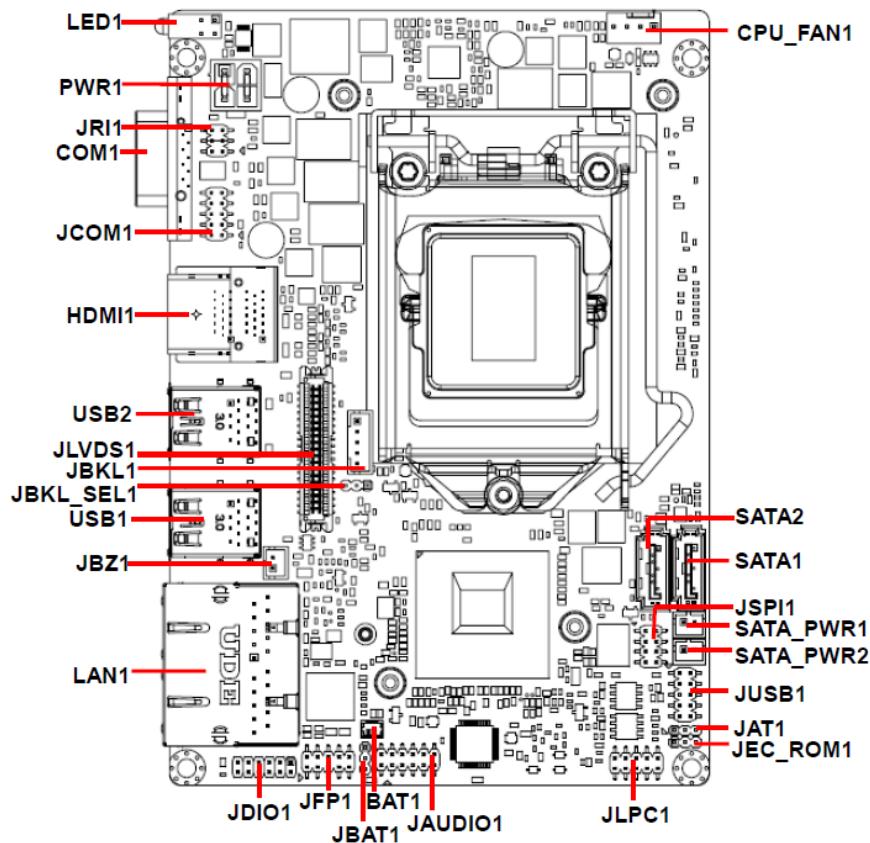
Signal	PIN	PIN	Signal
DCD#	1	6	DSR#
RXD	2	7	RTS#
TXD	3	8	CTS#
DTR#	4	9	RI#
GND	5		

### 2.1.2 General purpose I/O connector (GPIO)



Signal	PIN	PIN	Signal
GPO1	1	6	GPI3
GPI1	2	7	GPO4
GPO2	3	8	GPI4
GPI2	4	9	GND
GPO3	5		

## 2.2 ECM-CFS Overviews



## 2.3 ECM-CFS Jumper & Connector list

### Jumpers

Label	Function	Note
JBAT1	Clear CMOS	3 x 1 header, pitch 2.00 mm
JRI1	Serial port 1 pin9 signal select	3 x 2 header, pitch 2.00 mm
JAT1	AT/ATX Input power select	3 x 1 header, pitch 2.00 mm
JBKL_SEL1	LCD backlight brightness adjustment	3 x 1 header, pitch 2.00 mm

### Connectors

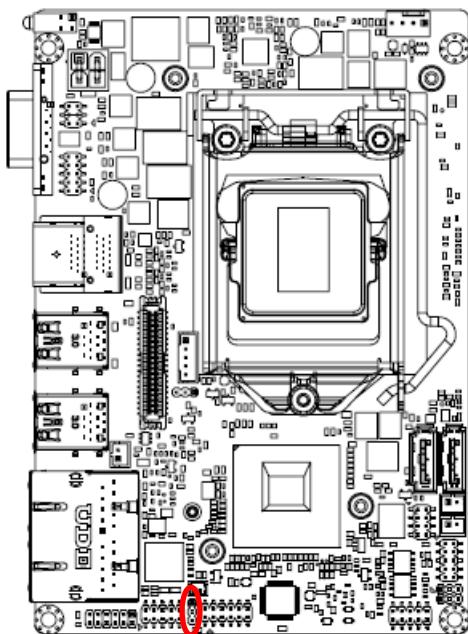
Label	Function	Note
BAT1	Battery connector	2 x 1 wafer, pitch 1.25 mm
CPU_FAN1	CPU fan connector	4 x 1 wafer, pitch 2.54 mm
JAUDIO1	Audio connector	6 x 2 header, pitch 2.00 mm
JBKL1	LCD inverter connector	5 x 1 wafer, pitch 2.00 mm Matching Connector: JST PHR-5
COM1	Serial port 1 connector	D-sub 9-pin, male
JCOM1	Serial port 2 connector	5 x 2 header, pitch 2.00 mm
JDIO1	General purpose I/O connector	6 x 2 header, pitch 2.00 mm
JFP1	Front Panel connector	5 x 2 header, pitch 2.00 mm
JLPC1	LPC connector	5 x 2 header, pitch 2.00 mm
JLVDS1	LVDS connector	20 x 2 header, pitch 1.25 mm Matching Connector: Hirose DF13-40DS-1.25C
JSPI1	SPI connector	4 x 2 header, pitch 2.00 mm
USB1/2	H310 support USB3.1 Gen1 Q370 support USB3.1 Gen2	
JUSB1	On-board header for USB2.0	5 x 2 header, pitch 2.00 mm
HDMI1	DUAL HDMI connector	
LAN1	RJ-45 Ethernet x 2	
LED1	HDD/Power LED indicator	
PWR1	Power connector	2 x 2 wafer, pitch 4.20 mm
SATA_PWR1	SATA power connector 1	2 x 1 wafer, pitch 2.00 mm
SATA_PWR2	SATA power connector 2	2 x 1 wafer, pitch 2.00 mm
SATA1/2	Serial ATA connector 1/2	

## **EPS-CFSE**

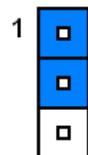
<b>JBZ1</b>	PC Buzzer header	2 x 1 wafer, pitch 2.00 mm
<b>JEC_ROM1</b>	EC Debug connector	3 x 1 header, pitch 2.00 mm
<b>MINI_PCIE1</b>	Full size Mini-PCI-e connector	
<b>SO_DIMM1</b>	DDR4 SODIMM socket	

## 2.4 Setting Jumpers & Connectors

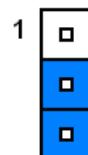
### 2.4.1 Clear CMOS (JBAT1)



Protect\*

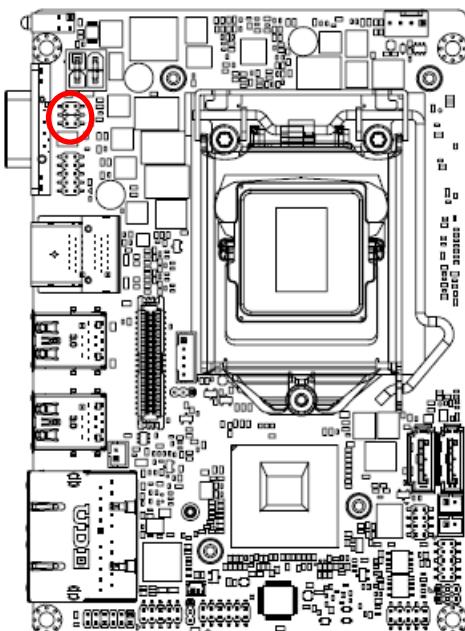


Clear CMOS

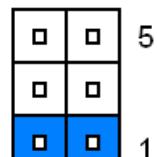


\* Default

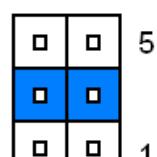
### 2.4.2 Serial port 1 pin9 signal select (JRI1)



Ring\*

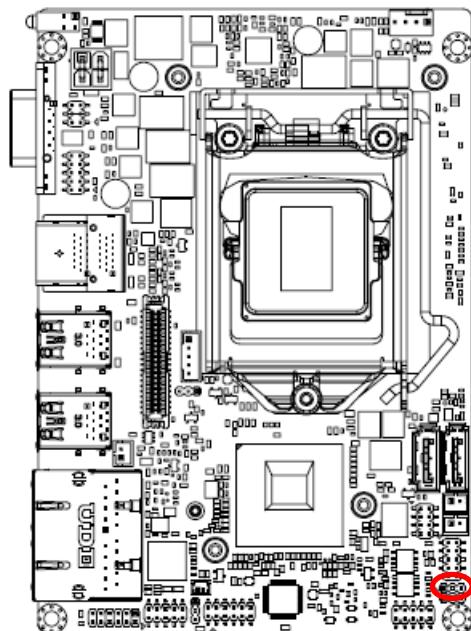


+12V

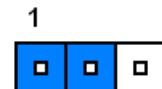


\* Default

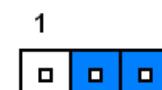
#### 2.4.3 AT/ATX Input power select (JAT1)



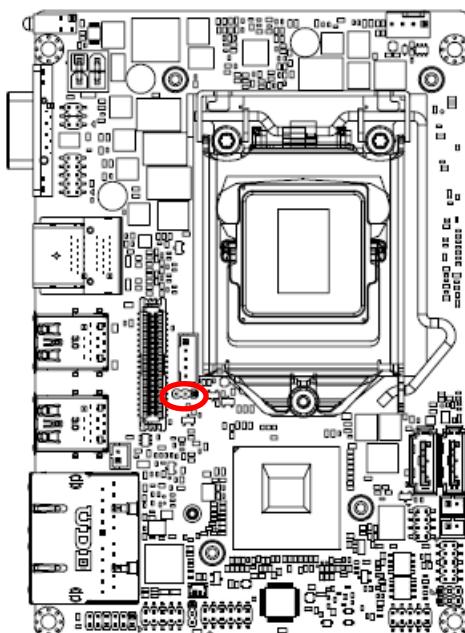
AT\*



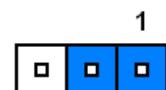
ATX



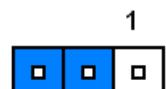
#### 2.4.4 LCD backlight brightness adjustment (JBKL\_SEL1)



PWM Mode\*

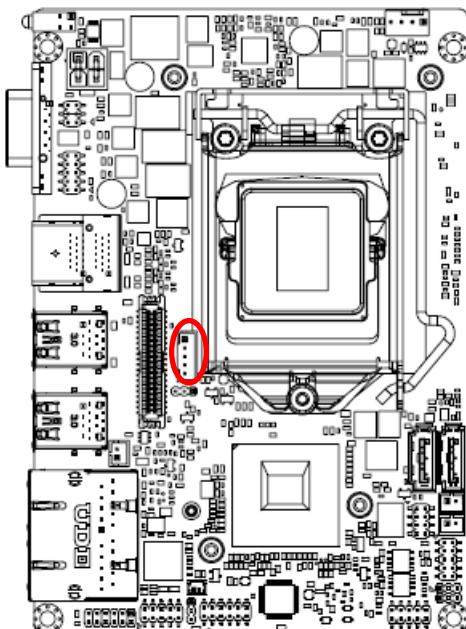


DC Mode



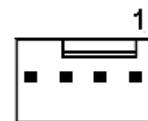
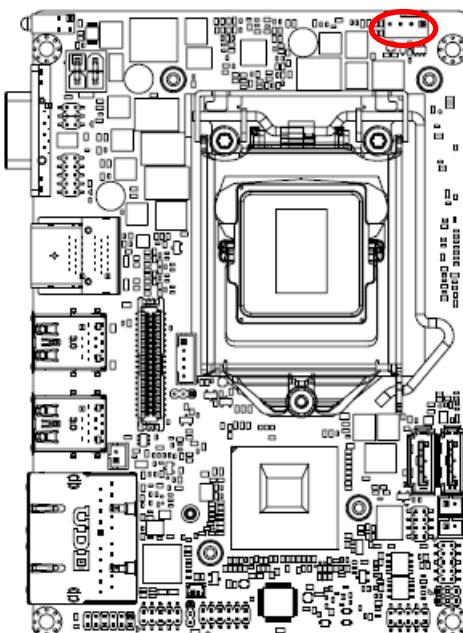
\* Default

#### 2.4.5 LCD Inverter connector (JBKL1)



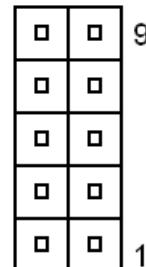
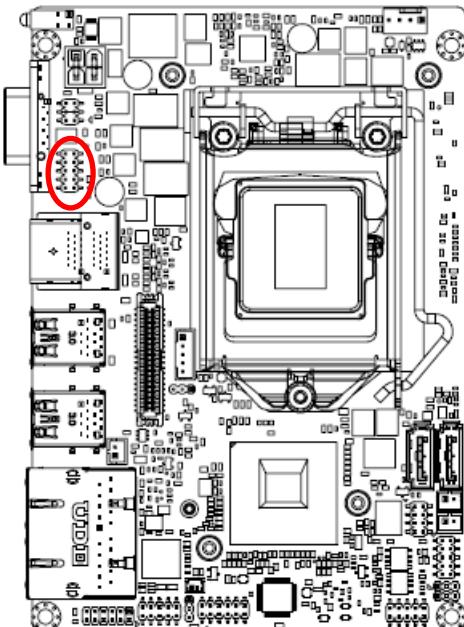
Signal	PIN
+12V	1
GND	2
BKLEN	3
VBRIGHT	4
+5V	5

#### 2.4.6 CPU fan connector (CPU\_FAN1)



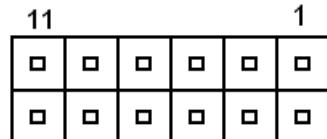
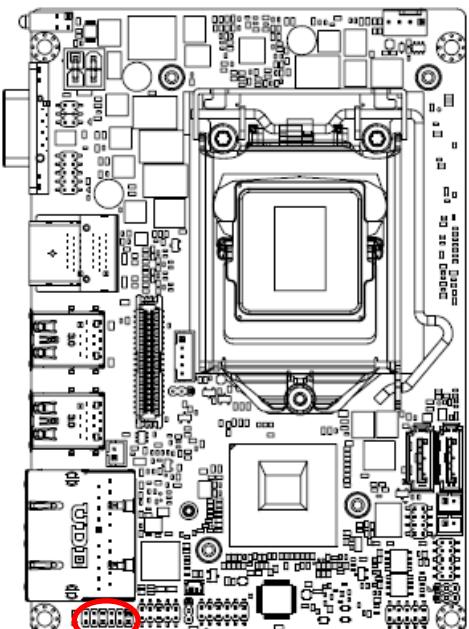
Signal	PIN
GND	1
+12V	2
CFAN_IN	3
CFAN_OUT	4

#### 2.4.7 Serial port 2 connector (JCOM1)



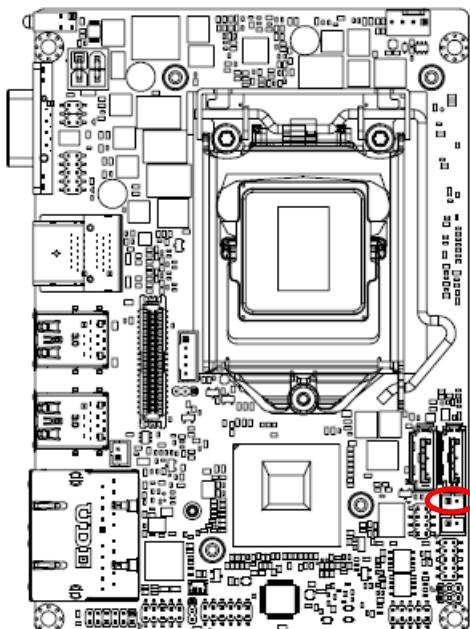
Signal	PIN	PIN	Signal
NC	10	9	COM_RI#_2
COM_CTS#_2	8	7	COM_RTS#_2
COM_DSR#_2	6	5	GND
COM_DTR#_2	4	3	COM_TXD_2
COM_RXD_2	2	1	COM_DCD#_2

#### 2.4.8 General purpose I/O connector (JDIO1)



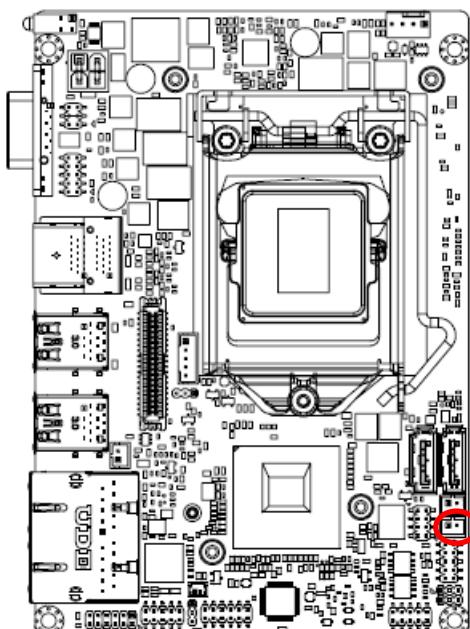
Signal	PIN	PIN	Signal
DIO_GP20	1	2	DIO_GP10
DIO_GP21	3	4	DIO_GP11
DIO_GP22	5	6	DIO_GP12
DIO_GP23	7	8	DIO_GP13
SMB_SCL_S0	9	10	SMB_SDA_S0
GND	11	12	+5V

#### 2.4.9 SATA power connector 1 (SATA\_PWR1)



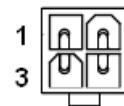
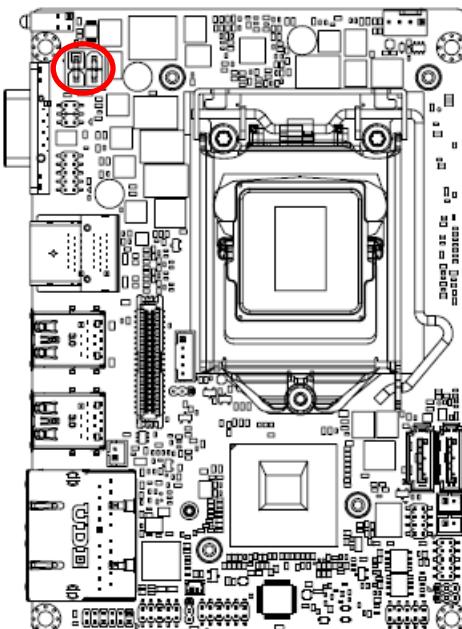
Signal	PIN
GND	1
SATA_PWR1	2

#### 2.4.10 SATA power connector 2 (SATA\_PWR2)



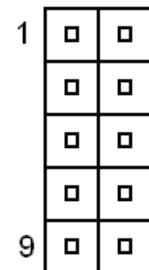
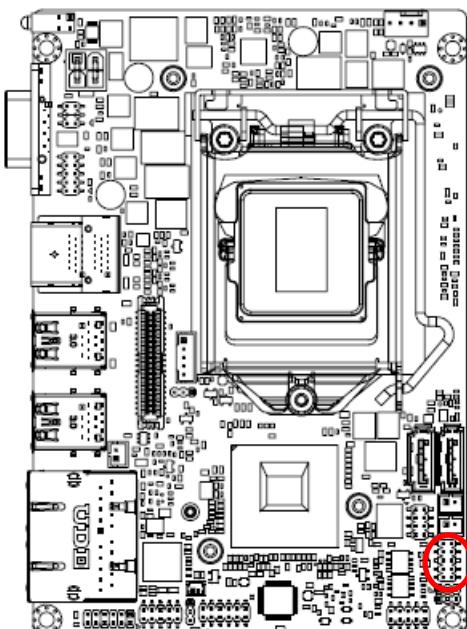
Signal	PIN
GND	1
SATA_PWR2	2

### 2.4.11 Power connector (PWR1)



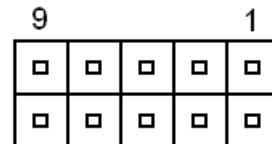
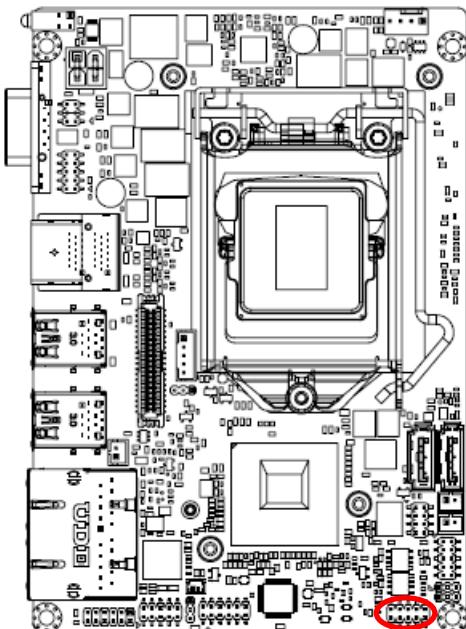
Signal	PIN	PIN	Signal
GND	1	2	GND
+12V	3	4	+12V

### 2.4.12 On-board header for USB2.0 (JUSB1)



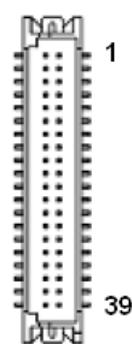
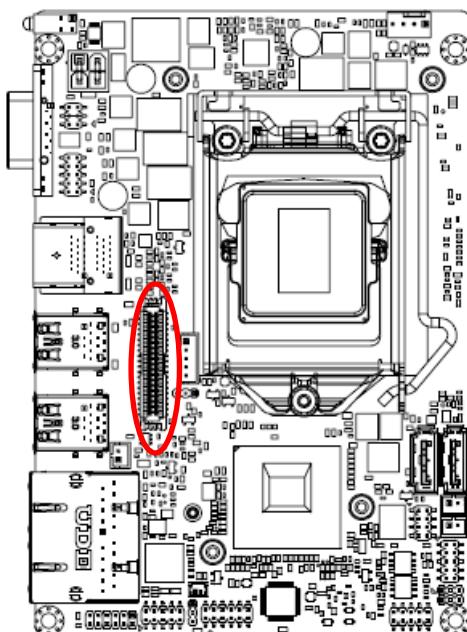
Signal	PIN	PIN	Signal
+5VSB	1	2	GND
USB_R_DN5	3	4	GND
USB_R_DP5	5	6	USB_R_DP6
GND	7	8	USB_R_DN6
GND	9	10	+5VSB

### 2.4.13 LPC connector (JLPC1)



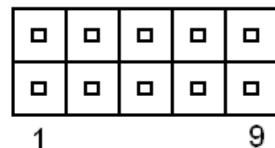
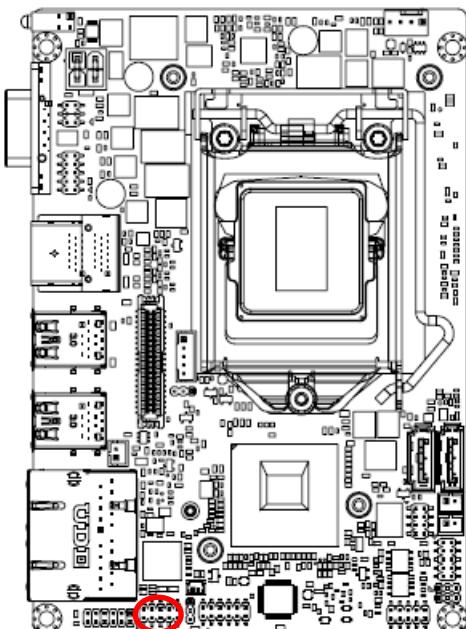
Signal	PIN	PIN	Signal
LPC_AD0	1	2	+3.3V
LPC_AD1	3	4	RST_TPM#
LPC_AD2	5	6	LPC_LFRAME#
LPC_AD3	7	8	CLK1_LPC_DEBUG
LPC_SERIRQ	9	10	GND

### 2.4.14 LVDS connector (JLVDS1)



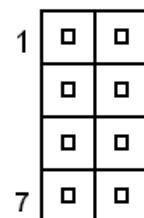
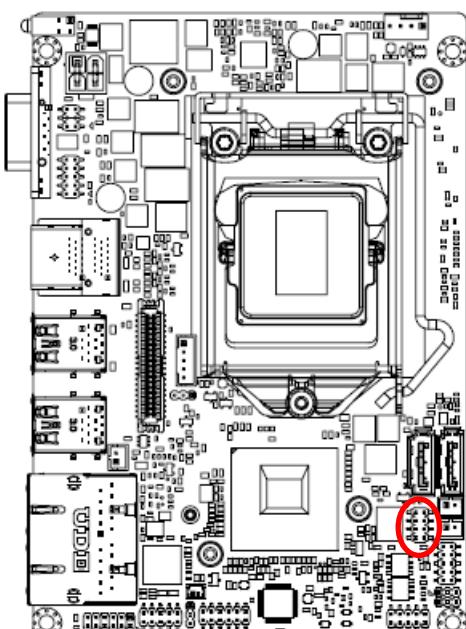
Signal	PIN	PIN	Signal
+5V	2	1	+3.3V
+5V	4	3	+3.3V
NC	6	5	NC
GND	8	7	GND
LVDS_DATA0_P	10	9	LVDS_DATA1_P
LVDS_DATA0_N	12	11	LVDS_DATA1_N
GND	14	13	GND
LVDS_DATA2_P	16	15	LVDS_DATA3_P
LVDS_DATA2_N	18	17	LVDS_DATA3_N
GND	20	19	GND
LVDS_DATA4_P	22	21	LVDS_DATA5_P
LVDS_DATA4_N	24	23	LVDS_DATA5_N
GND	26	25	GND
LVDS_DATA6_P	28	27	LVDS_DATA7_P
LVDS_DATA6_N	30	29	LVDS_DATA7_N
GND	32	31	GND
LVDS_CLK1_P	34	33	LVDS_CLK2_P
LVDS_CLK1_N	36	35	LVDS_CLK2_N
GND	38	37	GND
+12V	40	39	+5V

### 2.4.15 Front Panel connector (JFP1)



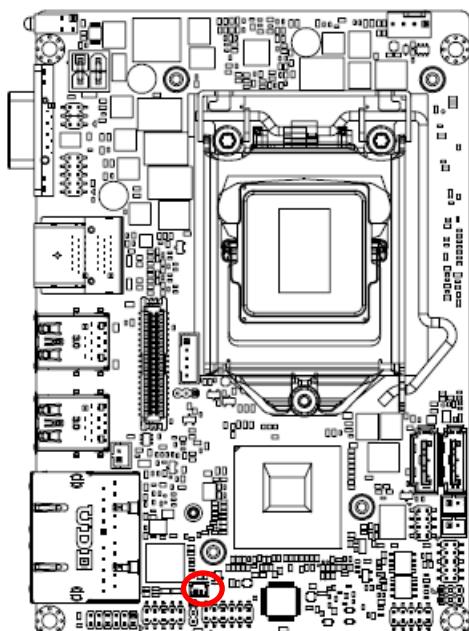
Signal	PIN	PIN	Signal
PWRBTN_IN#	1	2	GND
PM_SYSRST#	3	4	GND
FP_PWR_LED+	5	6	PWR_LED#
HDD_LED#	7	8	+5V
CASE_OPEN#	9	10	GND

### 2.4.16 SPI connector (JSPI1)



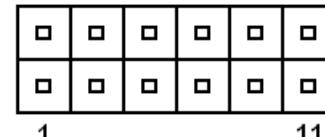
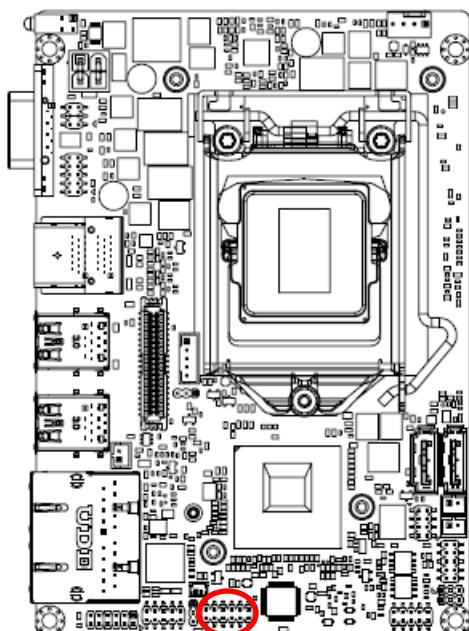
Signal	PIN	PIN	Signal
+3.3VSB	1	2	GND
SPI_CS0#	3	4	SPI_CLK
SPI_MISO	5	6	SPI_MOSI
BIOS_HOLD#	7	8	BIOS_WP#

### 2.4.17 Battery connector (BAT1)



Signal	PIN
+RTCBAT	1
GND	2

### 2.4.18 Audio connector (JAUDIO1)

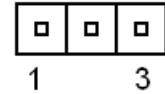
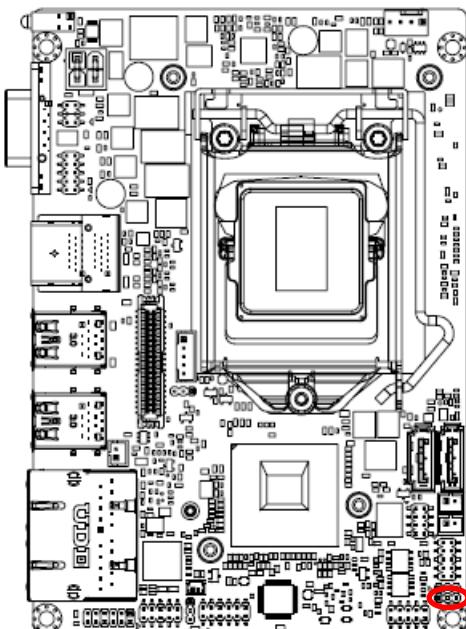


Signal	PIN	PIN	Signal
FRONT-R-OUT	1	2	FRONT-L-OUT
HD_AGND	3	4	HD_AGND
LINE1-R-IN	5	6	LIN1-L-IN
MIC1-R-IN	7	8	MIC1-L-IN
FRONT-JD	9	10	LINE1-JD
MIC1-JD	11	12	HD_AGND

#### 2.4.18.1 Signal Description – Audio connector (JAUDIO1)

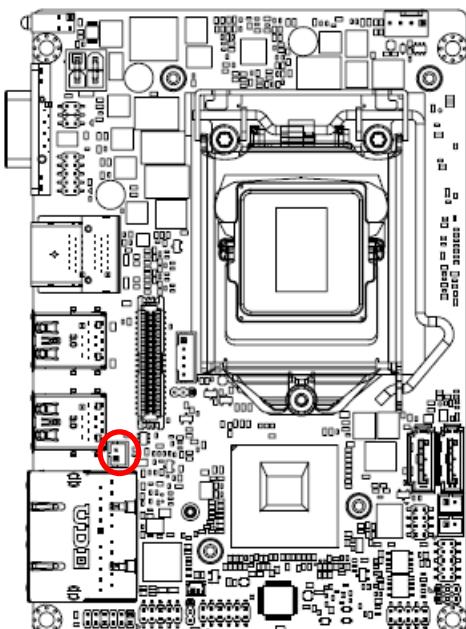
Signal	Signal Description
LINE1-JD	AUDIO IN (LINE_RIN/LIN)sense pin
FRONT-JD	AUDIO Out(ROUT/LOUT) sense pin
MIC1-JD	MIC IN (MIC_RIN/LIN) sense pin

#### 2.4.19 EC Debug connector (JEC\_ROM1)



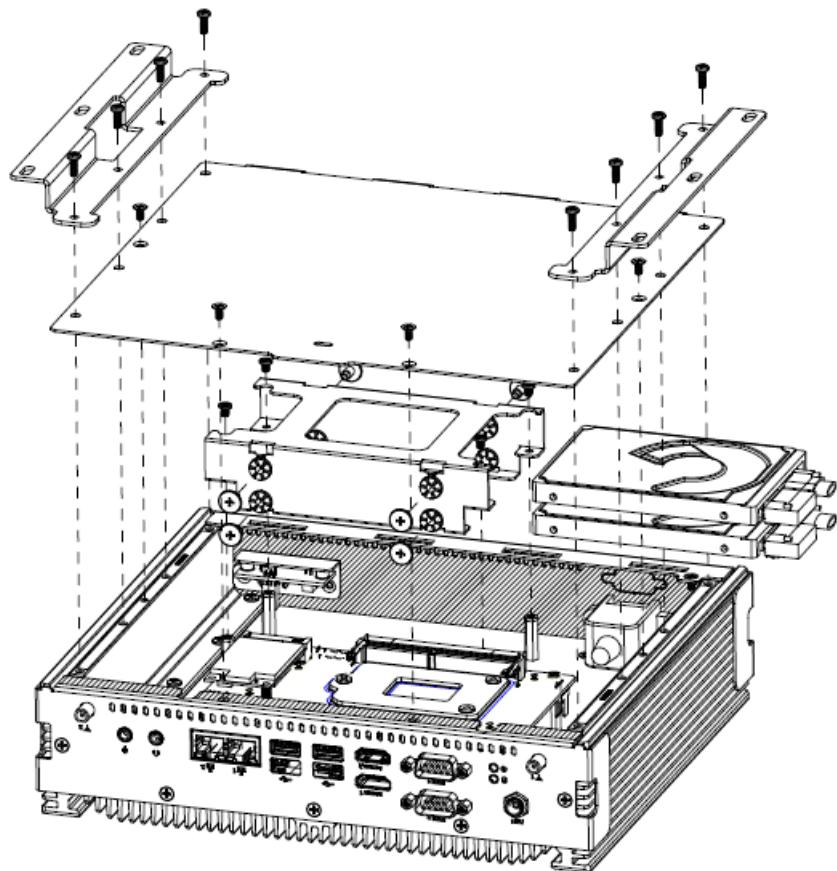
Signal	PIN
EC_SMCLK_DBG	1
EC_SMDAT_DBG	2
GND	3

#### 2.4.20 PC Buzzer header (JBZ1)



Signal	PIN
SPKR	1
+5V	2

## 2.5 Installing Hard Disk (EPS-CFSE)



**Step1.** For HDD installation, please remove 8 screws to detach top cover, HDD enclosure from board & system assembly.

**Step2.** Fix HDD using the 12 screws in the Accessory Kit.

**Step3.** For Wall Mount installation, Insert and fasten 8 screws on each side of the system to secure brackets.

**Step4.** Re-assemble your system back through previous steps to complete the installation.

## 3.BIOS Setup

### **3.1 Introduction**

The BIOS setup program allows users to modify the basic system configuration. In this following chapter will describe how to access the BIOS setup program and the configuration options that may be changed.

### **3.2 Starting Setup**

AMI BIOS™ is immediately activated when you first power on the computer. The BIOS reads the system information contained in the NVRAM and begins the process of checking out the system and configuring it. When it finishes, the BIOS will seek an operating system on one of the disks and then launch and turn control over to the operating system.

While the BIOS is in control, the Setup program can be activated in one of two ways:

By pressing <F2> or <Del> immediately after switching the system on, or

By pressing the <F2> or <Del> key when the following message appears briefly at the left-top of the screen during the POST (Power On Self Test).

**Press <F2> or <Del> to enter SETUP**

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt>, and <Delete> keys.

### 3.3 Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the PageUp and PageDown keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program using the keyboard.

Button	Description
↑	Move to previous item
↓	Move to next item
←	Move to the item in the left hand
→	Move to the item in the right hand
Esc key	Main Menu -- Quit and not save changes into NVRAM Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
+ key	Increase the numeric value or make changes
- key	Decrease the numeric value or make changes
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Previous Values
F3 key	Optimized defaults
F4 key	Save & Exit Setup

- **Navigating Through The Menu Bar**

Use the left and right arrow keys to choose the menu you want to be in.



**Note:** Some of the navigation keys differ from one screen to another.

- **To Display a Sub Menu**

Use the arrow keys to move the cursor to the sub menu you want. Then press <Enter>. A “➤” pointer marks all sub menus.

### **3.4 Getting Help**

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc> or the F1 key again.

### **3.5 In Case of Problems**

If, after making and saving system changes with Setup, you discover that your computer no longer is able to boot, the BIOS supports an override to the NVRAM settings which resets your system to its defaults.

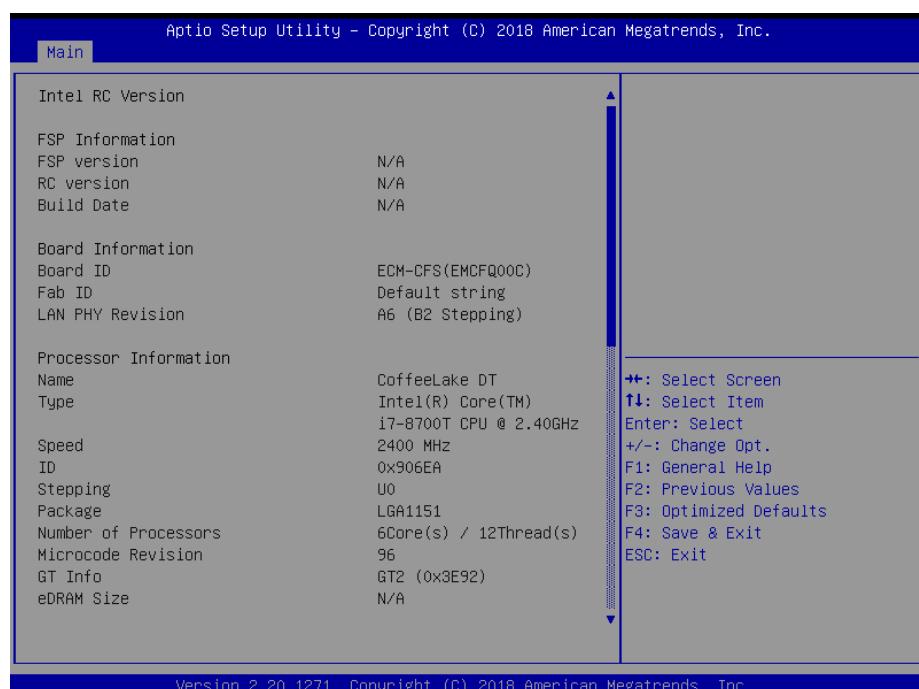
The best advice is to only alter settings which you thoroughly understand. To this end, we strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both your systems manufacturer to provide the absolute maximum performance and reliability. Even a seemingly small change to the chipset setup has the potential for causing you to use the override.

## 3.6 BIOS setup

Once you enter the Aptio Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

### 3.6.1 Main Menu

This section allows you to record some basic hardware configurations in your computer and set the system clock.



### 3.6.1.1 System Language

This option allows choosing the system default language.

### 3.6.1.2 System Date

Use the system date option to set the system date. Manually enter the day, month and year.

### 3.6.1.3 System Time

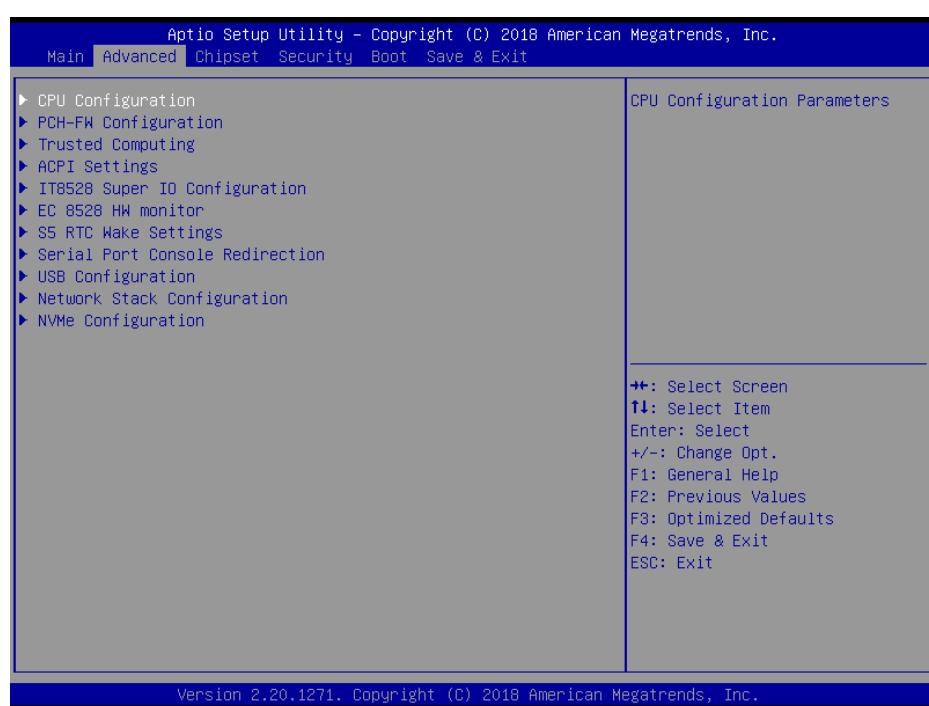
Use the system time option to set the system time. Manually enter the hours, minutes and seconds.



**Note:** The BIOS setup screens shown in this chapter are for reference purposes only, and may not exactly match what you see on your screen.  
Visit the Avalue website ([www.alue.com.tw](http://www.alue.com.tw)) to download the latest product and BIOS information.

## 3.6.2 Advanced Menu

This section allows you to configure your CPU and other system devices for basic operation through the following sub-menus.



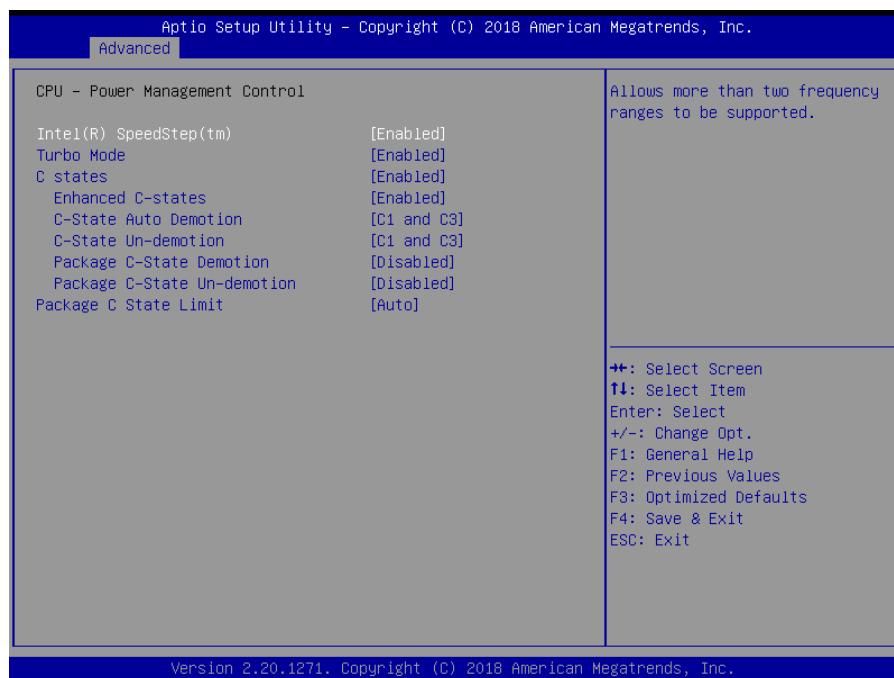
### 3.6.2.1 CPU Configuration

Use the CPU configuration menu to view detailed CPU specification and configure the CPU.



Item	Options	Description
<b>Intel (VMX) Virtualization Technology</b>	Disabled Enabled[Default]	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.
<b>Active Processor Cores</b>	All[Default] 1 2 3 4 5 6 7 8	Number of cores to enable in each processor package.

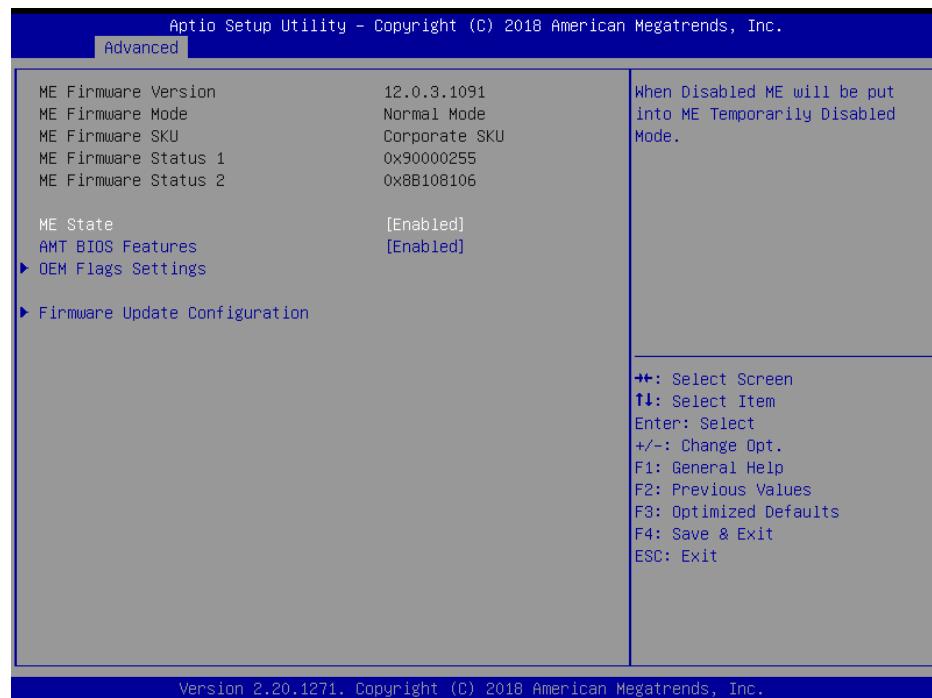
### 3.6.2.1.1 CPU – Power Management Control



Item	Option	Description
<b>Intel® SpeedStep™</b>	Enabled[ <b>Default</b> ], Disabled	Allows more than two frequency ranges to be supported.
<b>Turbo Mode</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable processor Turbo Mode (requires Intel Speed Step or Intel Speed Shift to be available and enabled).
<b>C States</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100% utilized
<b>Enhanced C-states</b>	Enabled[ <b>Default</b> ], Disabled	Enable/Disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-State.
<b>C-State Auto Demotion</b>	Disabled, C1 C3 C1 and C3[ <b>Default</b> ]	Configure C-State Auto Demotion.
<b>C-State Un-demotion</b>	Disabled, C1 C3 C1 and C3[ <b>Default</b> ]	Configure C-State Un-demotion.
<b>Package C-State Demotion</b>	Enabled Disabled[ <b>Default</b> ],	Package C-State Demotion.
<b>Package C-State Un-demotion</b>	Enabled Disabled[ <b>Default</b> ],	Package C-State Un-demotion.
<b>Package C State Limit</b>	C0/C1 C2 C3 C6 C7 C7S	Maximum Package C State Limit Setting. CPU Default: Leaves to Factory default value. Auto: Initializes to deepest available Package C State Limit.

	C8 C9 C10 CPU Default Auto[ <b>Default</b> ]	
--	--	--

### 3.6.2.2 PCH-FW Configuration



Item	Options	Description
<b>ME State</b>	Disabled, Enabled[ <b>Default</b> ]	When Disabled ME will be put into ME Temporarily Disabled Mode.
<b>AMT BIOS Features</b>	Disabled, Enabled[ <b>Default</b> ]	When disable AMT BIOS Features are no longer supported and user is no longer able to access MEBx Setup. Note: This option does not disable Manageability Features in FW.

### 3.6.2.2.1 OEM Flags Settings



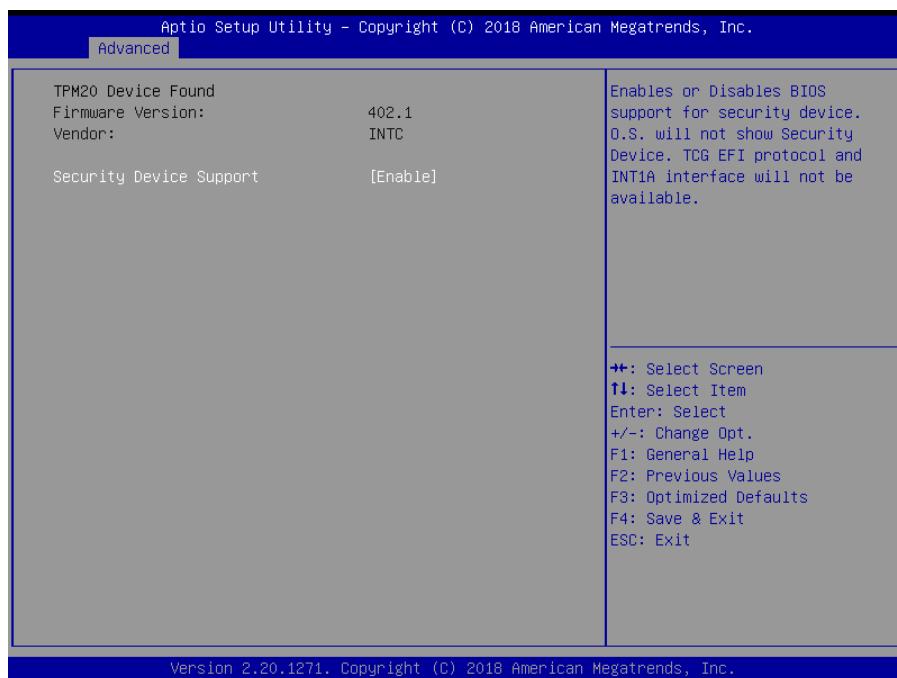
Item	Option	Description
<b>Unconfigure ME</b>	Disabled[ <b>Default</b> ], Enabled	OEMFlag Bit 15: Unconfigure ME with resetting MEBx password to default.

### 3.6.2.2.2 Firmware Update Configuration



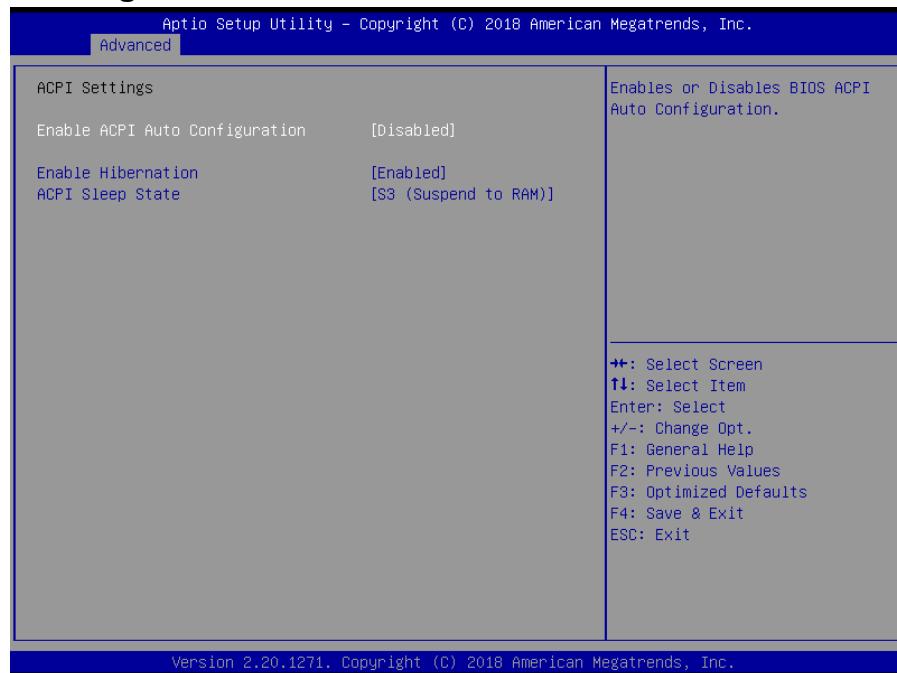
Item	Option	Description
<b>Unconfigure ME</b>	Disabled[ <b>Default</b> ], Enabled	OEMFlag Bit 15: Unconfigure ME with resetting MEBx password to default.

### 3.6.2.3 Trusted Computing



Item	Options	Description
<b>Security Device Support</b>	Disable, Enable <b>[Default]</b>	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

### 3.6.2.4 APCI Settings



Item	Options	Description
<b>Enable ACPI Auto Configuration</b>	Disabled[ <b>Default</b> ], Enabled	Enables or Disables BIOS ACPI Auto Configuration.
<b>Enable Hibernation</b>	Disabled Enabled[ <b>Default</b> ],	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some OS.
<b>ACPI Sleep State</b>	Suspend Disabled, S3 (Suspend to RAM)[ <b>Default</b> ]	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

### 3.6.2.5 IT8528 Super IO Configuration

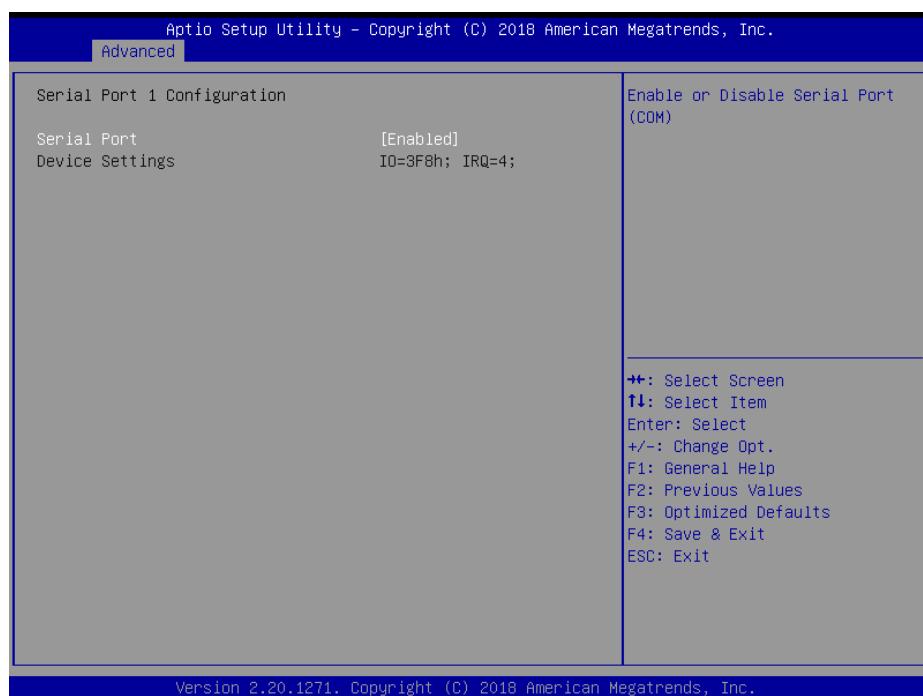
You can use this item to set up or change the IT8528 Super IO configuration for serial ports.

Please refer to 3.6.2.5.1~ 3.6.2.5.2 for more information.



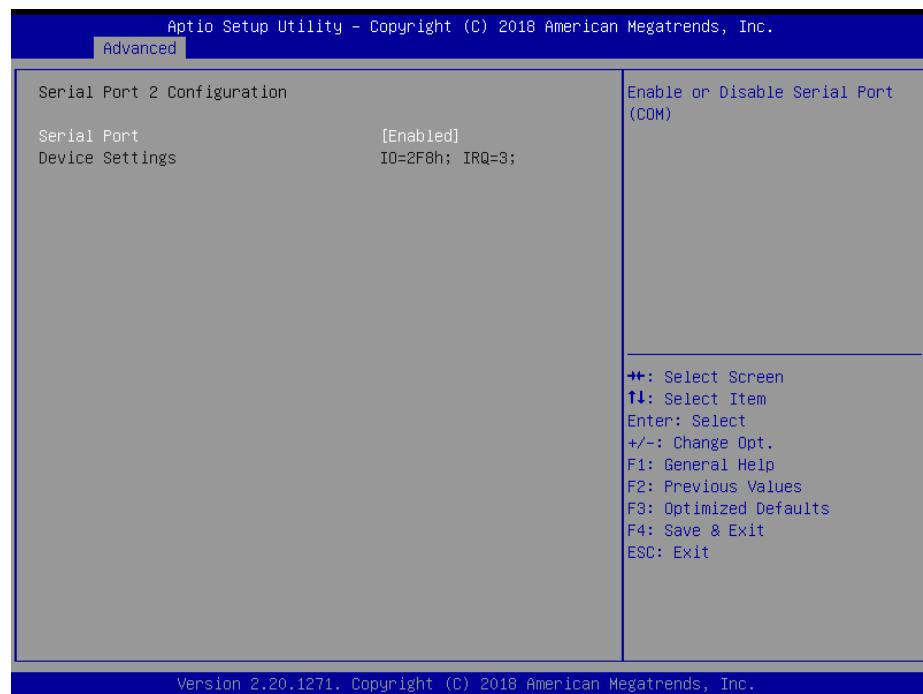
Item	Description
<b>Serial Port 1 Configuration</b>	Set Parameters of Serial Port 1 (COMA).
<b>Serial Port 2 Configuration</b>	Set Parameters of Serial Port 2 (COMB).

### 3.6.2.5.1 Serial Port 1 Configuration



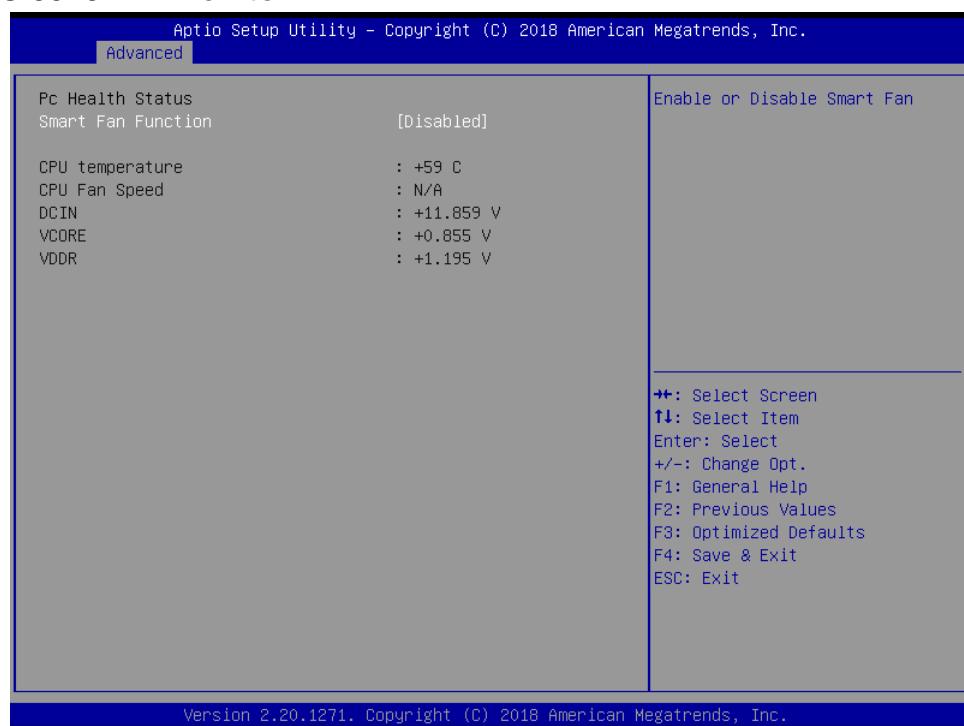
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.5.2 Serial Port 2 Configuration



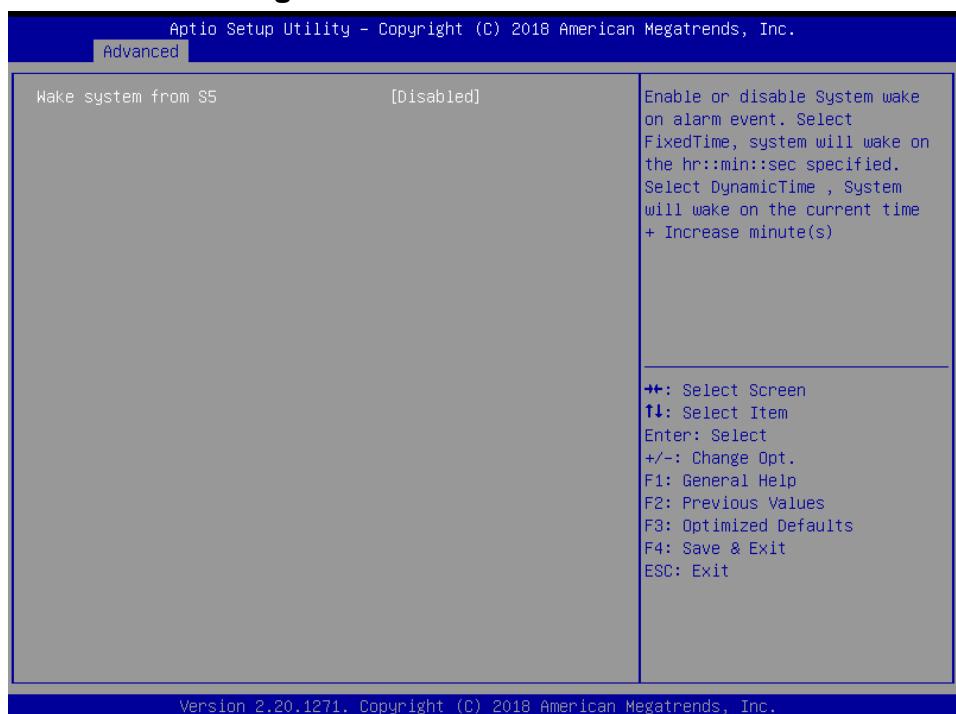
Item	Option	Description
Serial Port	Enabled[Default], Disabled	Enable or Disable Serial Port (COM).

### 3.6.2.6 EC 8528 HW Monitor



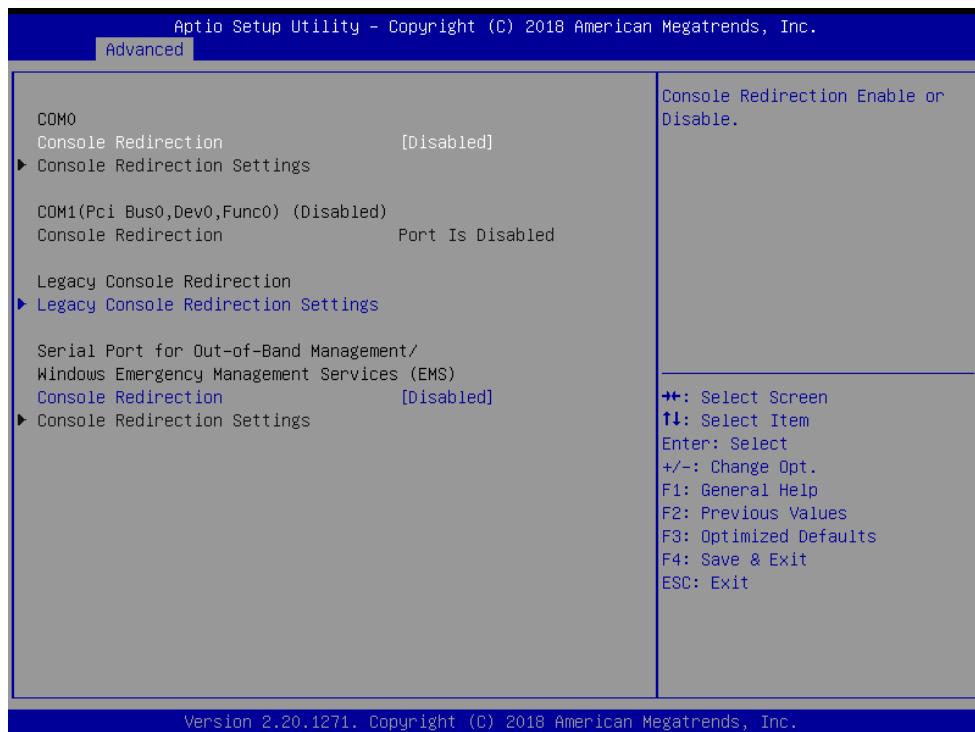
Item	Options	Description
<b>Smart Fan Function</b>	Enabled, Disabled[ <b>Default</b> ]	Enables or Disables Smart Fan.

### 3.6.2.7 S5 RTC Wake Settings



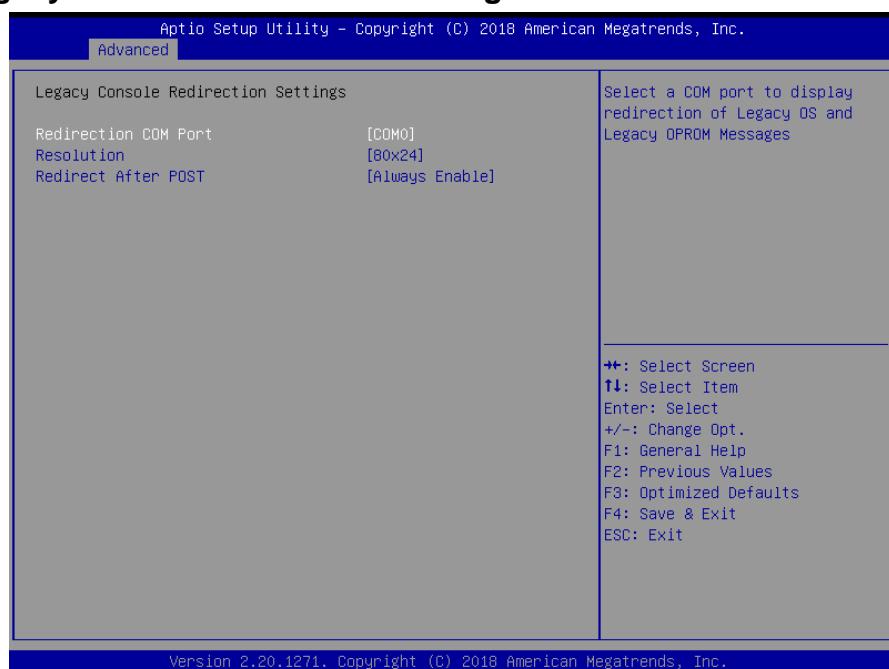
Item	Options	Description
Wake system from S5	Disabled[ <b>Default</b> ], Fixed Time Dynamic Time	Enable or disable System wake on alarm event. Select Fixed Time, system will wake on the hr::min::sec specified. Select Dynamic Time, System will wake on the current time + Increase minute(s).

### 3.6.2.8 Serial Port Console Redirection



Item	Options	Description
Console Redirection	Disabled[ <b>Default</b> ], Enabled	Console Redirection Enable or Disable.

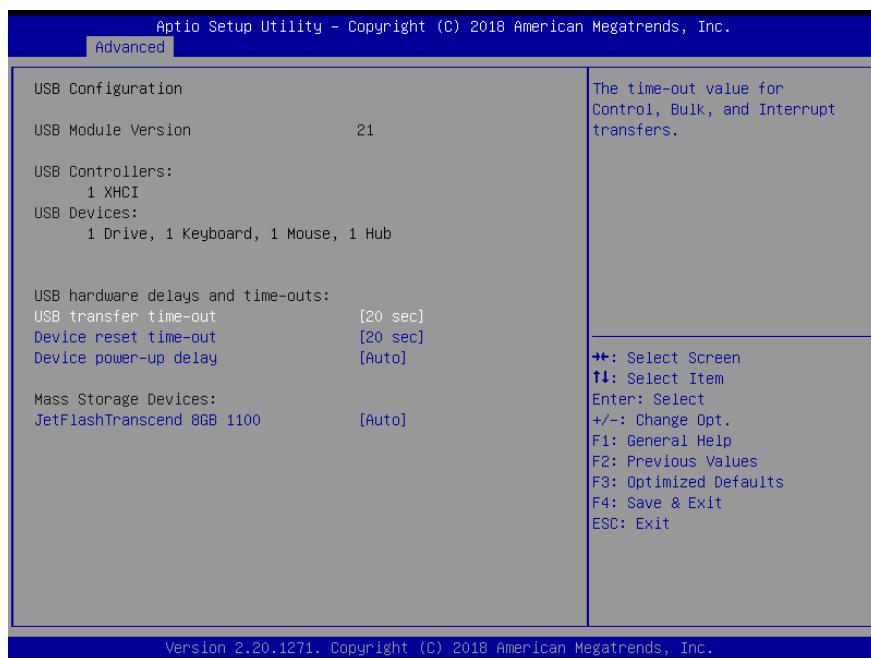
### 3.6.2.8.1 Legacy Console Redirection Settings



Item	Option	Description
<b>Redirection COM Port</b>	COM0[ <b>Default</b> ]	Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages.
<b>Resolution</b>	80x24[ <b>Default</b> ] 80x25	On Legacy OS, the Number of Rows and Columns supported redirection.
<b>Redirect After POST</b>	Always Enable[ <b>Default</b> ] BootLoader	When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable.

### 3.6.2.9 USB Configuration

The USB Configuration menu helps read USB information and configures USB settings.



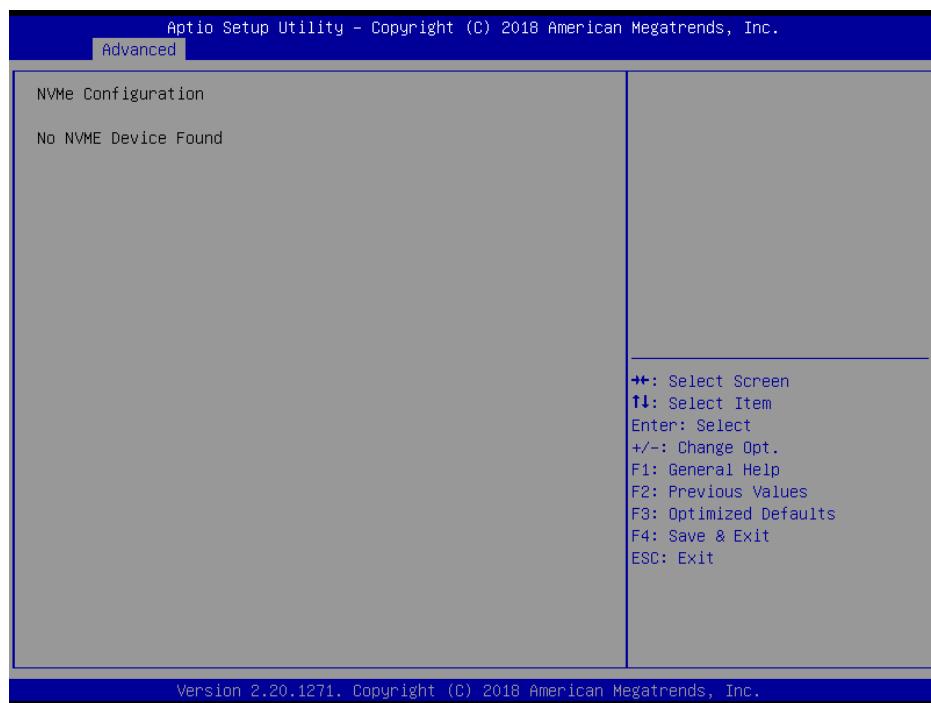
Item	Options	Description
<b>USB transfer time-out</b>	1 sec 5 sec 10 sec <b>20 sec[Default]</b>	The time-out value for Control, Bulk, and Interrupt transfers.
<b>Device reset time-out</b>	10 sec <b>20 sec[Default]</b> 30 sec 40 sec	USB mass storage device Start Unit command time-out.
<b>Device power-up delay</b>	Auto <b>[Default]</b> Manual	Maximum time the device will take before it properly reports itself to the Host Controller. 'Auto' uses default value: for a Root port it is 100ms, for a Hub port the delay is taken from Hub descriptor.
<b>Mass Storage Devices</b>	Auto <b>[Default]</b> Floppy Forced FDD Hard Disk CD-ROM	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type.

### 3.6.2.10 Network Stack Configuration

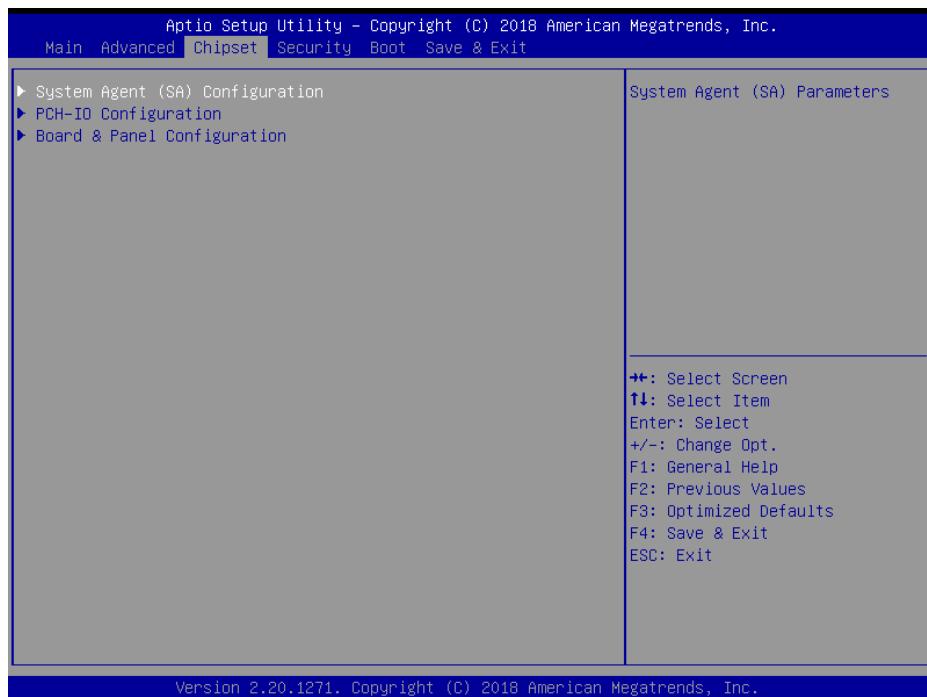


Item	Options	Description
<b>Network Stack</b>	Enabled Disabled <b>[Default]</b>	Enable/Disable UEFI Network Stack.

### 3.6.2.11 NVMe Configuration



### 3.6.3 Chipset

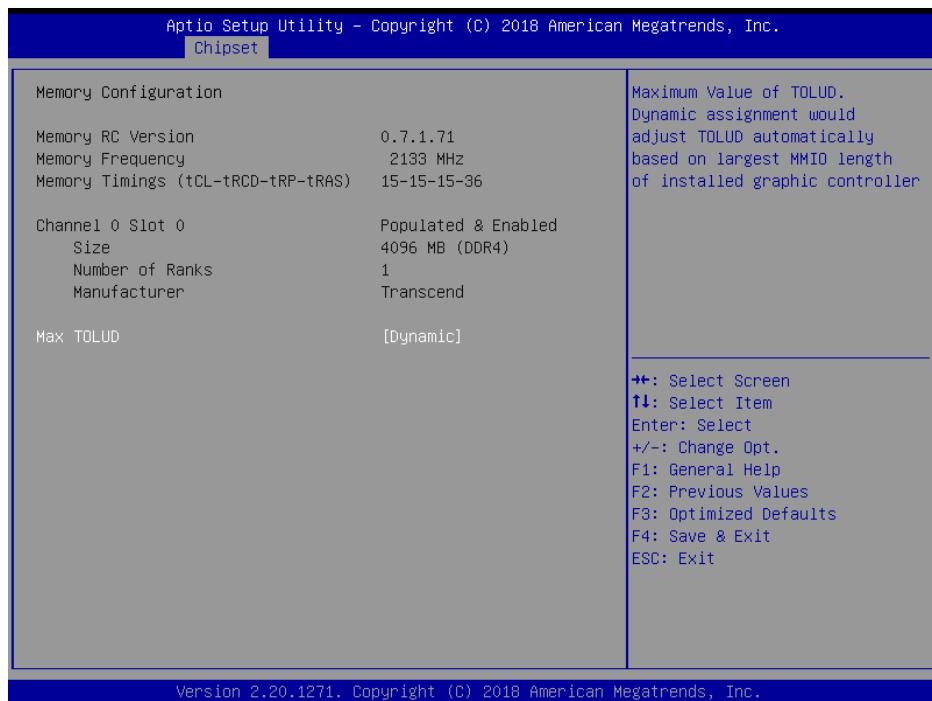


#### 3.6.3.1 System Agent (SA) Configuration



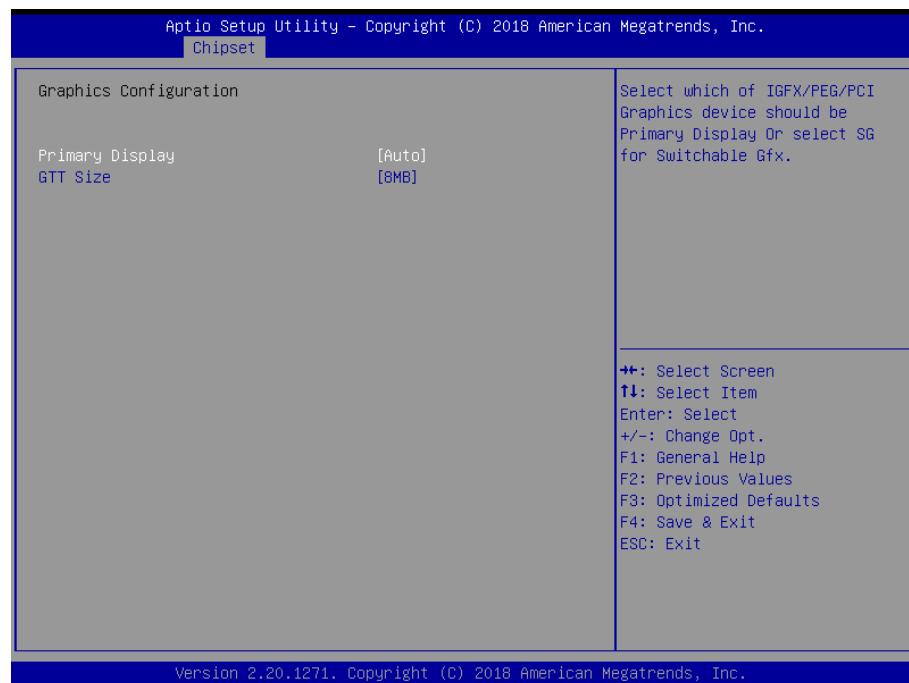
Item	Option	Description
VT-d	Enabled[Default] Disabled	VT-d capability.

### 3.6.3.1.1 Memory Configuration



Item	Option	Description
<b>Max TOLUD</b>	Dynamic <b>[Default]</b> 1 GB 1.25 GB 1.5 GB 1.75 GB 2 GB 2.25 GB 2.5 GB 2.75 GB 3 GB	Maximum Value of TOLUD. Dynamic assignment would adjust TOLUD automatically based on largest MMIO length of installed graphic controller.

### 3.6.3.1.2 Graphics Configuration

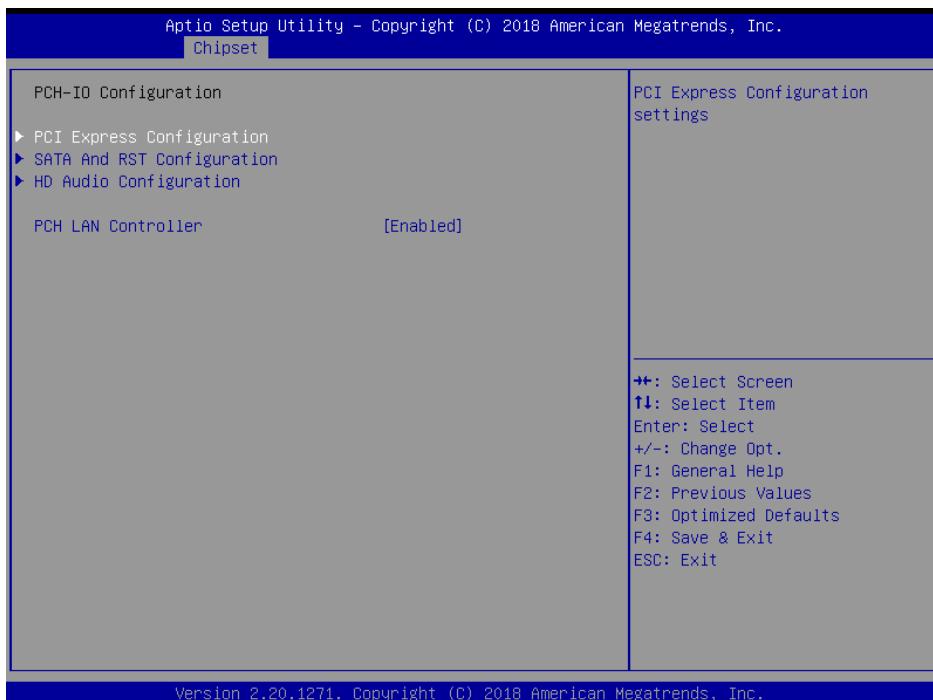


Item	Option	Description
<b>Primary Display</b>	Auto <b>[Default]</b> IGFX	Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select SG for Switchable Gfx.
<b>GTT Size</b>	2MB 4MB <b>8MB[Default]</b>	Select the GTT Size.

### 3.6.3.1.3 DMI/OPI Configuration

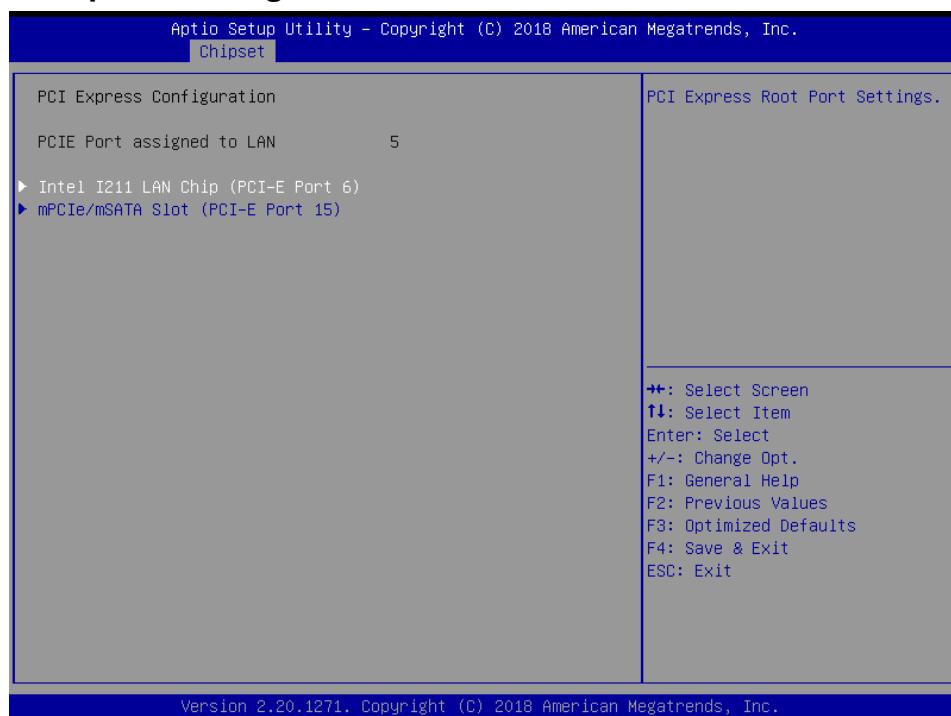


### 3.6.3.2 PCH-IO Configuration

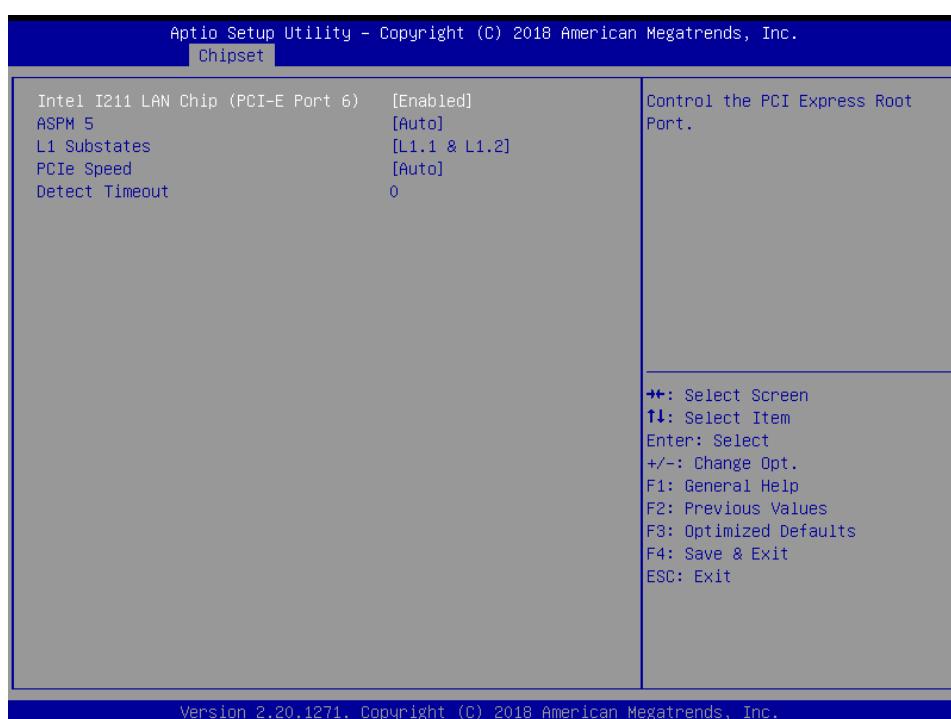


Item	Option	Description
PCH LAN Controller	Disabled Enabled[Default]	Enable/Disable onboard NIC.

### 3.6.3.2.1 PCI Express Configuration



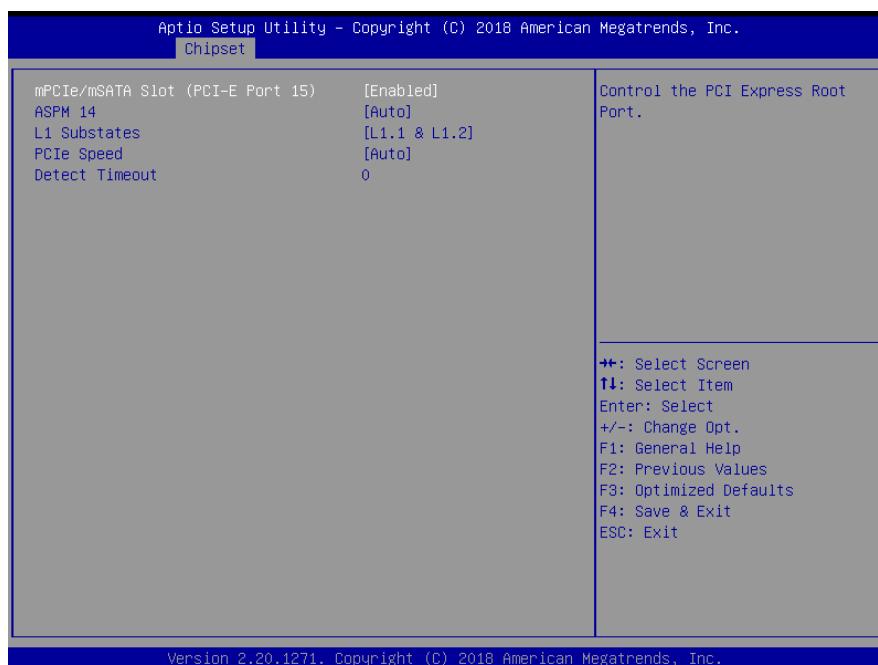
#### 3.6.3.2.1.1 Intel I211 LAN Chip (PCI-E Port 6)



Item	Option	Description
<b>Intel I211 LAN Chip (PCI-E Port 6)</b>	Enabled[ <b>Default</b> ], Disabled	Control the PCI Express Root Port.
<b>ASPM 5</b>	Disabled, L0s	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto

	L1 L0sL1 Auto[ <b>Default</b> ]	configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled, L1.1 L1.1 & L1.2[ <b>Default</b> ]	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto[ <b>Default</b> ] Gen1 Gen2 Gen3	Configure PCIe Speed.
<b>Detect Timeout</b>	0	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.

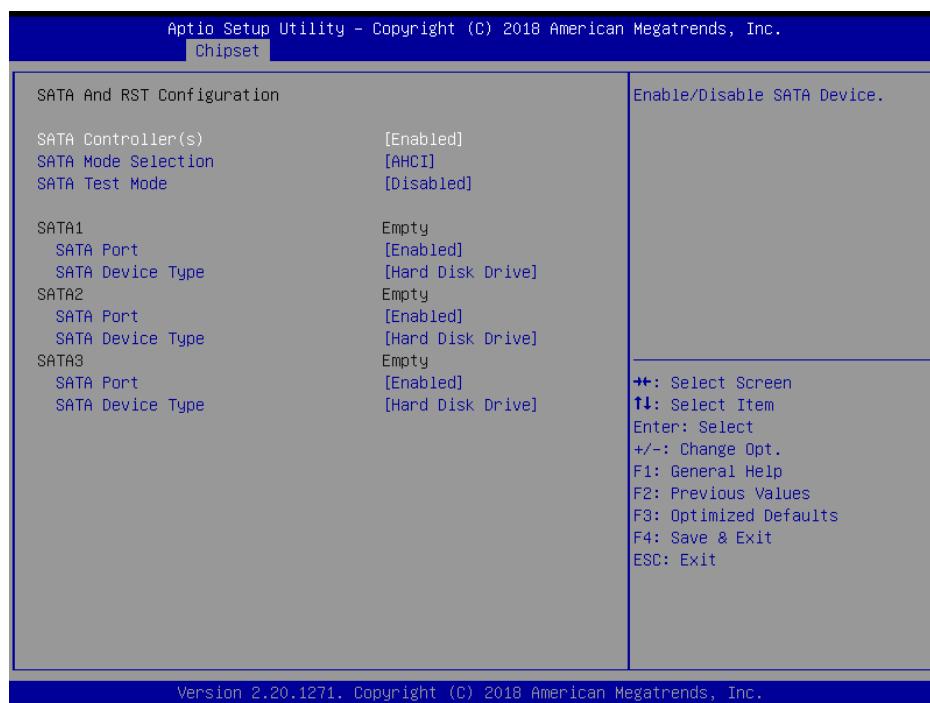
### 3.6.3.2.1.2 mPCIe/mSATA Slot (PEI-E Port 15)



Item	Option	Description
<b>mPCIe/mSATA Slot (PEI-E Port 15)</b>	Enabled[ <b>Default</b> ], Disabled	Control the PCI Express Root Port.
<b>ASPM 14</b>	Disabled, L0s L1 L0sL1 Auto[ <b>Default</b> ]	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO – BIOS auto configure DISABLE – Disables ASPM.
<b>L1 Substates</b>	Disabled, L1.1 L1.1 & L1.2[ <b>Default</b> ]	PCI Express L1 Substates settings.
<b>PCIe Speed</b>	Auto[ <b>Default</b> ] Gen1 Gen2	Configure PCIe Speed.

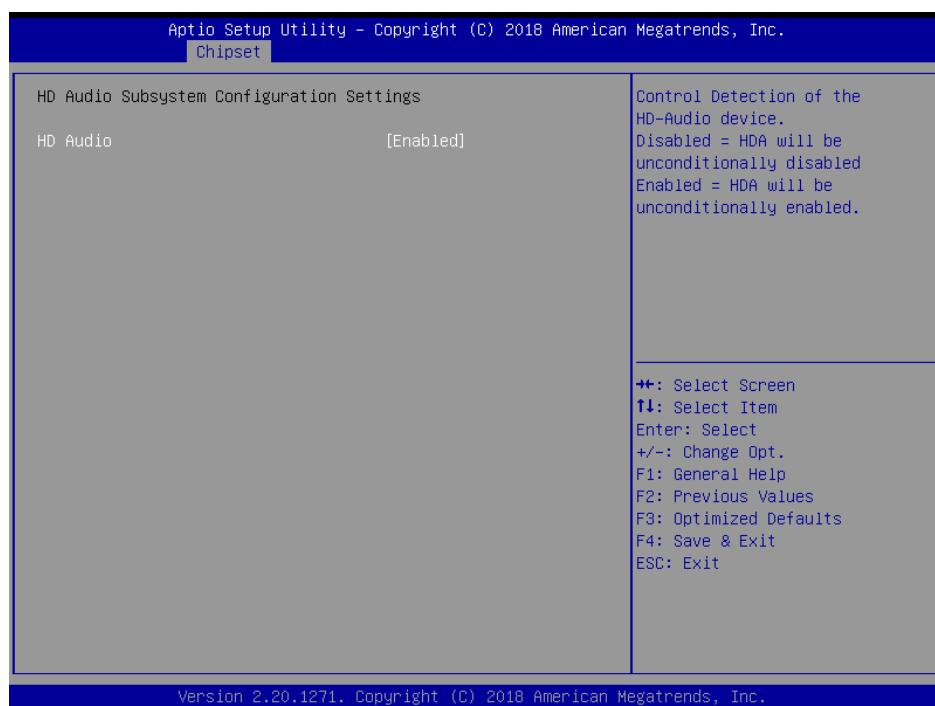
	Gen3	
<b>Detect Timeout</b>	0	The number of milliseconds reference code will wait for link to exit Detect state for enabled ports before assuming there is no device and potentially disabling the port.

### 3.6.3.2.2 SATA And RST Configuration



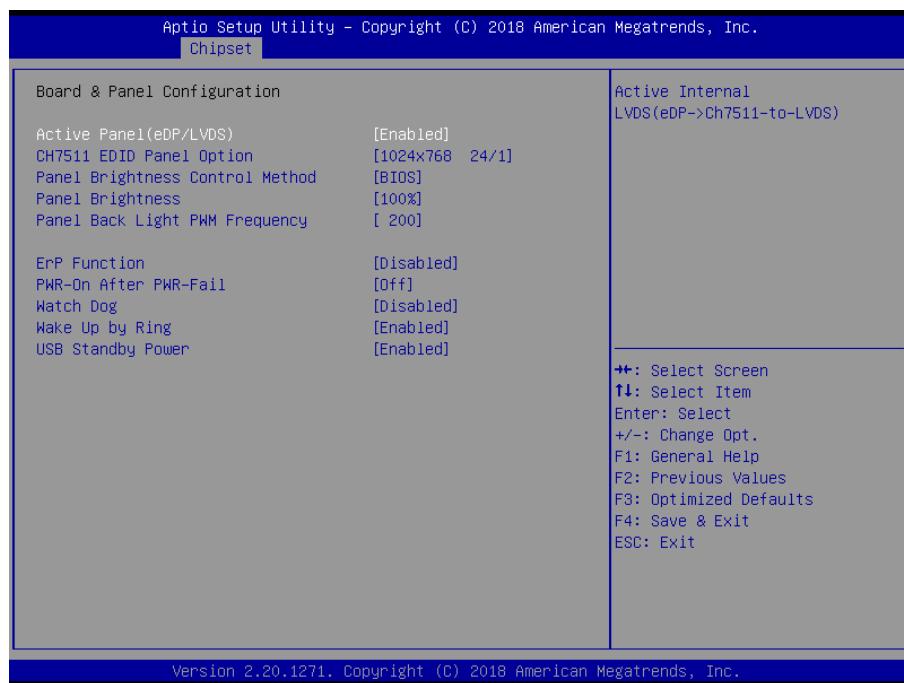
Item	Options	Description
<b>SATA Controller(s)</b>	Enabled[ <b>Default</b> ] Disabled,	Enable/Disable SATA Device.
<b>SATA Mode Selection</b>	AHCI[ <b>Default</b> ], RAID	Determines how SATA controller(s) operate.
<b>SATA Test Mode</b>	Enabled Disabled[ <b>Default</b> ]	The Mode Enable/Disable (Loop Back).
<b>SATA Port</b>	Enabled[ <b>Default</b> ] Disabled	Enable or Disable SATA Port.
<b>SATA Device Type</b>	Hard Disk Drive[ <b>Default</b> ] Solid State Drive	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive.

### 3.6.3.2.3 HD Audio Configuration



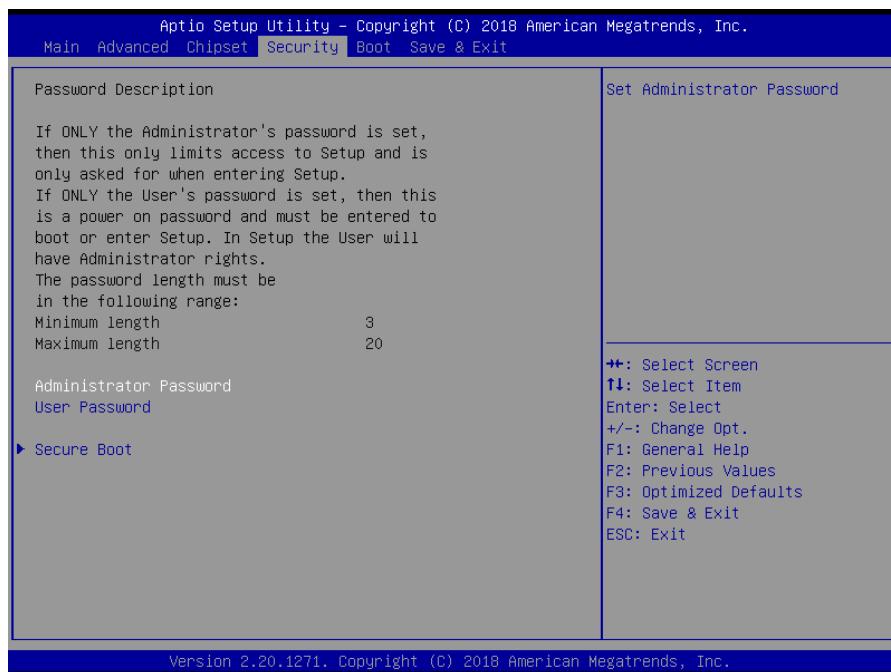
Item	Option	Description
HD Audio	Disabled Enabled[Default]	Control Detection of the HD-Audio device. Disable = HDA will be unconditionally disabled Enabled = HDA will be unconditionally enabled

### 3.6.3.3 Board & Panel Configuration



Item	Option	Description
<b>Active Panel (eDP/LVDS)</b>	Disabled Enabled <b>[Default]</b>	Active Internal LVDS(eDP->Cg7511-to-LVDS).
<b>CH7511 EDID Panel Option</b>	1024x768 24/1 <b>[Default]</b> 800x600 18/1 1024x768 18/1 1366x768 18/1 1024x600 18/1 1280x800 18/1 1920x1200 24/2 1920x1080 18/2 1280x1024 24/2 1366x768 24/1 1920x1080 24/2 1680x1050 24/2	Port-EDP to LVDS(Chrotel 7511) Panel EDID Option.
<b>Panel Brightness Control Method</b>	BIOS <b>[Default]</b> OS Driver	Panel Brightness Control Method. 1.BIOS 2.OS Driver.
<b>Panel Brightness</b>	00% 25% 50% 75% <b>100%[Default]</b>	Select Panel(eDP/LVDS) back light PWM duty.
<b>Panel Back Light PWM Frequency</b>	200 <b>[Default]</b> 300 400 500 700 1k 2k 3k 5k 10k 20k	Select Panel(eDP/LVDS) back light PWM Frequency.
<b>ErP Function</b>	Disabled <b>[Default]</b> Enabled	ErP Function (Deep S5).
<b>PWR-On After PWR-Fail</b>	Off <b>[Default]</b> On Last state	AC loss resume.
<b>Watch Dog</b>	Disabled <b>[Default]</b> 30 sec 40 sec 50 sec 1 min 2 min 10 min 30 min	Select WatchDog.
<b>Wake Up by Ring</b>	Disabled Enabled <b>[Default]</b>	Wake Up by Ring from S3/S4/S5.
<b>USB Standby Power</b>	Disabled Enabled <b>[Default]</b>	Enable/Disabled USB Standby Power during S3/S4/S5.

### 3.6.4 Security



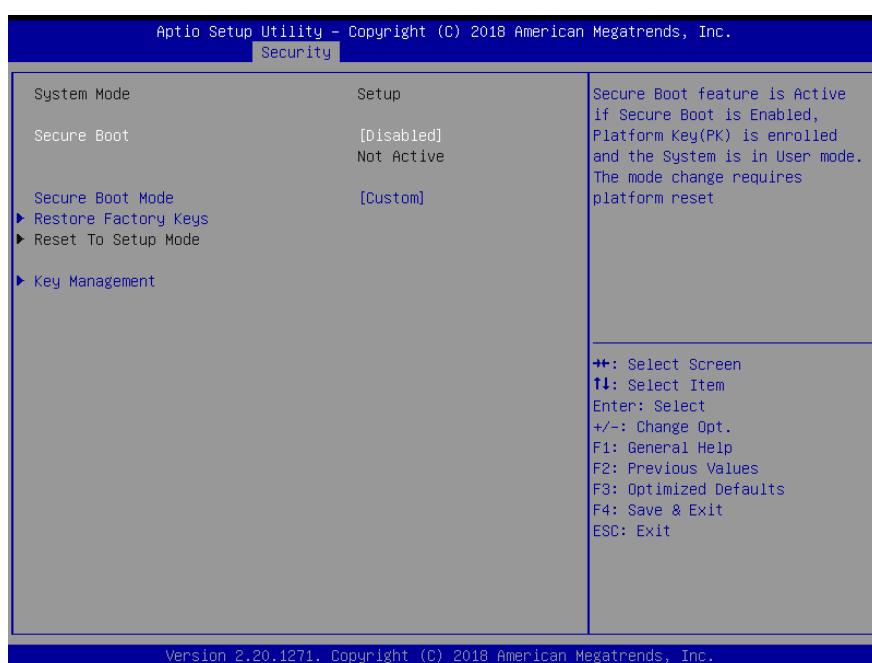
#### ● Administrator Password

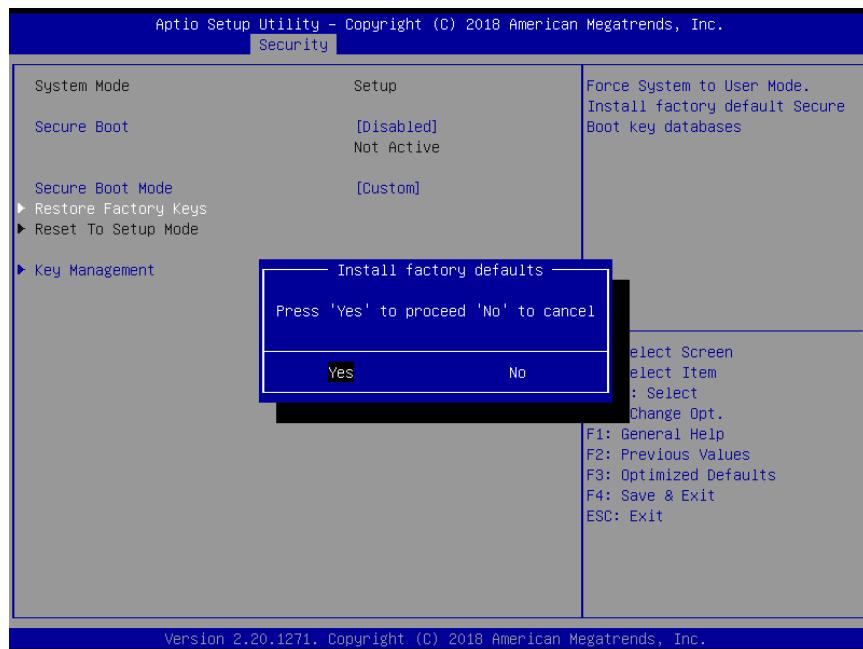
Set setup Administrator Password

#### ● User Password

Set User Password

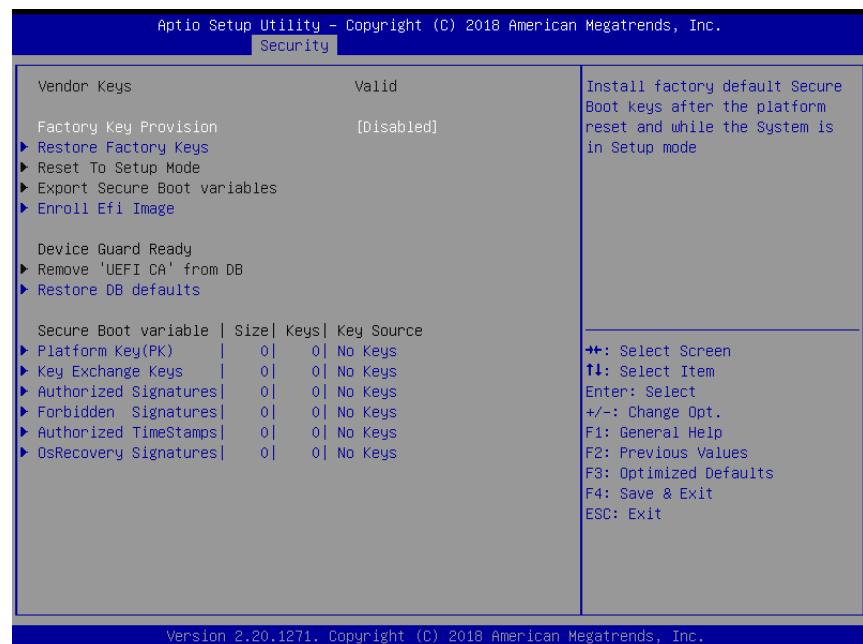
#### 3.6.4.1 Secure Boot



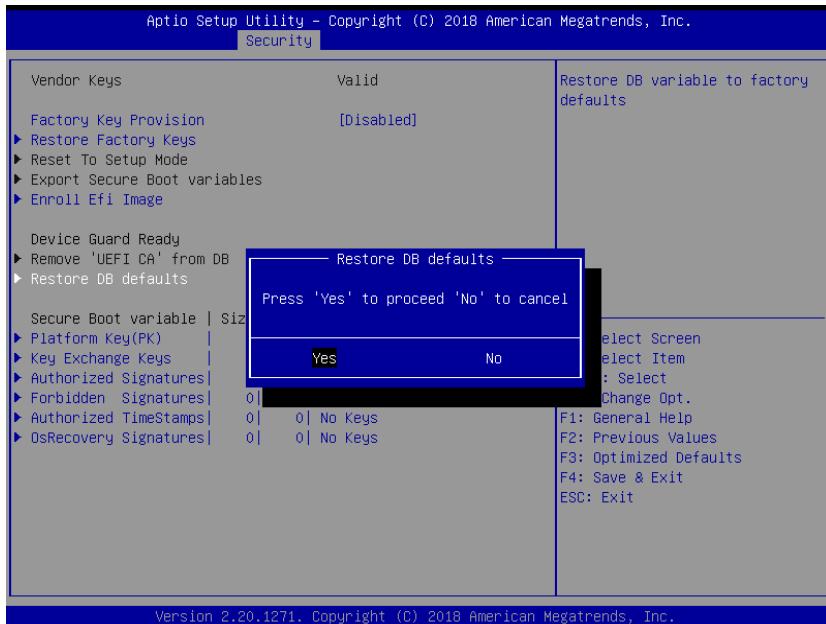
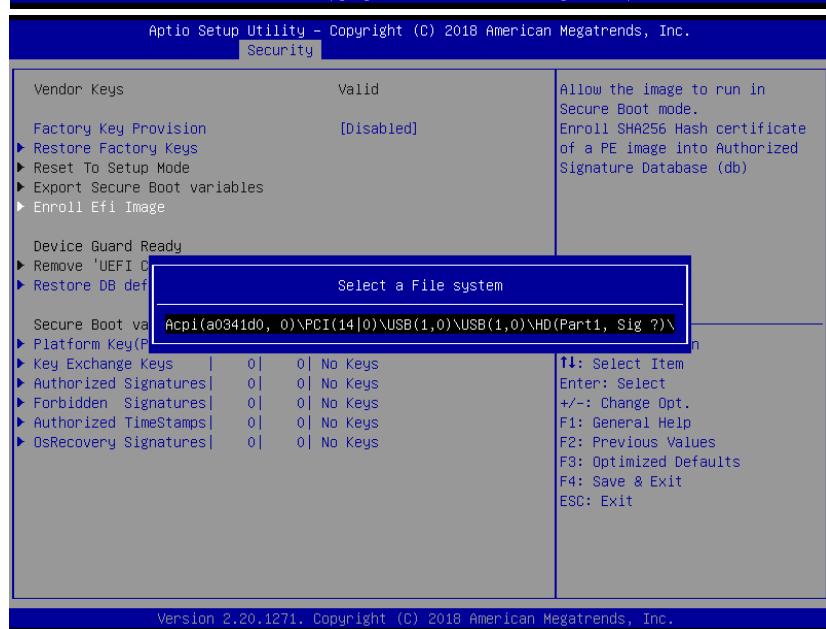
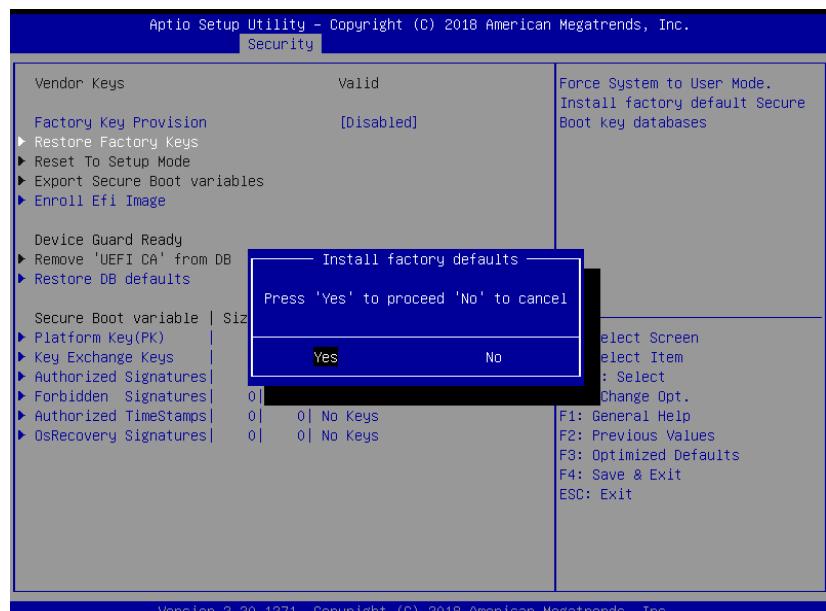


Item	Option	Description
Secure Boot	Disabled[Default] Enabled	Secure Boot feature is Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset.
Secure Boot Mode	Standard Custom[Default]	Secure Boot mode selector: Standard/Custom. In Custom mode Secure Boot Variables can be configured without authentication.

### 3.6.4.1.1 Key Management



## Quick Reference Guide



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**Security**

Vendor Keys	Valid
Factory Key Provision	[Disabled]
► Restore Factory Keys	
► Reset To Setup Mode	
► Export Secure Boot variables	
► Enroll Efi Image	
Device Guard Ready	
► Remove 'UEFI CA' from DB	
► Restore DB defaults	
Secure Boot variable   Size  Ke	
► Platform Key(PK)	0  No Keys
► Key Exchange Keys	0  No Keys
► Authorized Signatures	0  No Keys
► Forbidden Signatures	0  No Keys
► Authorized TimeStamps	0  No Keys
► OsRecovery Signatures	0  No Keys

**Platform Key(PK)**

**Key Exchange Keys**

**Authorized Signatures**

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:
  - a)EFI\_SIGNATURE\_LIST
  - b)EFI\_CERT\_X509 (DER)
  - c)EFI\_CERT\_RSA2048 (bin)
  - d)EFI\_CERT\_SHAXXX
2. Authenticated UEFI Variable
3. EFI PE/COFF Image(SHA256)

Key Source:  
Factory,External,Mixed

\*\*: Select Screen  
†\*: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F3: Optimized Defaults  
F4: Save & Exit  
ESC: Exit

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**Security**

Vendor Keys	Valid
Factory Key Provision	[Disabled]
► Restore Factory Keys	
► Reset To Setup Mode	
► Export Secure Boot variables	
► Enroll Efi Image	
Device Guard Ready	
► Remove 'UEFI CA' from DB	
► Restore DB defaults	
Secure Boot variable   Size  Ke	
► Platform Key(PK)	0  No Keys
► Key Exchange Keys	0  No Keys
► Authorized Signatures	0  No Keys
► Forbidden Signatures	0  No Keys
► Authorized TimeStamps	0  No Keys
► OsRecovery Signatures	0  No Keys

**Key Exchange Keys**

**Authorized Signatures**

Enroll Factory Defaults or load certificates from a file:

1. Public Key Certificate:
  - a)EFI\_SIGNATURE\_LIST
  - b)EFI\_CERT\_X509 (DER)
  - c)EFI\_CERT\_RSA2048 (bin)
  - d)EFI\_CERT\_SHAXXX
2. Authenticated UEFI Variable
3. EFI PE/COFF Image(SHA256)

Key Source:  
Factory,External,Mixed

\*\*: Select Screen  
†\*: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F3: Optimized Defaults  
F4: Save & Exit  
ESC: Exit

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Aptio Setup Utility - Copyright (C) 2018 American Megatrends, Inc.

**Security**

Vendor Keys	Valid
Factory Key Provision	[Disabled]
► Restore Factory Keys	
► Reset To Setup Mode	
► Export Secure Boot variables	
► Enroll Efi Image	
Device Guard Ready	
► Remove 'UEFI CA' from DB	
► Restore DB defaults	
Secure Boot variable   Size  Ke	
► Platform Key(PK)	0  No Keys
► Key Exchange Keys	0  No Keys
► Authorized Signatures	0  No Keys
► Forbidden Signatures	0  No Keys
► Authorized TimeStamps	0  No Keys
► OsRecovery Signatures	0  No Keys

**Authorized Signatures**

Enroll Factory Defaults or load certificates from a file:

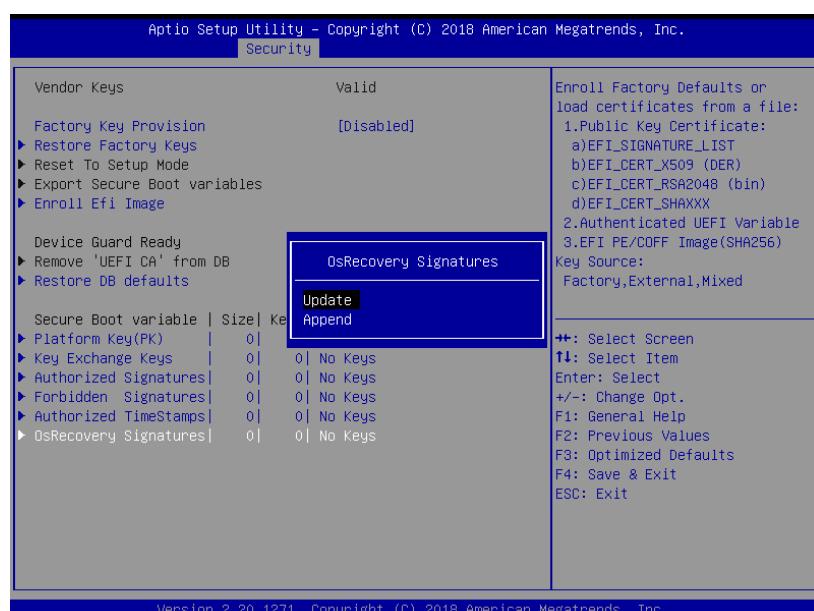
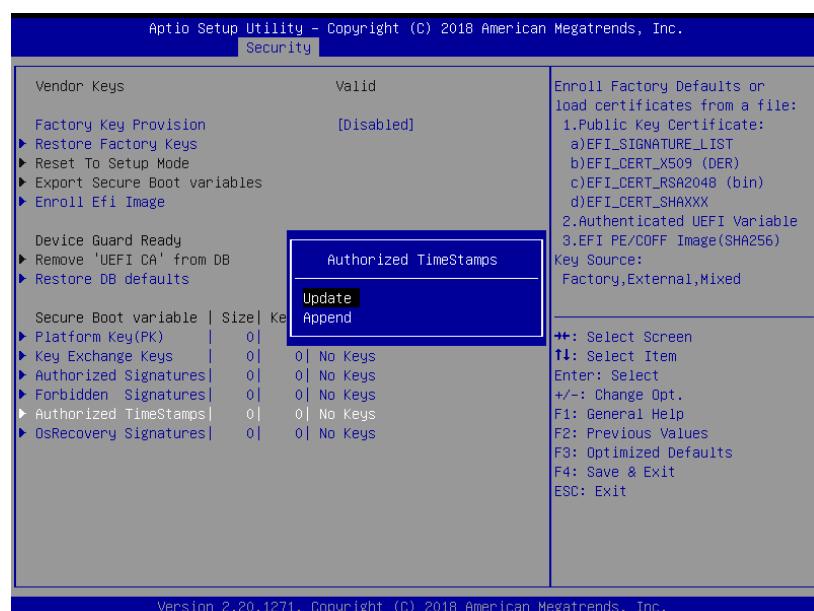
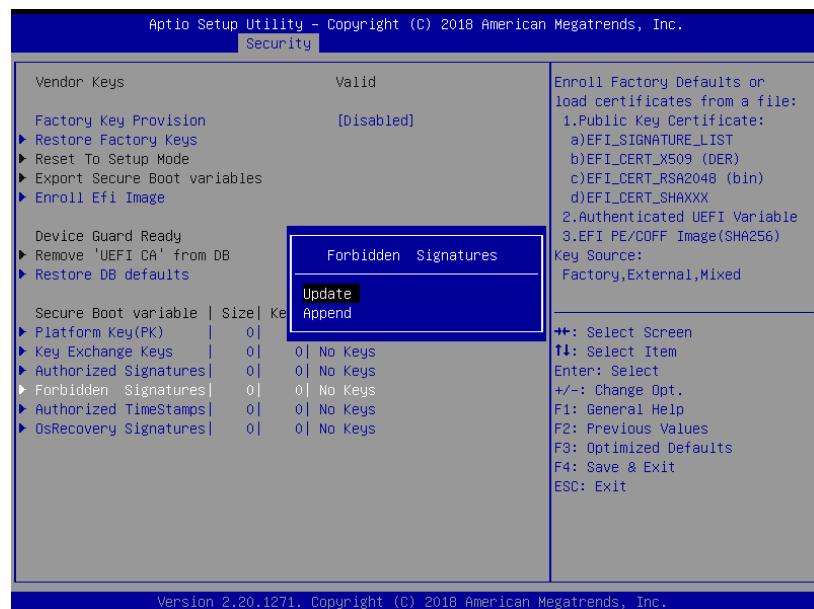
1. Public Key Certificate:
  - a)EFI\_SIGNATURE\_LIST
  - b)EFI\_CERT\_X509 (DER)
  - c)EFI\_CERT\_RSA2048 (bin)
  - d)EFI\_CERT\_SHAXXX
2. Authenticated UEFI Variable
3. EFI PE/COFF Image(SHA256)

Key Source:  
Factory,External,Mixed

\*\*: Select Screen  
†\*: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F3: Optimized Defaults  
F4: Save & Exit  
ESC: Exit

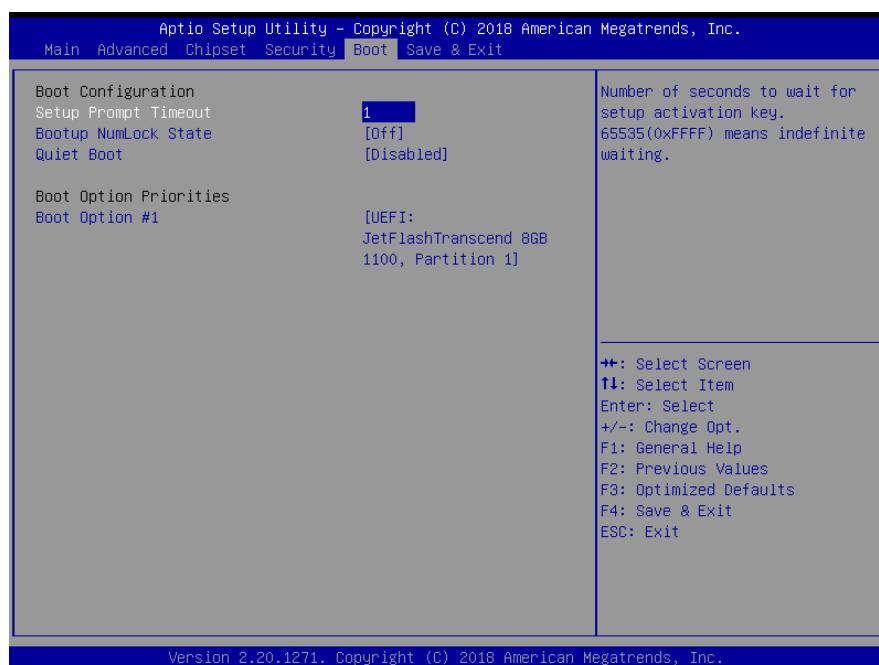
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## Quick Reference Guide



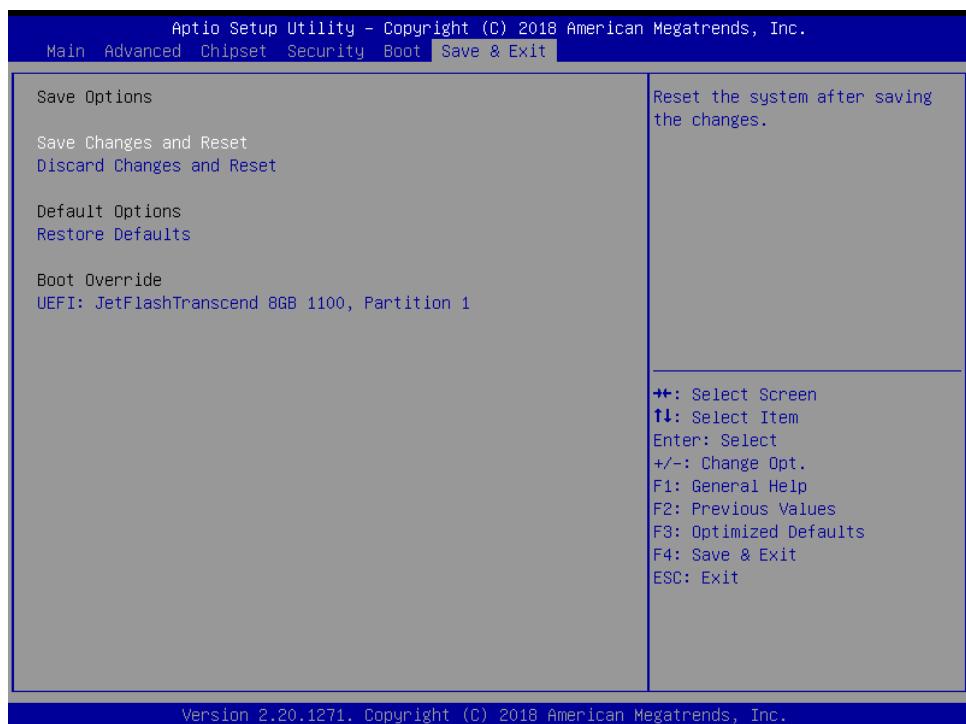
Item	Option	Description
<b>Factory Key Provision</b>	Disabled[ <b>Default</b> ] Enabled	Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.

### 3.6.5 Boot



Item	Option	Description
<b>Setup Prompt Timeout</b>	1~ 65535	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
<b>Bootup NumLock State</b>	On Off[ <b>Default</b> ]	Select the Keyboard NumLock state
<b>Quiet Boot</b>	Disabled[ <b>Default</b> ] Enabled	Enables or disables Quiet Boot option
<b>Boot Option #1</b>	Set the system boot order.	

### 3.6.6 Save and exit



#### 3.6.6.1 Save Changes and Reset

Reset the system after saving the changes.

#### 3.6.6.2 Discard Changes and Reset

Any changes made to BIOS settings during this session of the BIOS setup program are discarded. The setup program then exits and reboots the controller.

#### 3.6.6.3 Restore Defaults

This option restores all BIOS settings to the factory default. This option is useful if the controller exhibits unpredictable behavior due to an incorrect or inappropriate BIOS setting.

#### 3.6.6.4 Launch EFI Shell from filesystem device

Attempts to Launch EFI Shell application (Shellx64.efi) from one of the available filesystem devices.

## 4. Drivers Installation



**Note:** Installation procedures and screen shots in this section are for your reference and may not be exactly the same as shown on your screen.

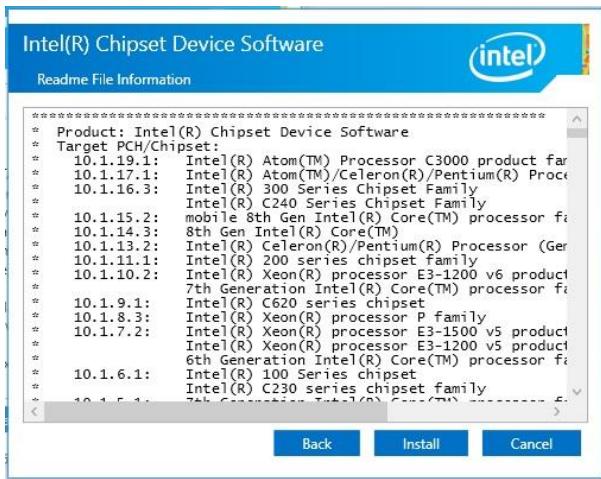
## 4.1 Install Chipset Driver

All drivers can be found on the Avalue Official Website:

<http://www.avalue.com.tw>.



**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



### Step1. Click Next.



### Step 2. Click Accept.

### Step 3. Click Install.



### Step 4. Click Finish to complete setup.

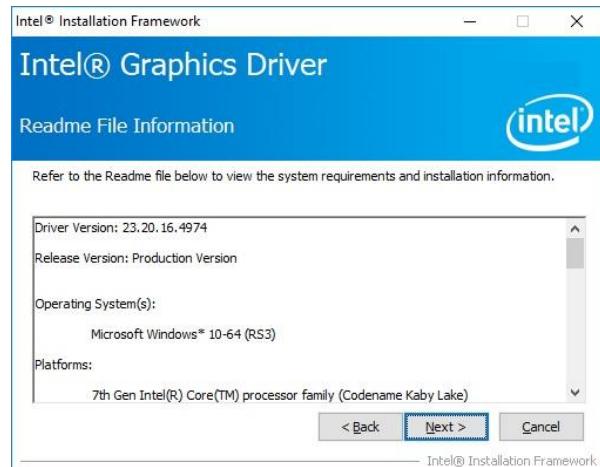
## 4.2 Install VGA Driver

All drivers can be found on the Avalue Official Website:

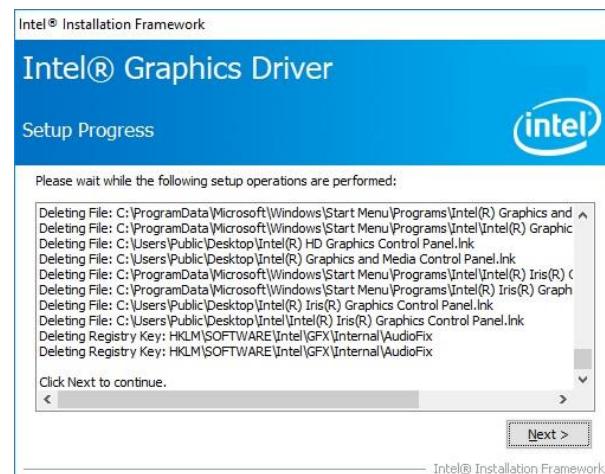
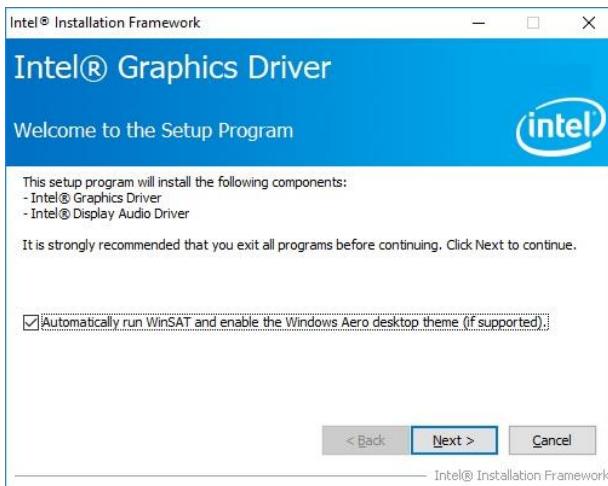
<http://www.avalue.com.tw>.



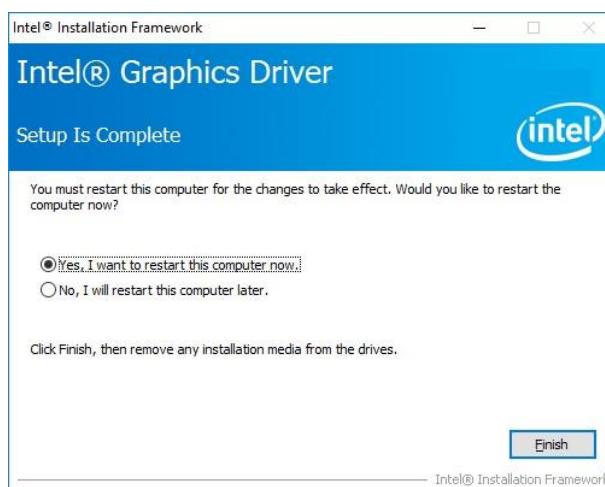
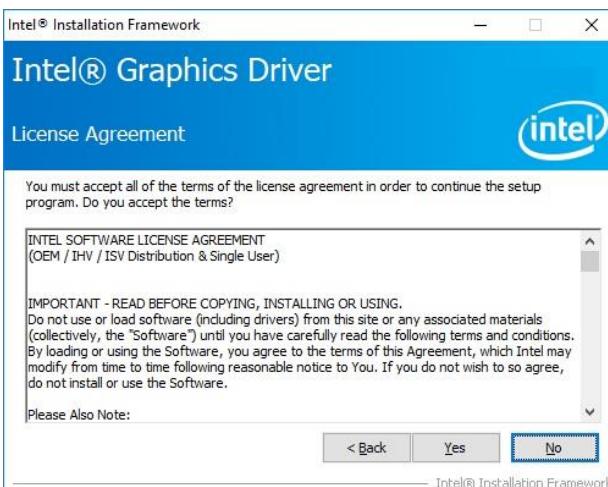
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



### Step 3. Click Next.



### Step 1. Click Next to continue installation.



### Step 2.

Click **Yes** to accept license agreement.

### Step 5. Click Finish to complete setup.

## 4.3 Install Audio Driver (For Realtek ALC888S HD Audio)

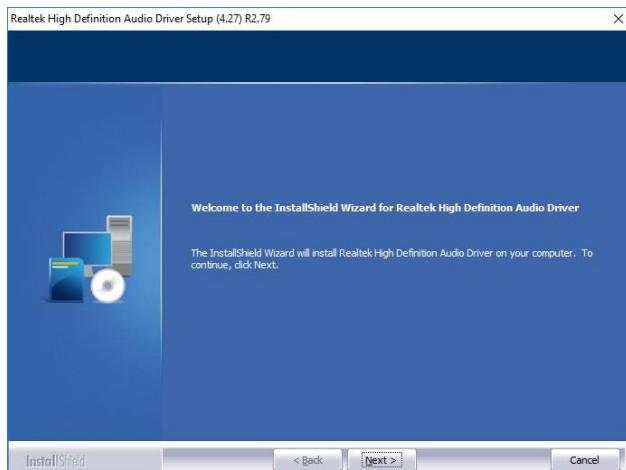
All drivers can be found on the Avalue

Official Website:

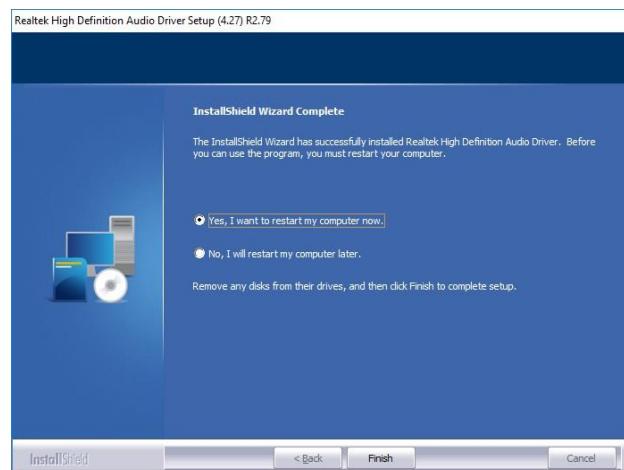
<http://www.alue.com.tw>.



**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system. If the warning message appears while the installation process, click Continue to go on.



**Step1.** Click **Next** to Install.



**Step 2.** Select **Finish** to complete Installation.

## 4.4 Install LAN Driver

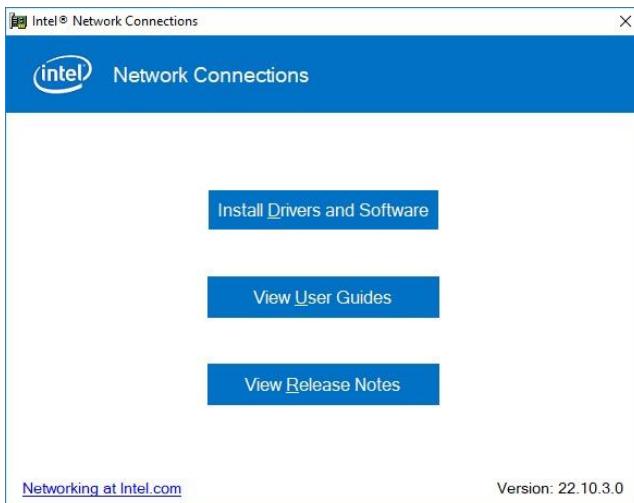
All drivers can be found on the Avalue Official Website:  
[http://www.alue.com.tw.](http://www.alue.com.tw)



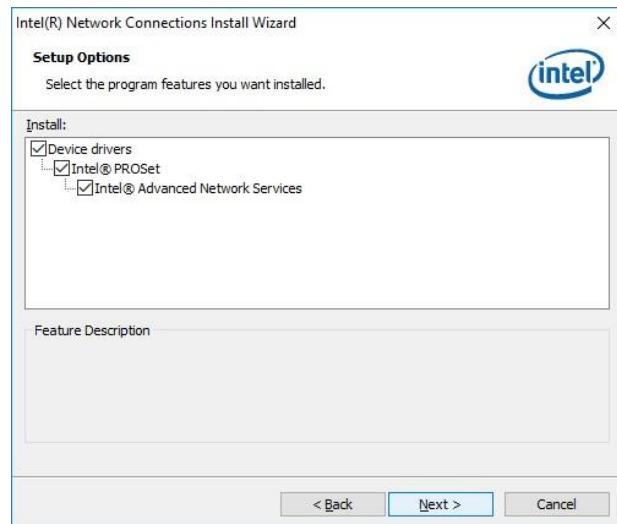
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



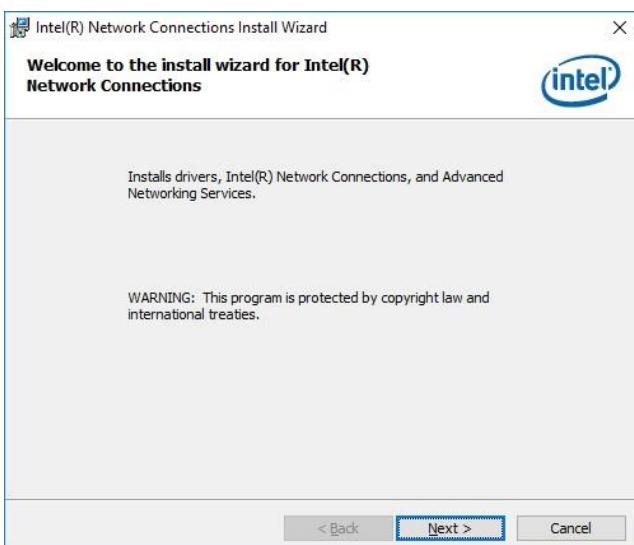
### Step 3. Click Next.



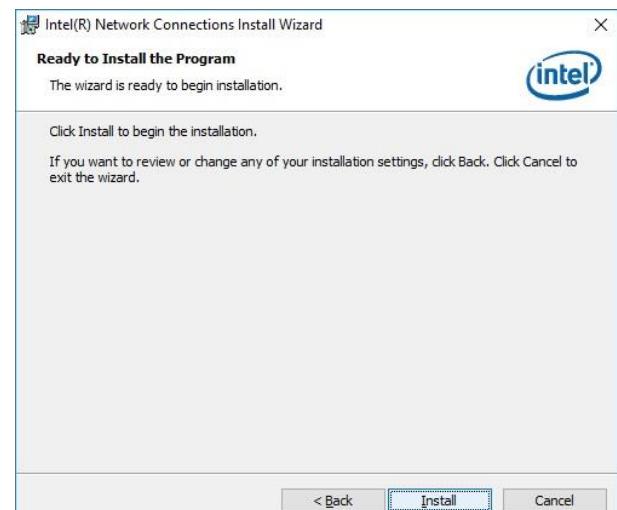
**Step 1. Click Install Drivers and Software** to continue installation.



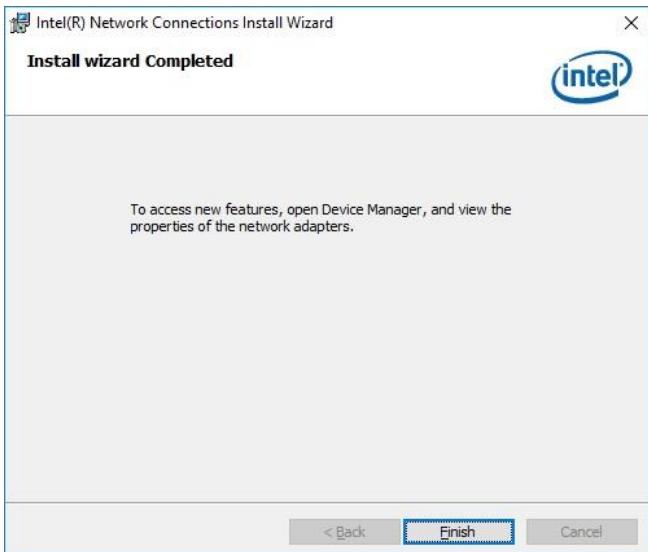
### Step 4. Click Next.



**Step 2. Click Next.**



### Step 5. Click Install.



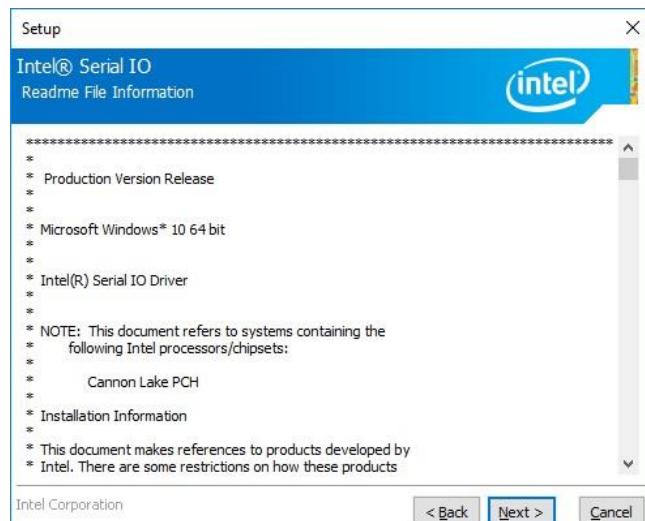
**Step 6.** Click **Finish** to complete setup.

## 4.5 Install Serial IO Driver

All drivers can be found on the Avalue Official Website:  
<http://www.alue.com.tw>.



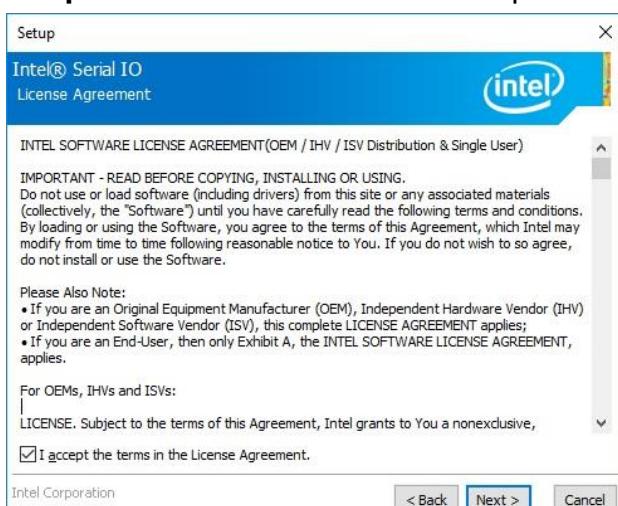
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



### Step 3. Click Next.



### Step 1. Click Next to continue setup.



### Step 2. Click Next.



### Step 4. Click Next.



### Step 5. Click Finish to complete the setup.

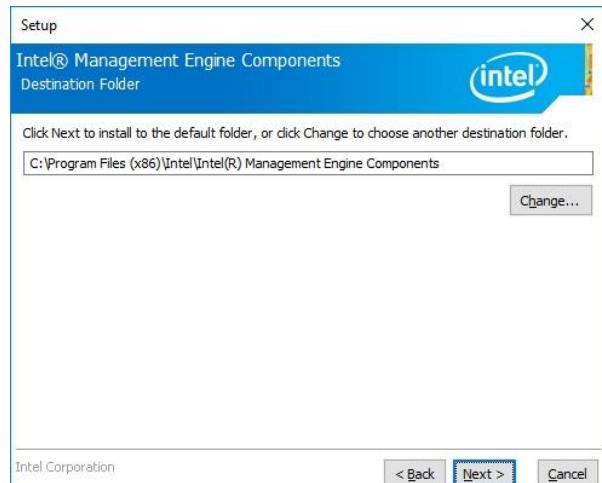
## 4.6 Install ME Driver

All drivers can be found on the Avalue Official Website:

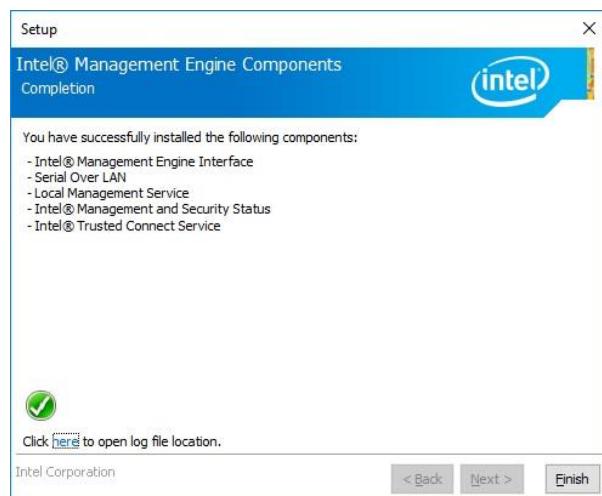
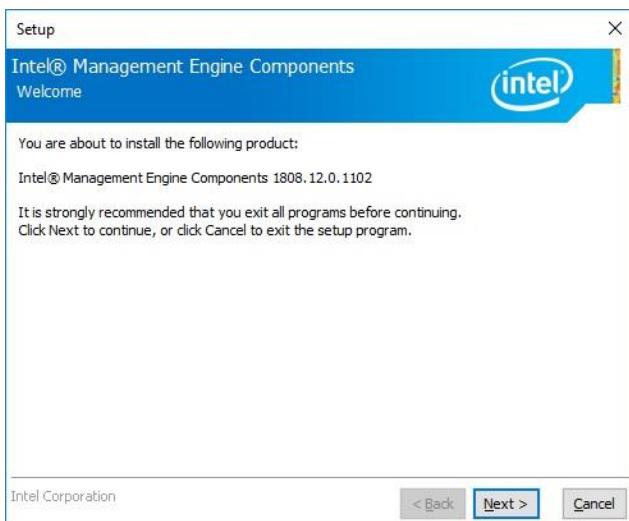
<http://www.alue.com.tw>.



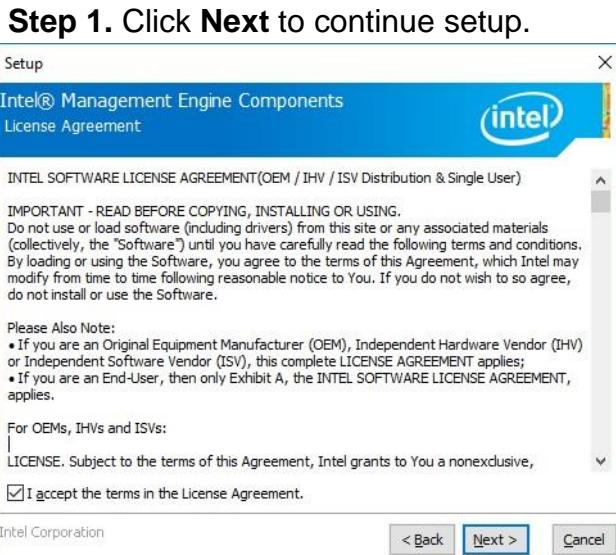
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



### Step 3. Click Next



### Step 4. Click Finish to complete the setup



### Step 2. Click Next.

## 4.7 Install IRST Driver

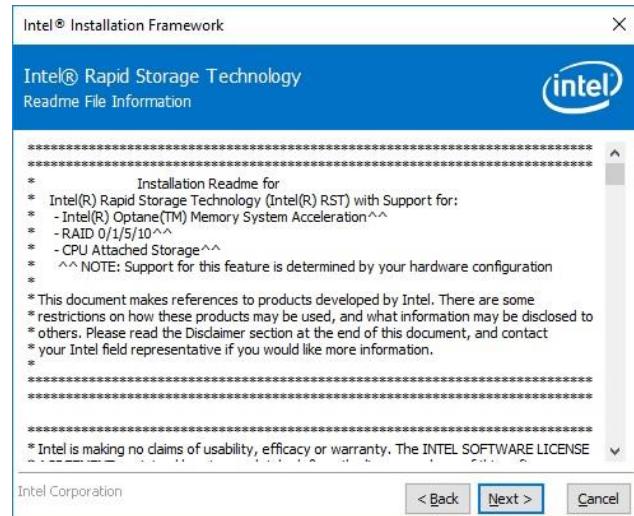
All drivers can be found on the Avalue

Official Website:

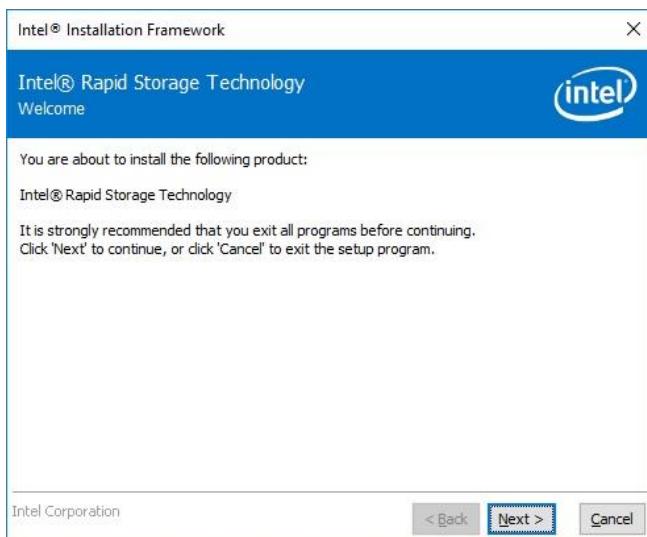
<http://www.avalue.com.tw>.



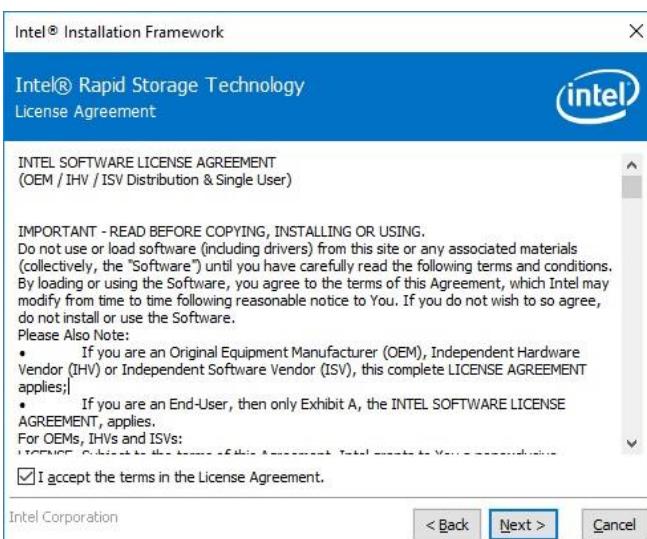
**Note:** The installation procedures and screen shots in this section are based on Windows 10 operation system.



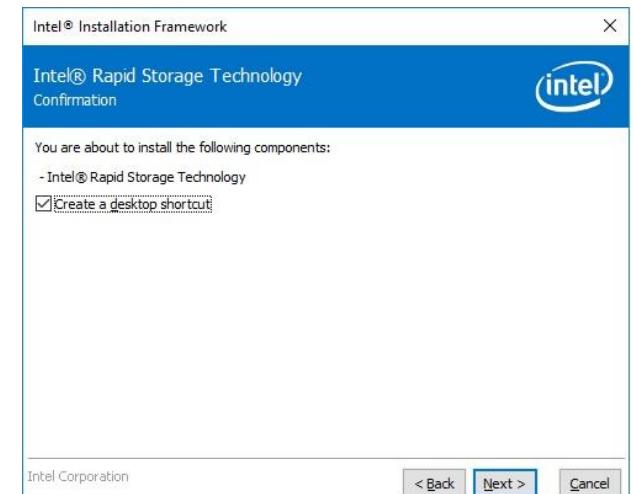
### Step 3. Click Next.



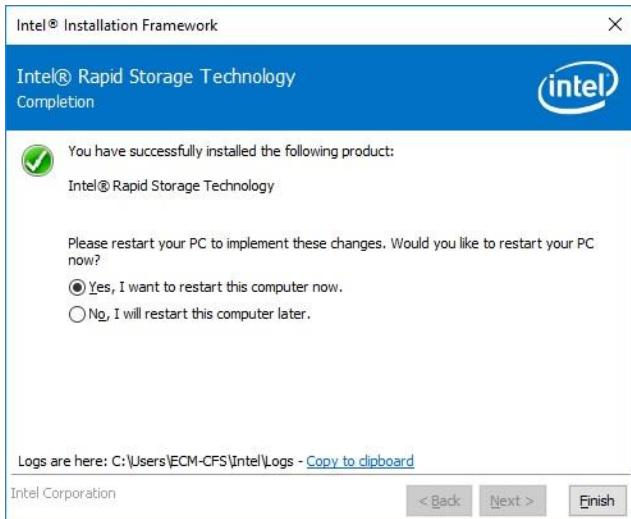
### Step 1. Click Next to continue installation.



### Step 2. Click Next.



### Step 5. Click Next.



**Step 6.** Click Finish to complete setup.

